

KVR333/512R

512MB 64M x 64-Bit DDR333

CL2.5 184-Pin DIMM

DESCRIPTION:

This document describes ValueRAM's 64M x 64-bit (512MB) DDR333 CL2.5 SDRAM (Synchronous DRAM) memory module. The components on this module include sixteen 32M x 8-bit (8M x 8-bit x 4 Bank) DDR333 SDRAM in TSOP packages. This 184-pin DIMM uses gold contact fingers and requires +2.5V. The electrical and mechanical specifications are as follows:

FEATURES:

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|---------------------------------|-------------------------------|
| ✓ Clock Cycle Time (tCK) | 6ns (min.) / 12ns (max.) |
| ✓ Row Cycle Time (tRC) | 60ns (min.) |
| ✓ Refresh Row Cycle Time (tRFC) | 72ns (min.) |
| ✓ Row Active Time (tRAS) | 42ns (min.) / 70,000ns (max.) |
| ✓ Single Power Supply of | +2.5V (+/- .2V) |
| ✓ Power (IDD0) | 2.900 W (operating) |
| ✓ UL Rating | 94 V - 0 |

Note: The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 1.250" (31.75mm).

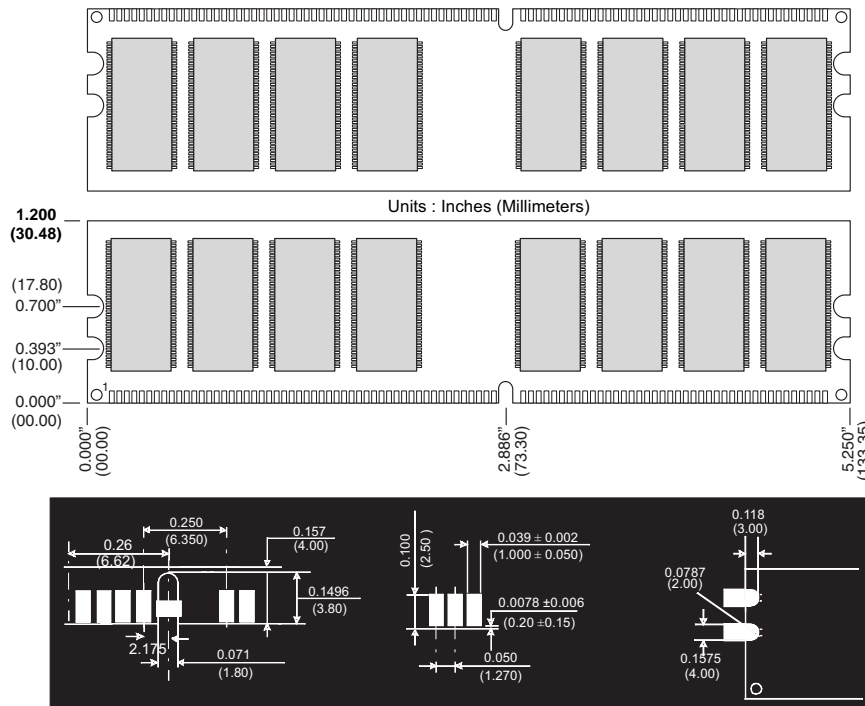


Figure 1: KVR333/512R 184-Pin DIMM