# KVR100X72RC2/512
## *PC100 Registered SDRAM 168-Pin*
## *CL2 ECC DIMM Memory Module*

### DESCRIPTION:

ValueRam's KVR100X72RC2/512 is a 64M x 72-bit (512MB) PC100 CL2 (CAS Latency 2) ECC Registered SDRAM (Synchronous DRAM) memory module. The components on this module include eighteen 64M x 4-bit (16M x 4-Bit x 4 bank) SDRAM CL2 PC100 components in TSOP packages. This 168-pin DIMM uses gold contact fingers and requires +3.3V. The electrical and mechanical specifications are as follows:

### FEATURES:

|   |   |   |
|---|---|---|
| ☑ | CLK Cycle Time (tCK) | min. = 10ns (CAS Latency = 2) / max. = 1000ns |
| ☑ | Row Cycle Time (tRC) | 70ns |
| ☑ | CLK to Output (tAC) | max. = 6ns (CAS Latency = 2) |
| ☑ | Single Power Supply of | +3.3V (+/- .3V) |
| ☑ | Power | 8.995 W (operating) |
| ☑ | UL Rating | 94 V - 0 |

**Note:** The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 1.700" (43.18mm).
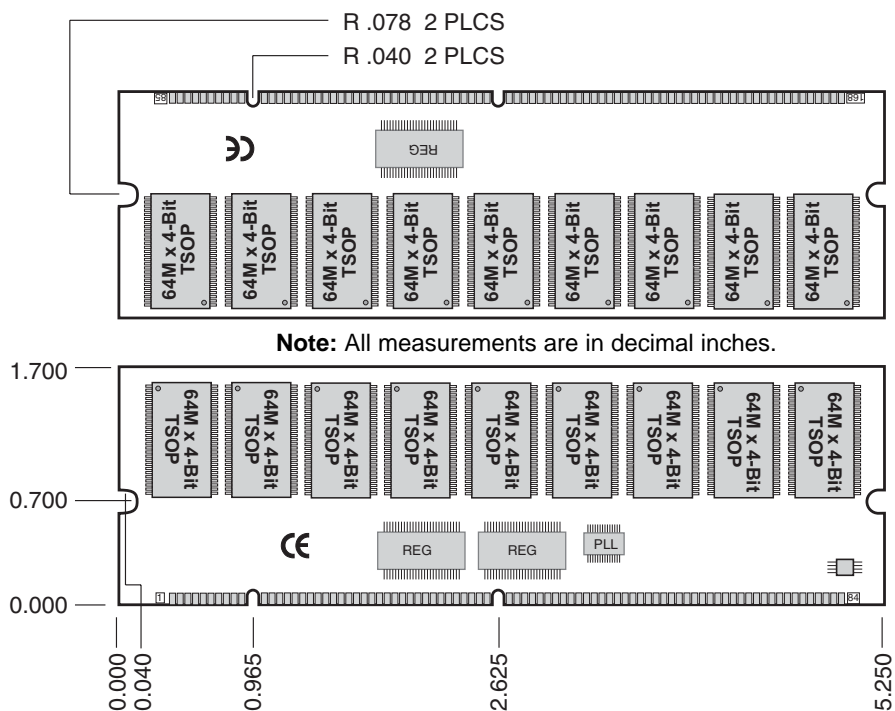


**Note:** All measurements are in decimal inches.

***Figure 1: KVR100X72RC2/512 168-Pin SDRAM Module***