

Memory Module Specification

KHX4300D2/256

256MB 32M x 64-Bit PC2-4300 CL3 240-Pin DIMM

DESCRIPTION:

This document describes Kingston's 32M x 64-bit (256MB) DDR2-533 CL3 SDRAM (Synchronous DRAM) memory module, based on four 32M x 16-bit DDR2 FBGA components. This module has been tested to run at DDR2 533MHz at low latency timing of 3-3-3-10 at 1.85V. The SPD is programmed to JEDEC standard latency timing of 4-4-4-12 at 1.8V. This 240-pin DIMM uses gold contact fingers and requires +1.8V. The electrical and mechanical specifications are as follows:

FEATURES:

- Power supply: Vdd: $1.8V \pm 0.1V$, Vddq: $1.8V \pm 0.1V$
- Double-data-rate architecture; two data transfers per clock cycle
- ☑ Bidirectional data strobe(DQS)
- ☑ Differential clock inputs(CK and CK)
- DLL aligns DQ and DQS transition with CK transition
- Programmable Read latency 3 (clock)
- Burst Length: 4, 8 (Interleave/nibble sequential)
- Programmable Burst type (sequential & interleave)
- ☑ Timing Reference: 3-3-3-10 at +1.85V
- Edge aligned data output, center aligned data input
- Auto & Self refresh, 7.8us refresh interval (8K/64ms refresh)
- Serial presence detect with EEPROM
- High Performance Heat Spreader
- PCB: Height 1.180" (30.00mm), single sided component

PERFORMANCE:

V	Clock Cycle Time (tCK) CL=3	5ns (min.) / 8ns (max.)

- Row Cycle Time (tRC)

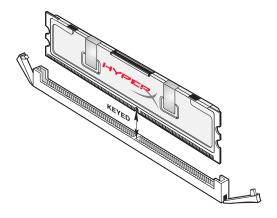
 Refresh to Active/Refresh Command Time (tRFC)

 60ns (min.)

 105ns
- Row Active Time (tRAS) 45ns (min.) / 70,000ns (max.)
- Single Power Supply of 43ns (min.) / /0,000ns (r
- Power 0.864 W (operating)
- ✓ Power 0.864 W (operating UL Rating 94 V 0
- Operating Temperature

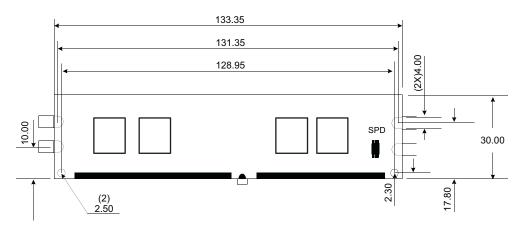
 Operating Temperature

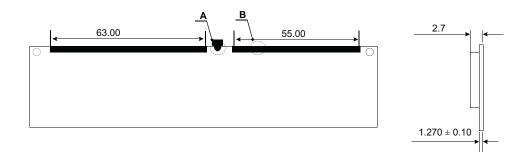
 Octo55° C
 - Storage Temperature -55° C to +125° C

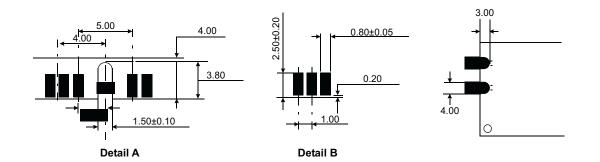


MODULE DIMENSIONS:









For more information, go to www.kingston.com

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