

Memory Module Specification

KVR400D4R3A/2G 2GB 256M x 72-Bit DDR400 CL3 Registered 184-Pin ECC DIMM

DESCRIPTION:

This document describes ValueRAM's 256M x 72-bit 2GB (2048MB) DDR400 CL3 SDRAM (Synchronous DRAM) "dual rank" Registered ECC memory module. The components on this module include thirty-six 128M x 4-bit (32M x 4-Bit x 4 bank) DDR400 SDRAM in FBGA packages. This 184-pin DIMM uses gold contact fingers and requires +2.6V. The electrical and mechanical specifications are as follows:

FEATURES:

- \checkmark Clock Cycle Time (tCK)
- \checkmark Row Cycle Time (tRC)
- ☑ Refresh Row Cycle Time (tRFC)
- ☑ Row Active Time (tRAS)
- Single Power Supply of
- Dever 2
- **UL** Rating

5ns (min.) / 10ns (max.) 55ns (min.) 70ns (min.) 40ns (min.) / 70,000ns (max.) +2.6V (+/- .1V) 11.024 W (operating) 94 V - 0

Note: The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 1.200" (30.48mm).

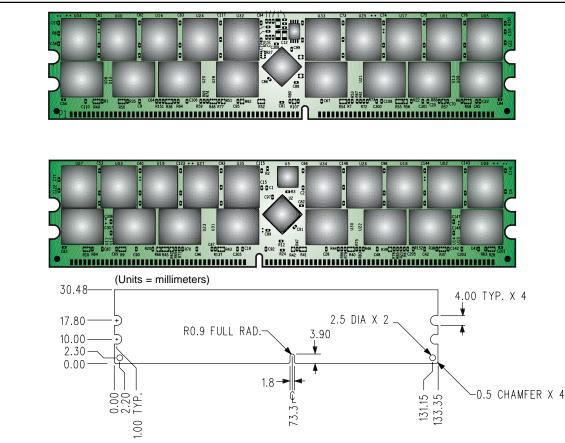


Figure 1: KVR400D4R3A/2G 184-Pin DIMM