

## KVR333X64C25/128 128MB 16M x 64-Bit DDR333 CL2.5 184-Pin DIMM

### DESCRIPTION:

This document describes ValueRAM's 16M x 64-bit (128MB) CL2.5 DDR333 SDRAM (Synchronous DRAM) memory module. The components on this module include four 16M x 16-bit (4M x 16-bit x 4 Bank) DDR333 SDRAM in TSOP packages. This 184-pin DIMM uses gold contact fingers and requires +2.5V. The electrical and mechanical specifications are as follows:

### FEATURES:

<input checked="" type="checkbox"/> Clock Cycle Time (tCK)	6ns (min.) / 12ns (max.)
<input checked="" type="checkbox"/> Row Cycle Time (tRC)	60ns (min.)
<input checked="" type="checkbox"/> Refresh Row Cycle Time (tRFC)	72ns (min.)
<input checked="" type="checkbox"/> Row Active Time (tRAS)	42ns (min.) / 70,000ns (max.)
<input checked="" type="checkbox"/> Single Power Supply of	+2.5V (+/- .25V)
<input checked="" type="checkbox"/> Power (IDD0)	0.900 W (operating)
<input checked="" type="checkbox"/> UL Rating	94 V - 0

**Note:** The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 1.250" (31.75mm).

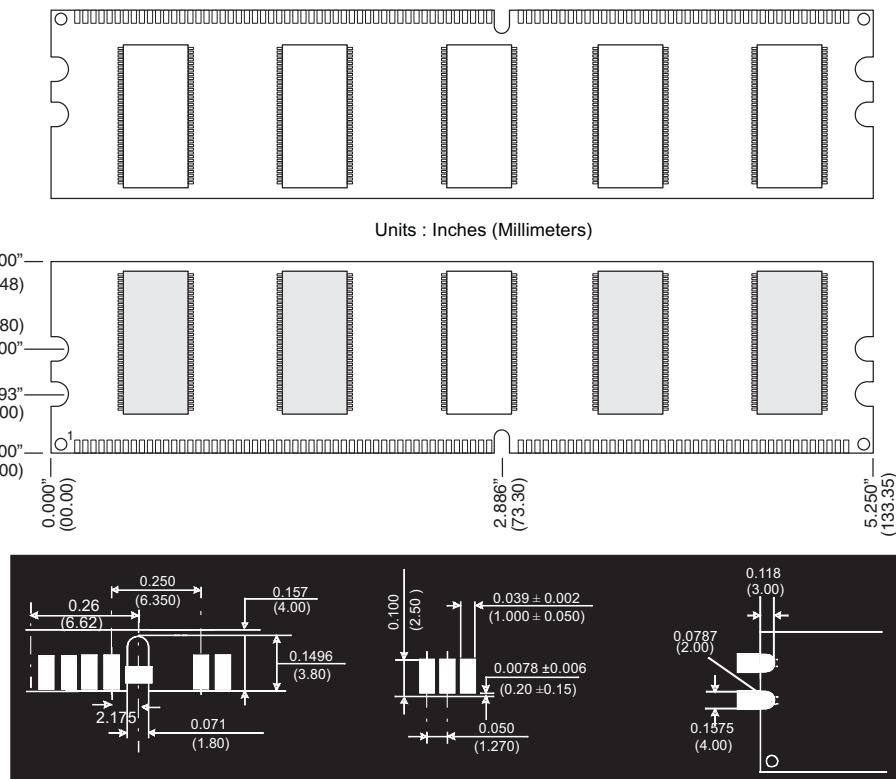


Figure 1: KVR333X64C25/128 184-Pin DDR SDRAM Module