

## KVR100X72RC2/128

### PC100 Registered SDRAM 168-Pin CL2 ECC DIMM Memory Module

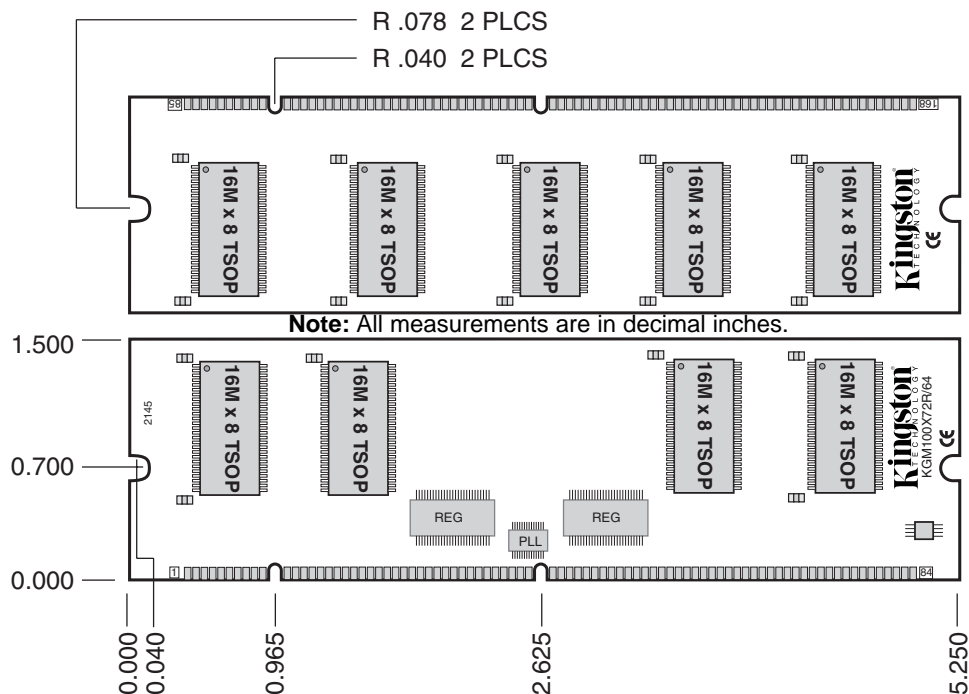
#### DESCRIPTION:

This document describes ValueRAM's 16M x 72-bit (128MB) PC100 CL2 (CAS Latency 2) ECC Registered SDRAM (Synchronous DRAM) memory module. The components on this module include nine 16M x 8-bit (4M x 8-bit x 4 Bank) SDRAM CL2 PC100 components in TSOP packages. This 168-pin DIMM uses gold contact fingers and requires +3.3V. The electrical and mechanical specifications are as follows:

#### FEATURES:

- |                          |   |
|--------------------------|---|
| ✓ CLK Cycle Time (tCK)   | min. = 10ns (CAS Latency = 2) / max. = 1000ns |
| ✓ Row Cycle Time (tRC)   | 70ns  |
| ✓ CLK to Output (tAC)    | max. = 6ns (CAS Latency = 2)                  |
| ✓ Single Power Supply of | +3.3V (+/- .3V)                               |
| ✓ Power                  | 3.901 W (operating)                           |
| ✓ UL Rating              | 94 V - 0                                      |

**Note:** The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 1.700" (43.18mm).



**Figure 1: KVR100X72RC2/128 168-Pin SDRAM Module**