

# Memory Module Specification

### KVR400X72C3/512 512MB 64M x 72-Bit DDR400

## ECC CL3 184-Pin DIMM

### **DESCRIPTION:**

This document describes ValueRAM's 64M x 72-bit (512MB) CL3 ECC DDR400 SDRAM (Synchronous **DRAM**) memory module. The components on this module include eighteen 32M x 8-bit (8M x 8-bit x 4 Bank) DDR400 SDRAM in TSOP packages. This 184-pin DIMM uses gold contact fingers and requires +2.5V. The electrical and mechanical specifications are as follows:

### **FEATURES:**

☑ Clock Cycle Time (tCK) 5ns (min.) / 8ns (max.)

✓ Row Cycle Time (tRC)✓ Refresh Row Cycle Time (tRFC)60ns (min.)70ns (min.)

☑ Row Active Time (tRAS) 40ns (min.) / 70,000ns (max.)

✓ Single Power Supply of +2.6V (+/- .1V)
✓ Power (IDD0) +2.12 W (operating)

**☑** UL Rating 94 V - 0

**Note:** For some configurations, the module height may vary from what is described here. However, in no configuration will the module height exceed 1.250" (31.75mm).

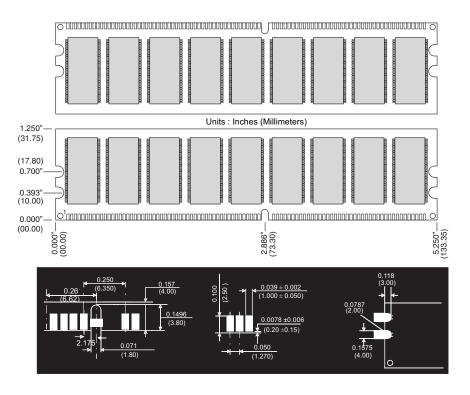


Figure 1: KVR400X72C3/512 184-Pin DIMM Module