

Memory Module Specification

KHX3200A/512 512MB 64M x 64-Bit PC3200 CL2 184-Pin DIMM



DESCRIPTION:

This document describes Kingston's 64M x 64-bit (512MB) DDR400 SDRAM (Synchronous DRAM) CL2 memory module. The components on this module include sixteen 32M x 8-bit (8M x 8-bit x 4 Bank / 200MHz, 5ns) DDR400 SDRAM in TSOP packages. This 184-pin DIMM uses gold contact fingers and requires +2.6V. The electrical and mechanical specifications are as follows:

FEATURES:

- Power supply: Vdd: $2.6V \pm 0.2V$, Vddq: $2.6V \pm 0.2V$
- Double-data-rate architecture; two data transfers per clock cycle
- ☑ Bidirectional data strobe(DQS)
- Differential clock inputs(CK and CK)
- ✓ DLL aligns DQ and DQS transition with CK transition
- Programmable Read latency 2 (clock)
- Programmable Burst length (2, 4, 8)
- Programmable Burst type (sequential & interleave)
- \checkmark Timing Reference: 2-3-2-6-1 at +2.6V
- Edge aligned data output, center aligned data input
- Auto & Self refresh, 7.8us refresh interval(8K/64ms refresh)
- Serial presence detect with EEPROM
- High Performance Heat Spreader
- PCB: Height 1.250" (34.75mm), double sided component

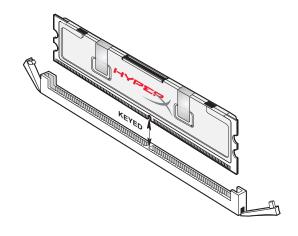
PERFORMANCE:

Row Cycle Time (tRC) 55ns (min.)
Refresh Row Cycle Time (tRFC) 65ns (min.)

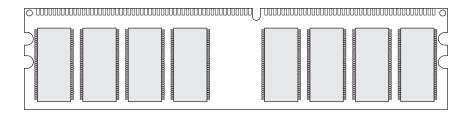
Row Active Time (tRAS) 40ns (min.) / 100,000ns (max.)

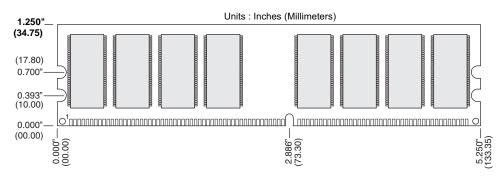
Power (IDD0) 3.744 W (operating)

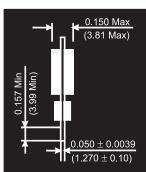
✓ UL Rating
 ✓ Operating Temperature
 ✓ Storage Temperature
 94 V - 0
 0° C to 70° C
 -55° C to +150° C

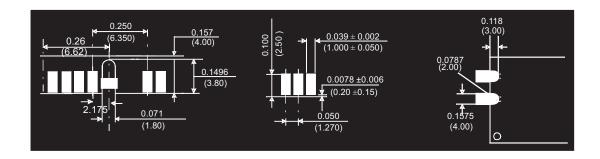


MODULE DIMENSIONS:









For more information, go to www.kingston.com

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