## KSM29RS8/8MEI

8GB 1Rx8 1G x 72-Bit PC4-2933
CL21 Registered w/Parity 288-Pin DIMM

## DESCRIPTION

Kingston's KSM29RS8/8MEI is a $1 \mathrm{G} \times 72$-bit (8GB) DDR4-2933 CL21 SDRAM (Synchronous DRAM) registered w/ parity, 1Rx8, ECC, memory module, based on nine 1G x 8-bit FBGA components. The SPD is programmed to JEDEC standard latency DDR4-2933 timing of 21-21-21 at 1.2V. Each 288-pin DIMM uses gold contact fingers. The electrical and mechanical specifications are as follows:

## FEATURES

- Power Supply: VDD $=1.2 \mathrm{~V}$
- $\mathrm{VDDQ}=1.2 \mathrm{~V}$
- $\mathrm{VPP}=2.5 \mathrm{~V}$
- $\mathrm{VDDSPD}=2.25 \mathrm{~V}$ to 2.75 V
- Functionality and operations comply with the DDR4 SDRAM datasheet
- 16 internal banks
- Bank Grouping is applied, and CAS to CAS latency (tCCD_L, tCCD_S) for the banks in the same or different bank group accesses are available
- Data transfer rates: PC4-2933, PC4-2666, PC4-2400, PC4-2133, PC4-1866, PC4-1600
- Bi-Directional Differential Data Strobe
- 8 bit pre-fetch
- Burst Length (BL) switch on-the-fly BL8 or BC4(Burst Chop)
- Supports ECC error correction and detection
- On-Die Termination (ODT)
- Temperature sensor with integrated SPD
- This product is in compliance with the RoHS directive.
- Per DRAM Addressability is supported
- Internal Vref DQ level generation is available
- Write CRC is supported at all speed grades
- CA parity (Command/Address Parity) mode is supported

SPECIFICATIONS

| CL(IDD) | 21 cycles |
| :--- | :--- |
| Row Cycle Time (tRCmin) | $45.75 \mathrm{~ns}(\mathrm{~min})$. |
| Refresh to Active/Refresh <br> Command Time (tRFCmin) | $350 \mathrm{~ns}(\mathrm{~min})$. |
| Row Active Time (tRASmin) | 32 ns (min.) |
| Maximum Operating Power | $*$ |
| UL Rating | $94 \mathrm{~V}-0$ |
| Operating Temperature | $0^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| Storage Temperature | $-55^{\circ} \mathrm{C}$ to $+100^{\circ} \mathrm{C}$ |
| *See IDD Specifications (page 2) |  |

Module Assembly
DRAM: MICRON (E-DIE)
RCD: IDT

## IDD Specifications

| Symbol | 2933 | Units |
| :---: | :---: | :---: |
| $\mathrm{IDDO}^{1}$ | 405 | mA |
| $\mathrm{IPPO}^{1}$ | 27 | mA |
| $\mathrm{IDD1}^{1}$ | 549 | mA |
| $\mathrm{IDD2N}^{2}$ | 288 | mA |
| $\mathrm{IDD2NT}^{1}$ | 378 | mA |
| $\mathrm{I}_{\text {DD2P }}{ }^{2}$ | 198 | mA |
| $\mathrm{IDD2Q}^{2}$ | 234 | mA |
| $\mathrm{I}_{\mathrm{DD} 3 \mathrm{~N}}{ }^{2}$ | 369 | mA |
| ${\mathrm{IPP} 3 \mathrm{~N}^{2}}$ | 27 | mA |
| $\mathrm{l}_{\text {D } 3 \text { P }}{ }^{2}$ | 288 | mA |
| $\mathrm{I}_{\text {D } 4 \text { 4 }}{ }^{1}$ | 1503 | mA |
| $\mathrm{I}_{\text {D } 4 W}{ }^{1}$ | 1269 | mA |
| $\mathrm{I}_{\text {DD5R }}{ }^{1}$ | 441 | mA |
| $\mathrm{IPP5R}^{1}$ | 45 | mA |
| $\mathrm{IDD6N}^{2}$ | 306 | mA |
| $\mathrm{IDD6E}^{2}$ | 522 | mA |
| $\mathrm{I}_{\mathrm{DD6R}}{ }^{2}$ | 189 | mA |
| $\mathrm{IDD6A}^{2}$ | 77.4 | mA |
| $\mathrm{IDD6A}^{2}$ | 189 | mA |
| $\mathrm{IDD6A}^{2}$ | 279 | mA |
| $\mathrm{IDD6A}^{2}$ | 522 | mA |
| $I_{\text {PP6 }}{ }^{2}$ | 45 | mA |
| $\mathrm{IDD7}^{1}$ | 1665 | mA |
| $\mathrm{lpP7}^{1}$ | 117 | mA |
| $\mathrm{IDD8}^{2}$ | 162 | mA |

Notes: 1. One module rank in the active IDD/PP, the other rank in IDD2P/PP3N.
2. All ranks in this IDD/PP condition.

## MODULE DIMENSIONS




The product images shown are for illustration purposes only and may not be an exact representation of the product.
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