

Memory Module Specification

KVR400D2S8R3/512I 512MB 64M x 72-Bit DDR2-400 CL3 Registered 240-Pin ECC DIMM

DESCRIPTION:

This document describes ValueRAM's 64M x 72-bit (512MB) DDR2-400 CL3 SDRAM (Synchronous **DRAM**) "single rank" registered ECC "Intel® Certified" memory module. The components on this module include nine 64M x 8-bit (16M x 8-bit x 4 Bank) DDR2-400 SDRAM in FBGA packages. This 240-pin DIMM uses gold contact fingers and requires +1.8V. The electrical and mechanical specifications are as follows:

FEATURES:

☑ Clock Cycle Time (tCK) CL=3

- ☑ Row Cycle Time (tRC)
- ☑ Refresh to Active/Refresh Command Time (tRFC)
- \mathbf{Y} Row Active Time (tRAS)
- Single Power Supply of
- Dever 2
- **UL** Rating

5ns (min.) / 8ns (max.) 55ns (min.) 105ns 40ns (min.) / 70,000ns (max.) +1.8V (+/- .1V) 2.277 W (operating) 94 V - 0

Note: The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 1.250" (31.75mm).



