

Memory Module Specification 

## KVR400D2S3/128 128MB 16M x 64-Bit DDR2-400 CL3 200-Pin SODIMM

## **DESCRIPTION:**

This document describes ValueRAM's 16M x 64-bit (128MB) DDR2-400 CL3 SDRAM (Synchronous DRAM) memory module. The components on this module include four 16M x 16-bit (4M x 16-bit x 4 Bank) DDR2-400 SDRAM in FBGA packages. This 200-pin SODIMM uses gold contact fingers and requires +1.8V. The electrical and mechanical specifications are as follows:

## FEATURES:

- $\checkmark$  Clock Cycle Time (tCK) CL=3
- Row Cycle Time (tRC)
- Refresh to Active/Refresh Command Time (tRFC)
- $\checkmark$  Row Active Time (tRAS)
- Single Power Supply of
- Dever Power
- **UL** Rating

5ns (min.) / 8ns (max.) 55ns (min.) 75ns 40ns (min.) / 70,000ns (max.) +1.8V (+/-.1V) 0.684 W (operating) 94 V - 0

Note: The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 30.00mm.



