

Memory Module Specification

KVR266X72RC25L/512 512MB 64M x 72-Bit DDR266 CL2.5 ECC Registered 184-Pin DIMM

DESCRIPTION:

This document describes ValueRAM's 64M x 72-bit (512MB) DDR266 CL2.5 SDRAM (Synchronous DRAM) low-profile Registered ECC memory module. The components on this module include eighteen 64M x 4-bit (16M x 4-bit x 4 Bank) DDR266 SDRAM in TSOP packages. This 184-pin DIMM uses gold contact fingers and requires +2.5V. The electrical and mechanical specifications are as follows:

FEATURES:

- \checkmark Clock Cycle Time (tCK)
- $\blacksquare Row Cycle Time (tRC)$
- ☑ Refresh Row Cycle Time (tRFC) 75ns (min.)
- \checkmark Row Active Time (tRAS)
- Single Power Supply of
- Dever 2
- 🗹 UL Rating

7.5ns (min.) / 12ns (max.) 65ns (min.) 75ns (min.) 45ns (min.) / 120,000ns (max.) +2.5V (+/- .2V) 5.175 W (operating) 94 V - 0

Note: The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 1.2" (30.48mm).



