

Memory Module Specification

KVR667D2S8P5/512

512MB 64M x 72-Bit DDR2-667 CL5 Registered w/Parity 240-Pin DIMM

DESCRIPTION:

This document describes ValueRAM's 64M x 72-bit (512MB) DDR2-667 CL5 SDRAM (Synchronous **DRAM**) "single rank" Registered w/Parity memory module. The components on this module include nine 64M x 8-bit (16M x 8-bit x 4 Bank) DDR2-667 SDRAM in FBGA packages. This 240-pin DIMM uses gold contact fingers and requires +1.8V. The electrical and mechanical specifications are as follows:

FEATURES:

☑ Clock Cycle Time (tCK) CL=5

☑ Row Cycle Time (tRC)

Refresh to Active/Refresh Command Time (tRFC)

☑ Row Active Time (tRAS)

☑ Single Power Supply of

2 Power

☑ UL Rating

3ns (min.) / 8ns (max.)

54ns (min.)

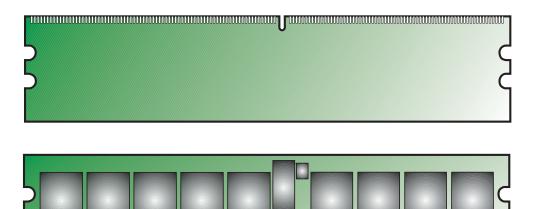
105ns

39ns (min.) / 70,000ns (max.)

+1.8V (+/-.1V)

2.277 W (operating)

94 V - 0



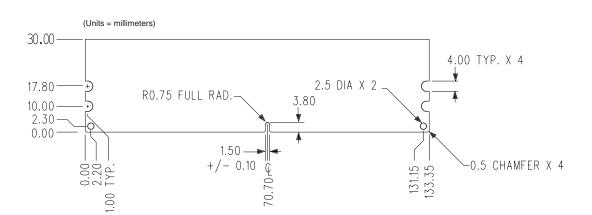


Figure 1: KVR667D2S8P5/512 240-Pin DIMM