

# PowerEdge R650xs Information Update - Tech Sheet

## Notes, cautions, and warnings

 **NOTE:** A NOTE indicates important information that helps you make better use of your product.

 **CAUTION:** A CAUTION indicates either potential damage to hardware or loss of data and tells you how to avoid the problem.

 **WARNING:** A WARNING indicates a potential for property damage, personal injury, or death.

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# Overview

The information in this document supersedes the information in the pertinent sections of the Installation and Service Manual, BIOS and UEFI Reference Guide, and Technical Specifications.

For a complete list of information, see the documents available at <https://www.dell.com/poweredgemanuals>.

## Topics:

- [Revision history](#)

## Revision history

This section provides a description of document changes.

**Table 1. Document Revision history**

Document Revision	Date	Description of changes
1	November, 2022	<ol style="list-style-type: none"><li>1. Removing the power interposer board</li><li>2. Updated BIOS, System Security, Intel(R) SGX</li><li>3. Updated Expansion cards</li><li>4. Updated minimum to post configuration</li><li>5. Updated the PSU's</li></ol>

# Information Update

## Topics:

- Removing the power interposer board
- System Security
- Expansion card installation guidelines
- Minimum configuration to POST
- PSU specifications

## Removing the power interposer board

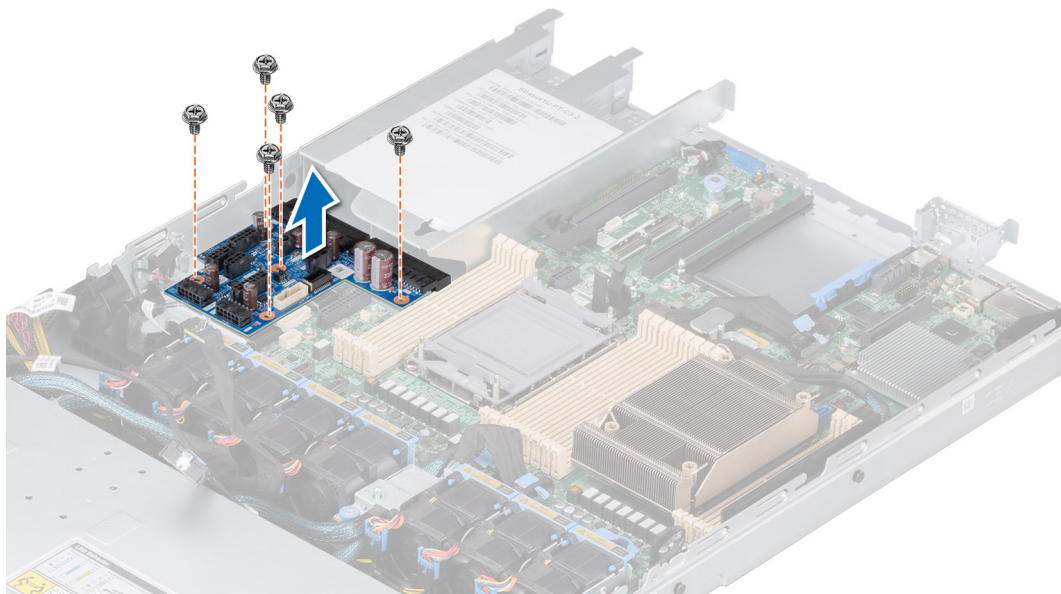
### Prerequisites

1. Follow the safety guidelines listed in the [Safety instructions](#).
2. Follow the procedure listed in [Before working inside your system](#).
3. [Remove the air shroud](#).
4. Remove the [PSU](#) or [PSU blank](#).
5. Disconnect the cables that are connected to power interposer board (PIB).

**i** **NOTE:** Systems shipped since Nov'22 may have a different power distribution board and connectors.

### Steps

1. Using a Phillips #2 screwdriver, remove the screws securing the power interposer board to the system.  
**i** **NOTE:** Observe the routing of the cable as you remove it from the system.
2. Lift the PIB away from the system.



**Figure 1. Removing the power interposer board**

## Next steps

Replace the power interposer board.


# System Security

To view the **System Security** screen, power on the system, press F2, and click **System Setup Main Menu > System BIOS > System Security**.


**Table 2. System Security details**

Option	Description
<b>CPU AES-NI</b>	Improves the speed of applications by performing encryption and decryption by using the Advanced Encryption Standard Instruction Set (AES-NI). This option is set to <b>Enabled</b> by default.
<b>System Password</b>	Sets the system password. This option is set to <b>Enabled</b> by default and is read-only if the password jumper is not installed in the system.
<b>Setup Password</b>	Sets the setup password. This option is read-only if the password jumper is not installed in the system.
<b>Password Status</b>	Locks the system password. This option is set to <b>Unlocked</b> by default.
<b>TPM Information</b>	Indicates the type of Trusted Platform Module, if present.

**Table 3. TPM 1.2 security information**

Option	Description
<b>TPM Information</b>	
<b>TPM Security</b>	 <b>NOTE:</b> The TPM menu is available only when the TPM module is installed.  Enables you to control the reporting mode of the TPM. The <b>TPM Security</b> option is set to <b>Off</b> by default. You can only modify the TPM Status, and TPM Activation if the <b>TPM Status</b> field is set to either <b>On with Pre-boot Measurements</b> or <b>On without Pre-boot Measurements</b> .  When TPM 1.2 is installed, the <b>TPM Security</b> option is set to <b>Off, On with Pre-boot Measurements, or On without Pre-boot Measurements</b> .
<b>TPM Information</b>	Displays the operational state of the TPM.
<b>TPM Firmware</b>	Indicates the firmware version of the TPM.
<b>TPM Status</b>	Specifies the TPM status.
<b>TPM Command</b>	Controls the Trusted Platform Module (TPM). When set to <b>None</b> , no command is sent to the TPM. When set to <b>Activate</b> , the TPM is enabled and activated. When set to <b>Deactivate</b> , the TPM is disabled and deactivated. When set to <b>Clear</b> , all the contents of the TPM are cleared. This option is set to <b>None</b> by default.
<b>TPM Advance Settings</b>	<b>TPM PPI Bypass Provision</b> When set to <b>Enabled</b> , allows the Operating System to bypass Physical Presence Interface (PPI) prompts when issuing PPI Advanced Configuration and Power interface (ACPI) provisioning operations.
	<b>TPM PPI Bypass Clear</b> When set to <b>Enabled</b> allows the Operating System to bypass Physical Presence Interface (PPI) prompts when issuing PPI Advanced Configuration and Power Interface (ACPI) clear operations.

**Table 4. TPM 2.0 security information**

Option	Description
<b>TPM Information</b>	
<b>TPM Security</b>	 <b>NOTE:</b> The TPM menu is available only when the TPM module is installed.



**Table 4. TPM 2.0 security information (continued)**

Option	Description	
	<p>Enables you to control the reporting mode of the TPM. The <b>TPM Security</b> option is set to <b>Off</b> by default.</p> <p>When TPM 2.0 is installed, the <b>TPM Security</b> option is set to <b>On</b> or <b>Off</b>. This option is set to <b>Off</b> by default.</p>	
<b>TPM Information</b>	Displays the operational state of the TPM.	
<b>TPM Firmware</b>	Indicates the firmware version of the TPM.	
<b>TPM Hierarchy</b>	<p>Enables, disables, or clears the storage and endorsement hierarchies. When set to <b>Enabled</b>, the storage and endorsement hierarchies can be used.</p> <p>When set to <b>Disabled</b>, the storage and endorsement hierarchies cannot be used.</p> <p>When set to <b>Clear</b>, the storage and endorsement hierarchies are cleared of any values, and then reset to <b>Enabled</b>.</p>	
<b>TPM Advanced Settings</b>	<b>TPM PPI Bypass Provision</b>	When set to <b>Enabled</b> , allows the Operating System to bypass Physical Presence Interface (PPI) prompts when issuing PPI Advanced Configuration and Power interface (ACPI) provisioning operations.
	<b>TPM PPI Bypass Clear</b>	When set to <b>Enabled</b> allows the Operating System to bypass Physical Presence Interface (PPI) prompts when issuing PPI Advanced Configuration and Power Interface (ACPI) clear operations.
	<b>TPM2 Algorithm Selection</b>	<p>Allows the user to change the cryptographic algorithms used in the Trusted Platform Module (TPM). The available options are dependent on the TPM firmware.</p> <p>To enable TPM2 Algorithm Selection, Intel(R) TXT technology must be disabled.</p> <p>The TPM2 Algorithm Selection option supports SHA1, SHA128, SHA256, SHA512 and SM3 by detecting the TPM module. This option is set to <b>SHA1</b> by default.</p>

**Table 5. System Security details**

Option	Description
<b>Intel(R) TXT</b>	Enables you to set the Intel Trusted Execution Technology (TXT) option. To enable the <b>Intel TXT</b> option, virtualization technology and TPM Security must be enabled with Pre-boot measurements for TPM 1.2 or set to <b>On</b> with SHA256 algorithm for TPM 2.0. This option is set to <b>Off</b> by default. It is set <b>On</b> for Secure Launch (Firmware Protection) support on Windows 2022.
<b>Memory Encryption</b>	Enables or disables the Intel Total Memory Encryption (TME) and Multi-Tenant (Intel® TME-MT). When option is set to <b>Disabled</b> , BIOS disables both TME and MK-TME technology. When option is set to <b>Single Key</b> BIOS enables the TME technology. When option is set to <b>Multiple Keys</b> , BIOS enables the TME-MT technology, the CPU Physical Address Limit option must be disabled for selecting Multiple Keys option. This option is set to <b>Disabled</b> by default.
<b>Intel(R) SGX</b>	Enables you to set the Intel Software Guard Extension (SGX) option. To enable the <b>Intel SGX</b> option, processor must be SGX capable, memory population must be compatible (minimum x8 identical DIMM1 to DIMM8 per CPU socket, not support on persistent memory configuration), memory operating mode must be set at optimizer mode, memory encryption must be enabled and node interleaving must be disabled. This option is set to <b>Off</b> by default. When this option is to <b>Off</b> , BIOS disables the SGX technology. When this option is to <b>On</b> , BIOS enables the SGX technology.
<b>SGX Package Info In-Band Access</b>	Enables you to access the Intel Software Guard Extension (SGX) package info in-band option. This option is set to <b>Off</b> by default.
<b>PPMRR Size</b>	Sets the PPMRR size.
<b>SGX QoS</b>	Enables or disables the SGX quality of service.

**Table 5. System Security details (continued)**

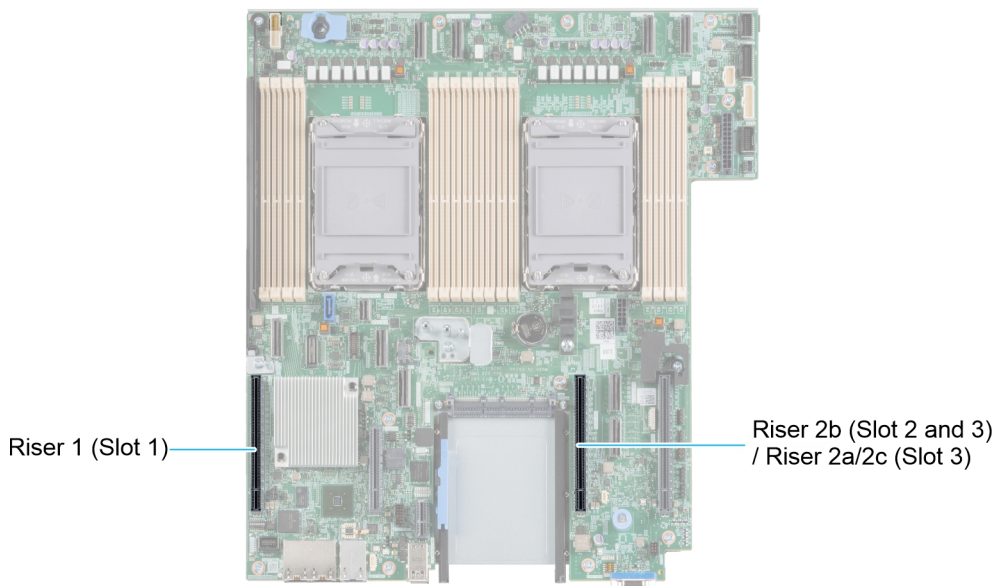
Option	Description
<b>Select Owner EPOCH input type</b>	Enables you to select <b>Change to New random Owner EPOCHs</b> or <b>Manual User Defined Owner EPOCHs</b> . Each EPOCH is 64-bit. After generating new EPOCH by selecting <b>Change to New random Owner EPOCHs</b> , the selection reverts back to <b>Manual User Defined Owner EPOCHs</b> .
	<b>Software Guard Extensions Epoch n:</b> Sets the Software Guard Extensions Epoch values.
<b>Enable writes to SGXLEPUBKEYHASH[3:0] from OS/SW</b>	Enables or disables the Enable writes to SGXLEPUBKEYHASH[3:0] from OS/SW.
	<b>SGX LE Public Key Hash0:</b> Sets the bytes from 0-7 for SGX Launch Enclave Public Key Hash.
	<b>SGX LE Public Key Hash1:</b> Sets the bytes from 8-15 for SGX Launch Enclave Public Key Hash.
	<b>SGX LE Public Key Hash2:</b> Sets the bytes from 16-23 for SGX Launch Enclave Public Key Hash.
<b>SGX LE Public Key Hash3:</b> Sets the bytes from 24-31 for SGX Launch Enclave Public Key Hash.	
<b>Enable/Disable SGX Auto MP Registration Agent</b>	Enables or disables the SGX Auto MP Registration. The MP registration agent is responsible to register the platform.
<b>SGX Factory Reset</b>	Enables you to reset the SGX option to factory settings. This option is set to <b>Off</b> by default.
<b>Power Button</b>	Enables or disables the power button on the front of the system. This option is set to <b>Enabled</b> by default.
<b>AC Power Recovery</b>	Sets how the system behaves after AC power is restored to the system. This option is set to <b>Last</b> by default.  <b>NOTE:</b> The host system will not power on up until iDRAC Root of Trust (RoT) is completed, host power on will be delayed by minimum 90 seconds after the AC applied.
<b>AC Power Recovery Delay</b>	Sets the time delay for the system to power up after AC power is restored to the system. This option is set to <b>Immediate</b> by default. When this option is set to <b>Immediate</b> , there is no delay for power up. When this option is set to <b>Random</b> , the system creates a random delay for power up. When this option is set to <b>User Defined</b> , the system delay time is manually to power up.
<b>User Defined Delay (60 s to 600 s)</b>	Sets the <b>User Defined Delay</b> option when the <b>User Defined</b> option for <b>AC Power Recovery Delay</b> is selected. The actual AC recovery time needs to add iDRAC root of trust time (around 50 seconds).
<b>UEFI Variable Access</b>	Provides varying degrees of securing UEFI variables. When set to <b>Standard</b> (the default), UEFI variables are accessible in the operating system per the UEFI specification. When set to <b>Controlled</b> , selected UEFI variables are protected in the environment and new UEFI boot entries are forced to be at the end of the current boot order.
<b>In-Band Manageability Interface</b>	When set to <b>Disabled</b> , this setting hides the Management Engine's (ME), HECI devices, and the system's IPMI devices from the operating system. This prevents the operating system from changing the ME power capping settings, and blocks access to all in-band management tools. All management should be managed through out-of-band. This option is set to <b>Enabled</b> by default.  <b>NOTE:</b> BIOS update requires HECI devices to be operational and DUP updates require IPMI interface to be operational. This setting needs to be set to Enabled to avoid updating errors.
<b>SMM Security Migration</b>	Enables or disables the UEFI SMM security migration protections. It is enabled for Windows 2022 support.



**Table 5. System Security details (continued)**

Option	Description								
<b>Secure Boot</b>	Enables Secure Boot, where the BIOS authenticates each pre-boot image by using the certificates in the Secure Boot Policy. Secure Boot is set to <b>Disabled</b> by default.								
<b>Secure Boot Policy</b>	When Secure Boot policy is set to <b>Standard</b> , the BIOS uses the system manufacturer's key and certificates to authenticate pre-boot images. When Secure Boot policy is set to <b>Custom</b> , the BIOS uses the user-defined key and certificates. Secure Boot policy is set to <b>Standard</b> by default.								
<b>Secure Boot Mode</b>	<p>Configures how the BIOS uses the Secure Boot Policy Objects (PK, KEK, db, dbx).</p> <p>If the current mode is set to <b>Deployed Mode</b>, the available options are <b>User Mode</b> and <b>Deployed Mode</b>. If the current mode is set to <b>User Mode</b>, the available options are <b>User Mode</b>, <b>Audit Mode</b>, and <b>Deployed Mode</b>.</p> <p><b>Table 6. Secure Boot Mode</b></p> <table border="1" data-bbox="518 674 1481 1285"> <thead> <tr> <th data-bbox="523 680 675 721">Options</th> <th data-bbox="679 680 1476 721">Descriptions</th> </tr> </thead> <tbody> <tr> <td data-bbox="523 728 675 875"><b>User Mode</b></td> <td data-bbox="679 728 1476 875">In <b>User Mode</b>, PK must be installed, and BIOS performs signature verification on programmatic attempts to update policy objects.  The BIOS allows unauthenticated programmatic transitions between modes.</td> </tr> <tr> <td data-bbox="523 882 675 1122"><b>Audit mode</b></td> <td data-bbox="679 882 1476 1122">In <b>Audit Mode</b>, PK is not present. BIOS does not authenticate programmatic update to the policy objects and transitions between modes. The BIOS performs a signature verification on pre-boot images and logs the results in the image Execution Information Table, but executes the images whether they pass or fail verification.  <b>Audit Mode</b> is useful for programmatic determination of a working set of policy objects.</td> </tr> <tr> <td data-bbox="523 1128 675 1279"><b>Deployed Mode</b></td> <td data-bbox="679 1128 1476 1279"><b>Deployed Mode</b> is the most secure mode. In <b>Deployed Mode</b>, PK must be installed and the BIOS performs signature verification on programmatic attempts to update policy objects.  <b>Deployed Mode</b> restricts the programmatic mode transitions.</td> </tr> </tbody> </table>	Options	Descriptions	<b>User Mode</b>	In <b>User Mode</b> , PK must be installed, and BIOS performs signature verification on programmatic attempts to update policy objects.  The BIOS allows unauthenticated programmatic transitions between modes.	<b>Audit mode</b>	In <b>Audit Mode</b> , PK is not present. BIOS does not authenticate programmatic update to the policy objects and transitions between modes. The BIOS performs a signature verification on pre-boot images and logs the results in the image Execution Information Table, but executes the images whether they pass or fail verification.  <b>Audit Mode</b> is useful for programmatic determination of a working set of policy objects.	<b>Deployed Mode</b>	<b>Deployed Mode</b> is the most secure mode. In <b>Deployed Mode</b> , PK must be installed and the BIOS performs signature verification on programmatic attempts to update policy objects.  <b>Deployed Mode</b> restricts the programmatic mode transitions.
Options	Descriptions								
<b>User Mode</b>	In <b>User Mode</b> , PK must be installed, and BIOS performs signature verification on programmatic attempts to update policy objects.  The BIOS allows unauthenticated programmatic transitions between modes.								
<b>Audit mode</b>	In <b>Audit Mode</b> , PK is not present. BIOS does not authenticate programmatic update to the policy objects and transitions between modes. The BIOS performs a signature verification on pre-boot images and logs the results in the image Execution Information Table, but executes the images whether they pass or fail verification.  <b>Audit Mode</b> is useful for programmatic determination of a working set of policy objects.								
<b>Deployed Mode</b>	<b>Deployed Mode</b> is the most secure mode. In <b>Deployed Mode</b> , PK must be installed and the BIOS performs signature verification on programmatic attempts to update policy objects.  <b>Deployed Mode</b> restricts the programmatic mode transitions.								
<b>Secure Boot Policy Summary</b>	Specifies the list of certificates and hashes that secure boot uses to authenticate images.								
<b>Secure Boot Custom Policy Settings</b>	<p>Configures the Secure Boot Custom Policy. To enable this option, set the Secure Boot Policy to <b>Custom</b> option. The list below provides the descriptions of different Secure Boot Custom Policy Settings available:</p> <ul style="list-style-type: none"> <li>● <b>Platform Key (PK)</b> - Import, export, delete, or restore the Platform Key (PK)</li> <li>● <b>Key Exchange Key Database (KEK)</b> - Import, export, delete, or restore entries in the key exchange key (KEK) database</li> <li>● <b>Authorized Signature Database (db)</b> - Import, export, delete, or restore entries in the authorized signature database (db)</li> <li>● <b>Forbidden Signature Database (dbx)</b> - Import, export, delete, or restore entries in the forbidden signature database (dbx)</li> <li>● <b>Delete All Policy Entries (PK, KEK, db, and dbx)</b> - Restore the system manufacturer's default entries for the PK, KEK, db, and dbx database. All imported entries will be removed.</li> <li>● <b>Export Firmware Hash Values</b> - Export values for third-party firmware images such as network controller firmware and storage controller firmware <ul style="list-style-type: none"> <li>○ <b>Select Firmware Image</b> - This is a list of third-party firmware images that the system attempted to load on this boot. Choose an image and then select "Export" to write the SHA-256 hash value of the image to a file</li> <li>○ <b>Export Selected Entry</b> - Write the selected database entry to a file</li> </ul> </li> </ul>								

# Expansion card installation guidelines

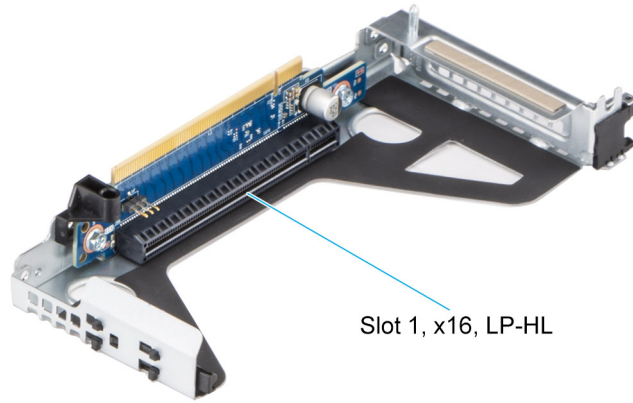


**Figure 2. Expansion card slot connectors**

The following table describes the expansion card riser configurations:

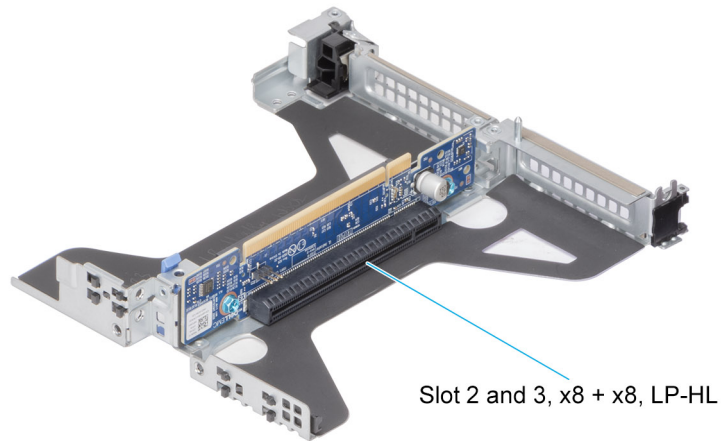
**Table 7. Expansion card riser configurations**

Configurations	Expansion card risers	PCIe Slots	Controlling processor	Height	Length	Slot width
Config0. with 1x LP	R1 + rear 2 drives	1	Processor 1	Low Profile	Half length	x16
Config1. with 3x LP	R1	1	Processor 1	Low Profile	Half length	x16
	R2a	2 and 3	Processor 2	Low Profile	Half length	x8 + x8
Config2. with 2x LP	R1	1	Processor 1	Low Profile	Half length	x16
	R2b (SNAPI)	3	Processor 1 and 2	Low Profile	Half length	x16
Config3. with 2x LP	R1	1	Processor 1	Low Profile	Half length	x16
	R2c	3	Processor 2	Low Profile	Half length	x16
Config4. with 1x LP	R1	1	Processor 1	Low Profile	Half length	x16
Config5. with 0x LP	NA	NA	NA	NA	NA	NA
Config6. with 1x LP	R2c	3	Processor 2	Low Profile	Half length	x16



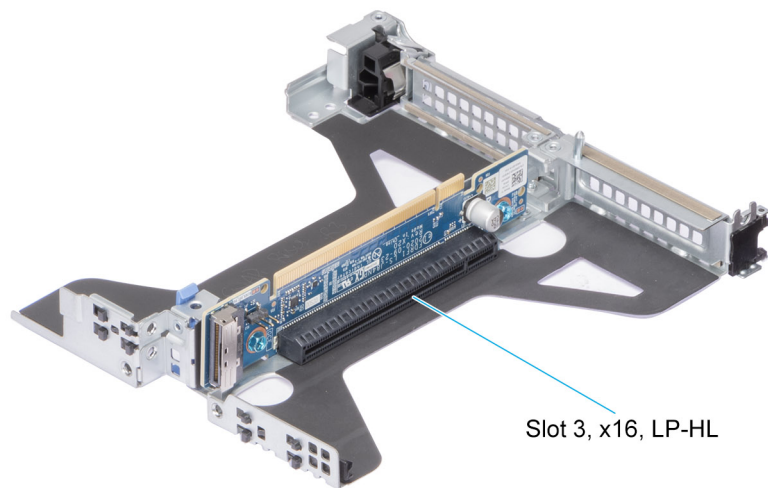
Slot 1, x16, LP-HL

Figure 3. Riser 1

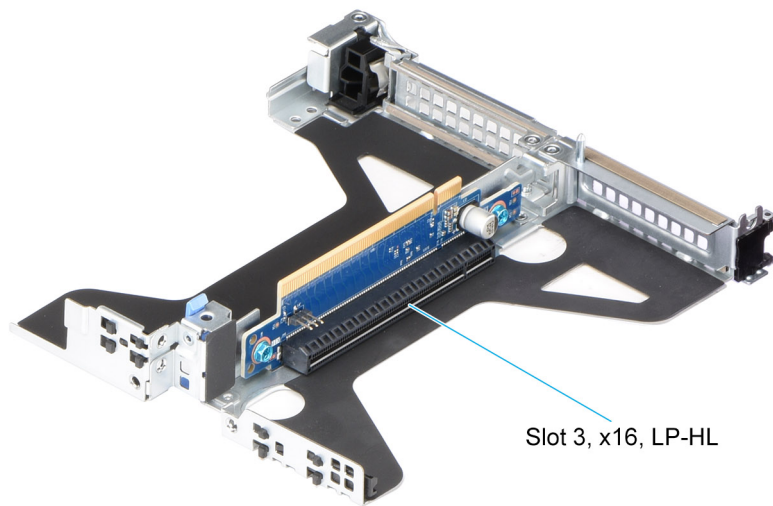


Slot 2 and 3, x8 + x8, LP-HL

Figure 4. Riser 2a



**Figure 5. Riser 2b (SNAPI)**



**Figure 6. Riser 2c**

**NOTE:** The expansion-card slots are not hot-swappable.

The following table provides guidelines for installing expansion cards to ensure proper cooling and mechanical fit. The expansion cards with the highest priority should be installed first using the slot priority indicated. All the other expansion cards should be installed in the card priority and slot priority order.

**Table 8. Configuration 0: R1**

Card type	Slot priority	Maximum number of cards
Dell Front PERC	Integrated slot	1
Dell Serial port module (LP)	1	1
Mellanox (NIC: 200Gb)	1	1
Intel (NIC: 100Gb)	1	1

**Table 8. Configuration 0: R1 (continued)**

Card type	Slot priority	Maximum number of cards
Mellanox (NIC: 100Gb)	1	1
Broadcom (NIC: 25Gb)	1	1
Intel (NIC: 25Gb)	1	1
Mellanox (NIC: 25Gb)	1	1
Qlogic (NIC: 25Gb)	1	1
SolarFlare (NIC: 25Gb)	1	1
Emulex (HBA: FC32)	1	1
Broadcom (HBA: FC32)	1	1
Marvell (HBA: FC32)	1	1
Avago (HBA: FC16)	1	1
QLogic (HBA: FC16)	1	1
Broadcom (NIC: 10Gb)	1	1
Intel (NIC: 10Gb)	1	1
Qlogic (NIC: 10Gb)	1	1
Broadcom (NIC: 1Gb)	1	1
Intel (NIC: 1Gb)	1	1
Mellanox (NIC: HDR100 VPI)	1	1
Mellanox (NIC: HDR VPI)	1	1
Intel (OCP: 100Gb)	Integrated slot	1
Broadcom (OCP: 25Gb)	Integrated slot	1
Intel (OCP: 25Gb)	Integrated slot	1
Marvell (OCP: 25Gb)	Integrated slot	1
Mellanox (OCP: 25Gb)	Integrated slot	1
SolarFlare (OCP: 25Gb)	Integrated slot	1
Broadcom (OCP: 10Gb)	Integrated slot	1
Marvell (OCP: 10Gb)	Integrated slot	1
Intel (OCP: 10Gb)	Integrated slot	1
Broadcom (OCP: 1Gb)	Integrated slot	1
Intel (OCP: 1Gb)	Integrated slot	1
Dell External PERC Adapter	1	1
Dell BOSS S1 Module	Integrated slot	1
Intel (PCIe SSD AIC)	1	1
Samsung (PCIe SSD AIC)	1	1

**Table 9. Configuration 1: R1+R2a**

Card type	Slot priority	Maximum number of cards
Dell Front PERC	Integrated slot	1
Dell Serial port module (LP)	2, 1	1

**Table 9. Configuration 1: R1+R2a (continued)**

<b>Card type</b>	<b>Slot priority</b>	<b>Maximum number of cards</b>
Mellanox (NIC: 200Gb)	1	1
Intel (NIC: 100Gb)	1	1
Mellanox (NIC: 100Gb)	1	1
Broadcom (NIC: 25Gb)	3, 1, 2	3
Intel (NIC: 25Gb)	3, 1, 2	3
Mellanox (NIC: 25Gb)	1	1
Qlogic (NIC: 25Gb)	3, 1, 2	3
SolarFlare (NIC: 25Gb)	3, 1, 2	3
Emulex (HBA: FC32)	3, 1, 2	3
Broadcom (HBA: FC32)	3, 1, 2	3
Marvell (HBA: FC32)	3, 1, 2	3
Avago (HBA: FC16)	3, 1, 2	3
QLogic (HBA: FC16)	3, 1, 2	3
Broadcom (NIC: 10Gb)	3, 1, 2	3
Intel (NIC: 10Gb)	3, 1, 2	3
Qlogic (NIC: 10Gb)	3, 1, 2	3
Broadcom (NIC: 1Gb)	3, 1, 2	3
Intel (NIC: 1Gb)	3, 1, 2	3
Mellanox (NIC: HDR100 VPI)	1	1
Mellanox (NIC: HDR VPI)	1	1
Intel (OCP: 100Gb)	Integrated slot	1
Broadcom (OCP: 25Gb)	Integrated slot	1
Intel (OCP: 25Gb)	Integrated slot	1
Marvell (OCP: 25Gb)	Integrated slot	1
Mellanox (OCP: 25Gb)	Integrated slot	1
SolarFlare (OCP: 25Gb)	Integrated slot	1
Broadcom (OCP: 10Gb)	Integrated slot	1
Marvell (OCP: 10Gb)	Integrated slot	1
Intel (OCP: 10Gb)	Integrated slot	1
Broadcom (OCP: 1Gb)	Integrated slot	1
Intel (OCP: 1Gb)	Integrated slot	1
Dell External PERC Adapter	3, 1, 2	3
Dell BOSS S1 Module	Integrated slot	1
Intel (PCIe SSD AIC)	3, 1, 2	3
Samsung (PCIe SSD AIC)	3, 1, 2	3

**Table 10. Configuration 2: R1+R2b**

<b>Card type</b>	<b>Slot priority</b>	<b>Maximum number of cards</b>
Dell Front PERC	Integrated slot	1
Dell Serial port module (LP)	2	1
Mellanox (NIC: 200Gb)	1	1
Intel (NIC: 100Gb)	1	1
Mellanox (NIC: 100Gb)	3, 1	2
Mellanox (NIC: 100Gb) - CSP	1	1
Broadcom (NIC: 25Gb)	1	1
Intel (NIC: 25Gb)	1	1
Mellanox (NIC: 25Gb)	3, 1	2
Mellanox (NIC: 25Gb) - CSP	1	1
Qlogic (NIC: 25Gb)	1	1
SolarFlare (NIC: 25Gb)	1	1
Emulex (HBA: FC32)	1	1
Broadcom (HBA: FC32)	1	1
Marvell (HBA: FC32)	1	1
Avago (HBA: FC16)	1	1
QLogic (HBA: FC16)	1	1
Broadcom (NIC: 10Gb)	1	1
Intel (NIC: 10Gb)	1	1
Qlogic (NIC: 10Gb)	1	1
Broadcom (NIC: 1Gb)	1	1
Intel (NIC: 1Gb)	1	1
Mellanox (NIC: HDR100 VPI)	3, 1	2
Mellanox (NIC: HDR VPI)	3, 1	2
Intel (OCP: 100Gb)	Integrated slot	1
Broadcom (OCP: 25Gb)	Integrated slot	1
Intel (OCP: 25Gb)	Integrated slot	1
Marvell (OCP: 25Gb)	Integrated slot	1
Mellanox (OCP: 25Gb)	Integrated slot	1
SolarFlare (OCP: 25Gb)	Integrated slot	1
Broadcom (OCP: 10Gb)	Integrated slot	1
Marvell (OCP: 10Gb)	Integrated slot	1
Intel (OCP: 10Gb)	Integrated slot	1
Broadcom (OCP: 1Gb)	Integrated slot	1
Intel (OCP: 1Gb)	Integrated slot	1
Dell External PERC Adapter	1	1
Dell BOSS S1 Module	Integrated slot	1

**Table 10. Configuration 2: R1+R2b (continued)**

Card type	Slot priority	Maximum number of cards
Intel (PCIe SSD AIC)	1	1
Samsung (PCIe SSD AIC)	1	1

**Table 11. Configuration 3: R1+R2c**

Card type	Slot priority	Maximum number of cards
Dell Front PERC	Integrated slot	1
Dell Serial port module (LP)	2	1
Mellanox (NIC: 200Gb)	3, 1	2
Intel (NIC: 100Gb)	3, 1	2
Mellanox (NIC: 100Gb)	3, 1	2
Broadcom (NIC: 25Gb)	3, 1	2
Intel (NIC: 25Gb)	3, 1	2
Mellanox (NIC: 25Gb)	3, 1	2
Qlogic (NIC: 25Gb)	3, 1	2
SolarFlare (NIC: 25Gb)	3, 1	2
Emulex (HBA: FC32)	3, 1	2
Broadcom (HBA: FC32)	3, 1	2
Marvell (HBA: FC32)	3, 1	2
Avago (HBA: FC16)	3, 1	2
QLogic (HBA: FC16)	3, 1	2
Broadcom (NIC: 10Gb)	3, 1	2
Intel (NIC: 10Gb)	3, 1	2
Qlogic (NIC: 10Gb)	3, 1	2
Broadcom (NIC: 1Gb)	3, 1	2
Intel (NIC: 1Gb)	3, 1	2
Mellanox (NIC: HDR100 VPI)	3, 1	2
Mellanox (NIC: HDR VPI)	3, 1	2
Intel (OCP: 100Gb)	Integrated slot	1
Broadcom (OCP: 25Gb)	Integrated slot	1
Intel (OCP: 25Gb)	Integrated slot	1
Marvell (OCP: 25Gb)	Integrated slot	1
Mellanox (OCP: 25Gb)	Integrated slot	1
SolarFlare (OCP: 25Gb)	Integrated slot	1
Broadcom (OCP: 10Gb)	Integrated slot	1
Marvell (OCP: 10Gb)	Integrated slot	1
Intel (OCP: 10Gb)	Integrated slot	1
Broadcom (OCP: 1Gb)	Integrated slot	1
Intel (OCP: 1Gb)	Integrated slot	1



**Table 11. Configuration 3: R1+R2c (continued)**

Card type	Slot priority	Maximum number of cards
Dell External PERC Adapter	3, 1	2
Dell BOSS S1 Module	Integrated slot	1
Intel (PCIe SSD AIC)	3, 1	2
Samsung (PCIe SSD AIC)	3, 1	2

**Table 12. Configuration 4: R1**

Card type	Slot priority	Maximum number of cards
Dell Front PERC	Integrated slot	1
Dell Serial port module (LP)	1	1
Mellanox (NIC: 200Gb)	1	1
Intel (NIC: 100Gb)	1	1
Mellanox (NIC: 100Gb)	1	1
Broadcom (NIC: 25Gb)	1	1
Intel (NIC: 25Gb)	1	1
Mellanox (NIC: 25Gb)	1	1
Qlogic (NIC: 25Gb)	1	1
SolarFlare (NIC: 25Gb)	1	1
Broadcom (HBA: FC32)	1	1
Emulex (HBA: FC32)	1	1
Marvell (HBA: FC32)	1	1
Avago (HBA: FC16)	1	1
QLogic (HBA: FC16)	1	1
Broadcom (NIC: 10Gb)	1	1
Intel (NIC: 10Gb)	1	1
Qlogic (NIC: 10Gb)	1	1
Broadcom (NIC: 1Gb)	1	1
Intel (NIC: 1Gb)	1	1
Mellanox (NIC: HDR100 VPI)	1	1
Mellanox (NIC: HDR VPI)	1	1
Intel (OCP: 100Gb)	Integrated slot	1
Broadcom (OCP: 25Gb)	Integrated slot	1
Intel (OCP: 25Gb)	Integrated slot	1
Marvell (OCP: 25Gb)	Integrated slot	1
Mellanox (OCP: 25Gb)	Integrated slot	1
SolarFlare (OCP: 25Gb)	Integrated slot	1
Broadcom (OCP: 10Gb)	Integrated slot	1
Marvell (OCP: 10Gb)	Integrated slot	1
Intel (OCP: 10Gb)	Integrated slot	1

**Table 12. Configuration 4: R1 (continued)**

Card type	Slot priority	Maximum number of cards
Broadcom (OCP: 1Gb)	Integrated slot	1
Intel (OCP: 1Gb)	Integrated slot	1
Dell External PERC Adapter	1	1
Dell BOSS S1 Module	Integrated slot	1
Intel (PCIe SSD AIC)	1	1
Samsung (PCIe SSD AIC)	1	1

**Table 13. Configuration 5: No Riser**

Card type	Slot priority	Maximum number of cards
Intel (OCP: 100Gb)	Integrated slot	1
Broadcom (OCP: 25Gb)	Integrated slot	1
Intel (OCP: 25Gb)	Integrated slot	1
Marvell (OCP: 25Gb)	Integrated slot	1
Mellanox (OCP: 25Gb)	Integrated slot	1
SolarFlare (OCP: 25Gb)	Integrated slot	1
Broadcom (OCP: 10Gb)	Integrated slot	1
Intel (OCP: 10Gb)	Integrated slot	1
Marvell (OCP: 10Gb)	Integrated slot	1
Broadcom (OCP: 1Gb)	Integrated slot	1
Intel (OCP: 1Gb)	Integrated slot	1
Dell BOSS S1 Module	Integrated slot	1

**Table 14. Configuration 6: R2c**

Card type	Slot priority	Maximum number of cards
Dell Front PERC	Integrated slot	1
Dell Serial port module (LP)	2	1
Mellanox (NIC: 200Gb)	3	1
Intel (NIC: 100Gb)	3	1
Mellanox (NIC: 100Gb)	3	1
Broadcom (NIC: 25Gb)	3	1
Intel (NIC: 25Gb)	3	1
Mellanox (NIC: 25Gb)	3	1
Qlogic (NIC: 25Gb)	3	1
SolarFlare (NIC: 25Gb)	3	1
Emulex (HBA: FC32)	3	1
Broadcom (HBA: FC32)	3	1
Marvell (HBA: FC32)	3	1
Avago (HBA: FC16)	3	1
QLogic (HBA: FC16)	3	1

**Table 14. Configuration 6: R2c (continued)**

<b>Card type</b>	<b>Slot priority</b>	<b>Maximum number of cards</b>
Broadcom (NIC: 10Gb)	3	1
Intel (NIC: 10Gb)	3	1
Qlogic (NIC: 10Gb)	3	1
Broadcom (NIC: 1Gb)	3	1
Intel (NIC: 1Gb)	3	1
Mellanox (NIC: HDR100 VPI)	3	1
Mellanox (NIC: HDR VPI)	3	1
Intel (OCP: 100Gb)	Integrated slot	1
Broadcom (OCP: 25Gb)	Integrated slot	1
Intel (OCP: 25Gb)	Integrated slot	1
Marvell (OCP: 25Gb)	Integrated slot	1
Mellanox (OCP: 25Gb)	Integrated slot	1
SolarFlare (OCP: 25Gb)	Integrated slot	1
Broadcom (OCP: 10Gb)	Integrated slot	1
Marvell (OCP: 10Gb)	Integrated slot	1
Intel (OCP: 10Gb)	Integrated slot	1
Broadcom (OCP: 1Gb)	Integrated slot	1
Intel (OCP: 1Gb)	Integrated slot	1
Dell External PERC Adapter	3	1
Dell BOSS S1 Module	Integrated slot	1
Intel (PCIe SSD AIC)	3	1
Samsung (PCIe SSD AIC)	3	1

## Minimum configuration to POST

The components listed below are the minimum configuration to POST:

- One power supply unit
- System board
- One processor in socket processor 1
- Power Interposer Board (PIB) and cables
- One memory module (DIMM) installed in the socket A1

## PSU specifications

The PowerEdge R650xs system supports up to two AC or DC power supply units (PSUs).

**Table 15. PSU specifications**

PSU	Class	Heat dissipation (maximum)	Frequency	Voltage	AC		DC	Current
					High line 200–240 V	Low line 100–120 V		
1400 W Mixed Mode	Platinum	5,250 BTU/hr	50/60 Hz	100–240 V AC	1400 W	1050 W	NA	12 A–8 A
	NA	5,250 BTU/hr	NA	240 V DC	NA	NA	1400 W	6.6 A
1100 W Mixed Mode	Titanium	4,125 BTU/hr	50/60 Hz	100–240 V AC	1100 W	1050 W	NA	12 A–6.3 A
	NA	4,125 BTU/hr	NA	240 V DC	NA	NA	1100 W	5.2 A
1100 W DC	NA	4,265 BTU/hr	NA	-48–(-60) V	NA	NA	1100 W	27 A
800 W Mixed Mode	Platinum	3,000 BTU/hr	50/60 Hz	100–240 V AC, autoranging	800 W	800 W	NA	9.2 A–4.7 A
	NA	3,000 BTU/hr	NA	240 V DC	NA	NA	800 W	3.8 A
600 W Mixed Mode	Platinum	2,250 BTU/hr	50/60 Hz	100–240 V AC, autoranging	600 W	600 W	NA	7.1 A–3.6 A
	NA	2,250 BTU/hr	NA	240 V DC	NA	NA	600 W	2.9 A
700 W Mixed Mode	Titanium	2,625 BTU/hr	50/60 Hz	200–240 V AC	700 W	NA	NA	4.1 A
	NA	2,625 BTU/hr	NA	240 V DC	NA	NA	700 W	3.4 A
1800 W Mixed Mode	Titanium	6,000 BTU/hr	50/60 Hz	200–240 V AC	1800 W	NA	NA	10 A
	NA	6000 BTU/hr	NA	240 V DC	NA	NA	1800 W	8.2 A

**NOTE:** This system is also designed to connect to the IT power systems with a phase-to-phase voltage not exceeding 240 V.

**NOTE:** Heat dissipation is calculated using the PSU wattage rating.

**NOTE:** When selecting or upgrading the system configuration, to ensure optimum power utilization, verify the system power consumption with the Dell Energy Smart Solution Advisor available at [Dell.com/ESSA](https://www.dell.com/ESSA).