

Statement of Volatility – Dell Precision 3571

⚠ CAUTION: A CAUTION indicates either potential damage to hardware or loss of data and tells you how to avoid the problem.

The Dell Precision 3571 contains both volatile and non-volatile components. Volatile components lose their data immediately after power is removed from the component. Non-volatile components continue to retain their data even after power is removed from the component. The following Non-volatile components are present on the Precision 3571 system board.

Table 1. List of Non-Volatile Components on System Board

Description	Reference designator	Volatility description	User accessible for external data	Remedial action (action necessary to prevent loss of data)
Panel EEDID EEPROM	Part of panel assembly	Non-Volatile memory, 128 bytes.	No	Part of panel assembly
System BIOS	U2501	Non-Volatile memory, System BIOS, embedded controller and Video BIOS for basic boot operation, PSA (on board diags), PXE diags.	No	N/A
System Memory – DDR5 memory	Two SODIMM connectors: JDIMM1, 2 presents	Volatile memory in OFF state NOTE: See state definitions later in text. One to Two modules must be populated.	Yes	Power off system
System memory SPD EEPROM	On System memory SODIMM(s) JDIMM1,2 present	Non-Volatile memory 512 Bytes. Stores memory manufacturer data and timing information for correct operation of system memory.	No	N/A
RTC CMOS – BBRAM (battery backed up)	PCH1	Non-Volatile memory, 256 Bytes. Stores CMOS information.	No	Remove the onboard coin cell battery
Video memory – frame buffer	For UMA platform: using system DDR5 For DSC platform: VRAM1, VRAM2	Volatile memory in off state. UMA uses main system memory size allocated out of main memory. Discrete graphics system uses 2 GB GDDR6.	No	
Security Controller Serial Flash Memory	U401 (up-sell USH daughter board)	Non-Volatile memory, 128 Mbit (16 Mbyte)	No	N/A
Hard drive(s)	User replaceable	SSD (solid State flash drive). various sizes in GB	Yes	Low level format
TPM Controller	U9101	Non Volatile memory, 24 K bytes flash memory	No	N/A

Description	Reference designator	Volatility description	User accessible for external data	Remedial action (action necessary to prevent loss of data)
Thunderbolt controller FW	U7103	Non Volatile memory, 8 M bits (1M bytes) flash memory	No	N/A

△ CAUTION: All other components on the system board lose data if power is removed from the system. Primary power loss (unplugging the power cord and removing the battery) destroys all user data on the memory (DDR5, 4800 MHz). Secondary power loss (removing the on-board coin-cell battery) destroys system data on the system configuration and time-of-day information.

In addition, to clarify memory volatility and data retention in situations where the system is put in different ACPI power states the following is provided (those ACPI power states are S0, S1, S3, S4 and S5):

S0 state is the working state where the dynamic RAM is maintained and is read/write by the processor.

S1 state is a low wake-up latency sleeping state. In this state, the context computer is not lost (CPU or chip set) and hardware maintains all computer contexts.

S3 is called **suspend to RAM** state or stand-by mode. In this state the dynamic RAM is maintained. Dell systems will be able to go to S3 if the OS and the peripherals used in the system supports S3 state. Windows operating system support S3 state.

S4 is called **suspend to disk** state or **hibernate** mode. There is no power. In this state, the dynamic RAM is not maintained. If the system has been commanded to enter S4, the OS will write the system context to a non-volatile storage file and leave appropriate context markers. When the system is coming back to the working state, a restore file from the nonvolatile storage can occur. The restore file must be valid. Dell systems will be able to go to S4 if the OS and the peripherals support S4 state. Windows operating system support S4 state.

S5 is the **soft off** state. There is no power. The OS does not save any context to wake up the system. No data will remain in any component on the system board, i.e., cache or memory. The system will require a complete boot when awakened. Since S5 is the shut off state, coming out of S5 requires power on which clears all registers.