GIGABYTE™

MC12-LE0

AMD Ryzen™ 5000 Server Motherboard

User Manual

Rev. 1.0

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Documentation Classifications

In order to assist in the use of this product, GIGABYTE provides the following types of documentation:

- User Manual: detailed information & steps about the installation, configuration and use this product (e.g. motherboard, server barebones), covering hardware and BIOS.
- User Guide: detailed information about the installation & use of an add-on hardware or software component (e.g. BMC firmware, rail-kit) compatible with this product.
- Quick Installation Guide: a short guide with visual diagrams that you can reference easily for installation purposes of this product (e.g. motherboard, server barebones).

Please see the support section of the online product page to check the current availability of these documents

For More Information

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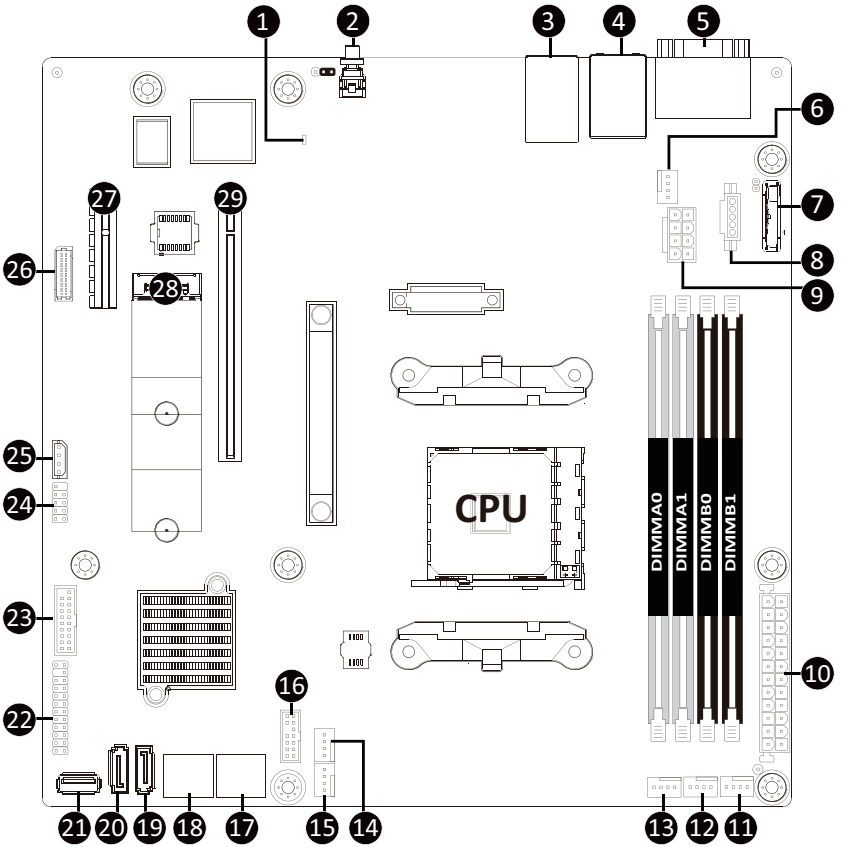
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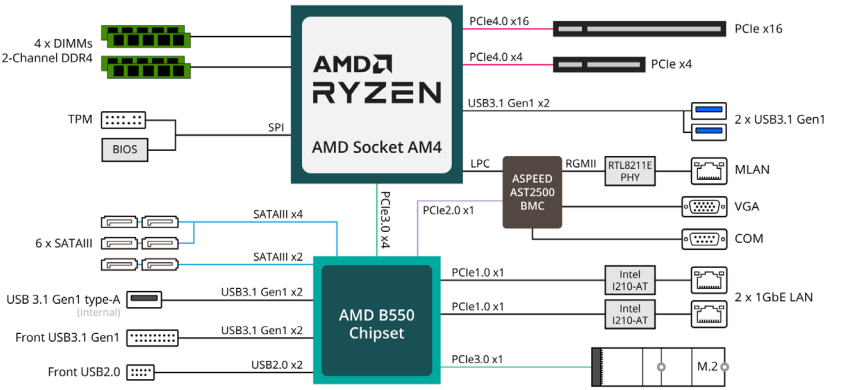
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MC12-LE0 Motherboard Layout



Item	Code	Description
1	LED_BMC	BMC Firmware Readiness LED
2	SW_ID	ID Button with LED
3	LAN3_4	GbE LAN Port #3/#4
4	USB31_MLAN	Server Management LAN Port (Top)/USB 3.1 Ports (Bottom)
5	COM_VGA	Serial Port (Top)/VGA Port (Bottom)
6	CPU_FAN	CPU Fan Connector
7	BATTERY	Battery Socket
8	PMBUS	PMBus Connector
9	ATX_12V	2x4 Pin 12V Power Connector
10	ATX	2x12 Pin Main Power Connector
11	SYS_FAN5	System Fan Connector #5
12	SYS_FAN4	System Fan Connector #4
13	SYS_FAN3	System Fan Connector #3
14	SYS_FAN2	System Fan Connector #2
15	SYS_FAN1	System Fan Connector #1
16	SPI_TPM	TPM Connector
17	SATA_0_1	SATA III 6Gb/s Connector #0/#1
18	SATA_2_3	SATA III 6Gb/s Connector #2/#3
19	SATA5	SATA III 6Gb/s Connector #5
20	SATA4	SATA III 6Gb/s Connector #4
21	USB_A	Vertical USB 3.1 Connector
22	FP_1	Front Panel Header
23	F_USB3_1	Front Panel USB 3.1 Connector
24	F_USB2_1	Front Panel USB 2.0 Connector
25	IPMB	IPMB Connector
26	BP_1	HDD Back Plane Board Connector
27	PCIEX4	PCIe x4 Slot (Gen4 x4)
28	M2M_1	M.2 Slot (PCIe Gen3 x1, Support NGFF-2280)
29	PCIEX16	PCIe x16 Slot (Gen4 x16)

Block Diagram



Chapter 1 Hardware Installation

1-1 Installation Precautions

The motherboard contains numerous delicate electronic circuits and components which can become damaged as a result of electrostatic discharge (ESD). Prior to installation, carefully read the user's manual and follow these procedures:

- Prior to installation, do not remove or break motherboard S/N (Serial Number) sticker or warranty sticker provided by your dealer. These stickers are required for warranty validation.
- Always remove the AC power by unplugging the power cord from the power outlet before installing or removing the motherboard or other hardware components.
- When connecting hardware components to the internal connectors on the motherboard, make sure they are connected tightly and securely.
- When handling the motherboard, avoid touching any metal leads or connectors.
- It is best to wear an electrostatic discharge (ESD) wrist strap when handling electronic components such as a motherboard, CPU or memory. If you do not have an ESD wrist strap, keep your hands dry and first touch a metal object to eliminate static electricity.
- Prior to installing the motherboard, please have it on top of an antistatic pad or within an electrostatic shielding container.
- Before unplugging the power supply cable from the motherboard, make sure the power supply has been turned off.
- Before turning on the power, make sure the power supply voltage has been set according to the local voltage standard.
- Before using the product, please verify that all cables and power connectors of your hardware components are connected.
- To prevent damage to the motherboard, do not allow screws to come in contact with the motherboard circuit or its components.
- Make sure there are no leftover screws or metal components placed on the motherboard or within the computer casing.
- Do not place the computer system on an uneven surface.
- Do not place the computer system in a high-temperature environment.
- Turning on the computer power during the installation process can lead to damage to system components as well as physical harm to the user.
- If you are uncertain about any installation steps or have a problem related to the use of the product, please consult a certified computer technician.
- To avoid any potential short circuit of the DIMM slots, please remove any stand-offs from the chassis that will be located underneath the DIMM slots, before installing the motherboard into the chassis.

1-2 Product Specifications

NOTE:
We reserve the right to make any changes to the product specifications and product-related information without prior notice.

Form Factor	◆ microATX ◆ 244W x 244D (mm)
CPU	◆ AMD Ryzen™ 5000 Series/ 3rd Gen Ryzen™ Processors ◆ AMD 3rd Gen Ryzen™ with Radeon™ Graphics Processors
Chipset	◆ AMD B550
Memory	◆ 4 x DDR4 DIMM sockets supporting up to 128 GB (32 GB single DIMM capacity) of system memory ◆ Dual channel memory architecture ◆ Supported ECC Un-buffered DIMM 1Rx8/2Rx8 memory modules ◆ Supported non-ECC Un-buffered DIMM 1Rx8/2Rx8/1Rx16 memory modules ◆ Memory speed: Up to 3200/ 2933/ 2667/ 2400/ 2133 MHz
LAN	◆ 2 x 1GbE LAN ports (Intel® I210-AT) ◆ 1 x 10/100/1000 management LAN
Onboard Graphics	◆ Integrated in Aspeed® AST2500 ◆ 2D Video Graphic Adapter with PCIe bus interface ◆ 1920x1200@60Hz 32bpp,
Storage Interface	◆ 6 x SATA 6Gb/s ports
RAID	◆ RAID 0, RAID 1, and RAID 10
Expansion Slots	◆ Slot_6 (PCIEX16): 1 x PCIe x16 (Gen4 x16 bus) slot from CPU ◆ Slot_4 (PCIEX4): 1 x PCIe x4 (Gen4 x4 bus) slot from CPU ◆ 1 x M.2 slot: - M-key - PCIe Gen3 x1 per slot - Supports NGFF-2242/2280 cards

Internal I/O Connectors	◆ 1 x 24-pin ATX main power connector ◆ 1 x 8-pin ATX 12V power connector ◆ 6 x SATA ports ◆ 1 x M.2 slot ◆ 1 x HDD back plane board header ◆ 1 x CPU fan header ◆ 5 x System fan headers ◆ 1 x USB 3.1 Gen1 header with 2-port ◆ 1 x USB 3.1 Gen1 type-A connector ◆ 1 x USB 2.0 header with 2-port ◆ 1 x TPM header ◆ 1 x Front panel header ◆ 1 x IPMB connector ◆ 1 x PMBUS connector ◆ 1 x Clear CMOS jumper
Rear I/O Connectors	◆ 2 x USB 3.1 Gen1 ◆ 1 x VGA ◆ 1 x COM ◆ 2 x RJ45 ◆ 1 x MLAN ◆ 1 x ID button with LED
TPM	◆ 1 x TPM Header with SPI Interface ◆ Optional TPM2.0 kit: CTM010
Board Management	◆ Aspeed® AST2500 Management Controller ◆ GIGABYTE Management Console (AMI MegaRAC SP-X) Web Interface
Operating Properties	◆ Operating temperature: 10°C to 40°C ◆ Operating humidity: 8-80% (non-condensing) ◆ Non-operating temperature: -40°C to 60°C ◆ Non-operating humidity: 20%-95% (non-condensing)

1-3 Installing and Removing the CPU

Read the following guidelines before you begin to install the CPU:

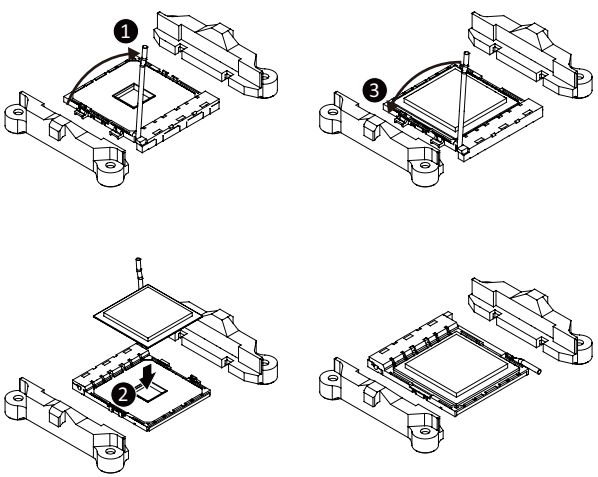
- Make sure that the motherboard supports the CPU.
- Always turn off the computer and unplug the power cord from the power outlet before installing the CPU to prevent hardware damage.
- Unplug all cables from the power outlets.
- Disconnect all telecommunication cables from their ports.
- Place the system unit on a flat and stable surface.
- Open the system according to the instructions.

WARNING!

Failure to properly turn off the server before you start installing components may cause serious damage. Do not attempt the procedures described in the following sections unless you are a qualified service technician.

Follow these instructions to Install the CPU:

1. Lift up the CPU socket locking lever.
2. Align the CPU pin one (triangle marking) with the pin one corner of the CPU socket. Install the CPU onto the socket.
3. Ensure the CPU is positioned into its socket and secure the CPU socket lever.



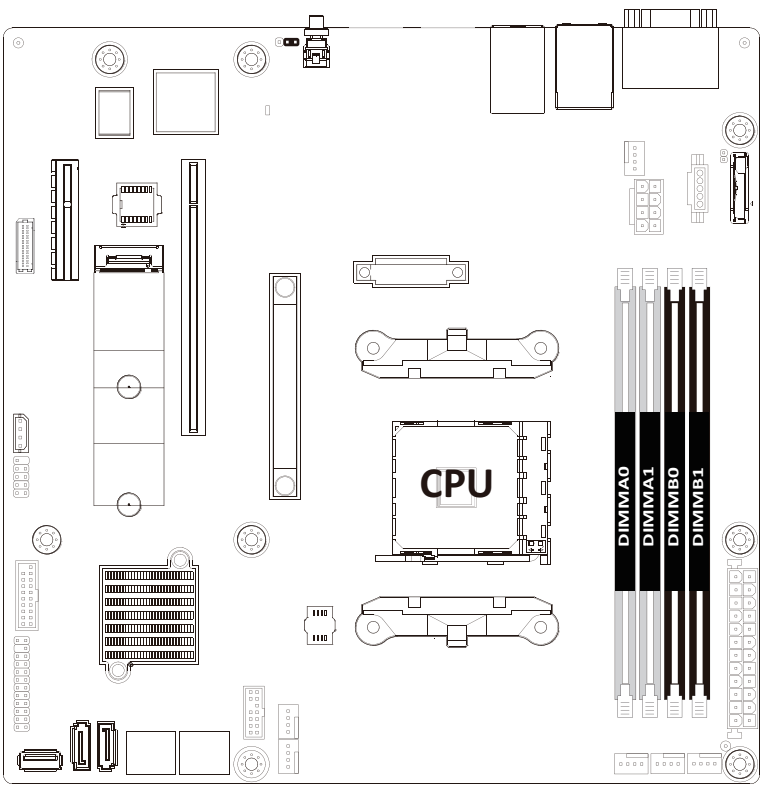
1-4 Installing and Removing Memory

Read the following guidelines before you begin to install the memory:

- Make sure that the motherboard supports the memory. It is recommended to use memory of the same capacity, brand, speed, and chips.
- Always turn off the computer and unplug the power cord from the power outlet before installing the memory to prevent hardware damage.
- Memory modules have a foolproof design. A memory module can be installed in only one direction. If you are unable to insert the memory, switch the direction.

1-4-1 2-Channel Memory Configuration

This motherboard provides 4 DDR4 memory sockets and supports 2-Channel Technology. After the memory is installed, the BIOS will automatically detect the specifications and capacity of the memory.

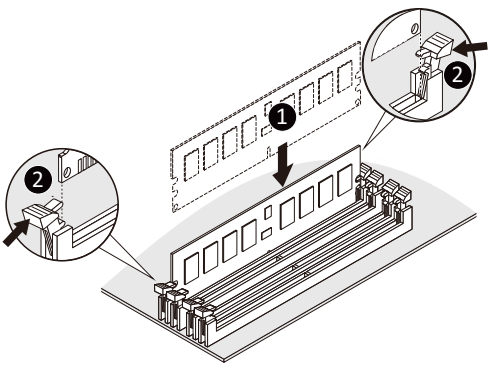


1-4-2 Installing and Removing a Memory Module

Before installing a memory module, make sure to turn off the computer and unplug the power cord from the power outlet to prevent damage to the memory module.
Be sure to install DDR4 DIMMs on this motherboard.

Follow these instructions to install a DIMM module:

1. Insert the DIMM memory module vertically into the DIMM slot and push it down.
2. Close the plastic clip at both edges of the DIMM slots to lock the DIMM module.
3. Reverse the installation steps when you want to remove the DIMM module.



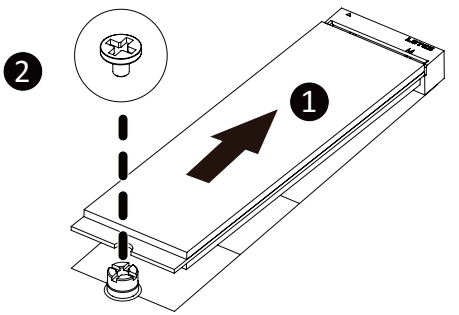
Note: To avoid any potential short circuit of the DIMM slots, please remove any stand-offs from the chassis that will be located underneath the DIMM slots, before installing the motherboard into the chassis.

Memory Type	DDR4	
Voltage (V)	1.2V	
Connector	UDIMM ECC/Non-ECC	
Speed (MT/s)	3200	2133
Channels	1,2	
DIMM Per Channel	1,2	
DIMM Capacity (GB)	2,4,8,16,32	

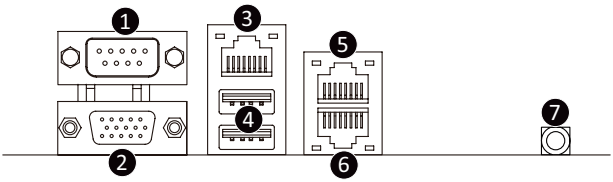
1-5 Installing the M.2 SSD Module

Follow the steps below to install a M.2 SSD module on your motherboard.

Step1. Insert the M.2 SSD module into the slot.
Step2. Secure it with the screw, tightening as necessary to fasten the M.2 SSD module in place.



1-6 Back Panel Connectors



❶ Serial Port

Connects to serial-based mouse or data processing devices.

❷ VGA Port

Connect to a monitor device.

❸ Server Management LAN Port

The LAN port provides Internet connection with data transfer speeds of 10/100/1000Mbps. This port is the dedicated LAN port for Server Management.

❹ USB 3.1 Ports

The USB port supports the USB 3.1 specification. Use this port for USB devices such as a USB keyboard/mouse, USB printer, USB flash drive etc.

❺ GbE LAN Port #3

The Gigabit Ethernet LAN port provides Internet connection at up to 1 Gbps data rate. See the section below for a description of the states of the LAN port LEDs.

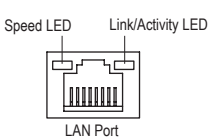
❻ GbE LAN Port #4

The Gigabit Ethernet LAN port provides Internet connection at up to 1 Gbps data rate. See the section below for a description of the states of the LAN port LEDs.

❼ ID button with LED

When the system identification is active, the ID LED on the front/ back panel glows blue.

LAN and ID Button LEDs



10/100/1000 LAN LED:

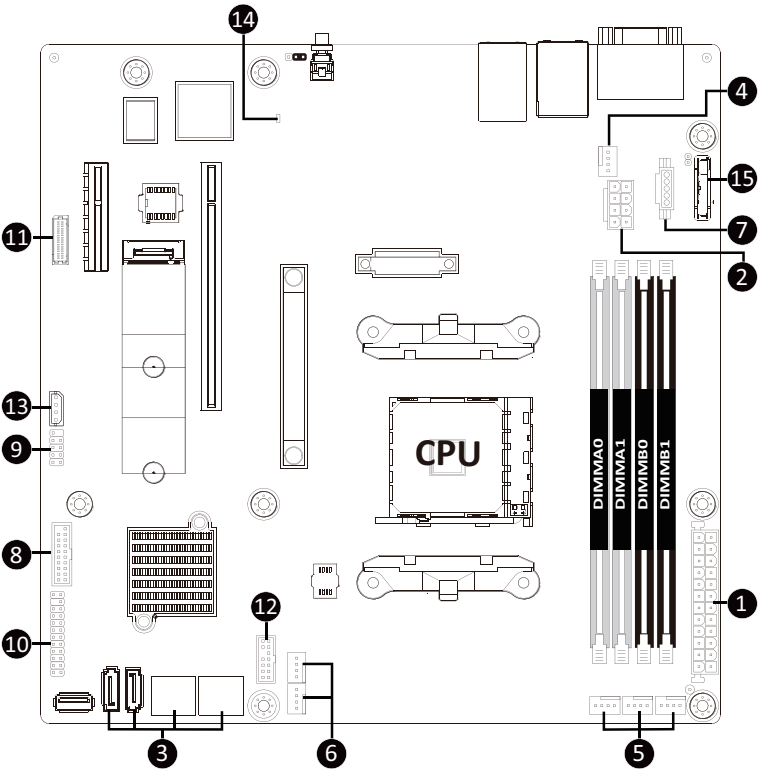
State	Description
Yellow On	1Gbps data rate
Green On	100Mbps data rate
Off	10Mbps data rate

ID button/LED:

State	Description
Blue On	System identification is active
Off	System identification is disabled

- When removing the cable connected to a back panel connector, first remove the cable from your device and then remove it from the motherboard.
- When removing the cable, pull it straight out from the connector. Do not rock it side to side to prevent an electrical short inside the cable connector.

1-7 Internal Connectors



1)	ATX	11)	BP_1
2)	ATX_12V	12)	SPI_TPM
3)	SATA_0_1/SATA_2_3/SATA4/SATA5	13)	IPMB
4)	CPU_FAN	14)	LED_BMC
5)	SYS_FAN3/4/5	15)	BATTERY
6)	SYS_FAN1/2		
7)	PMBUS		
8)	F_USB3_1		
9)	F_USB2_1		
10)	FP_1		

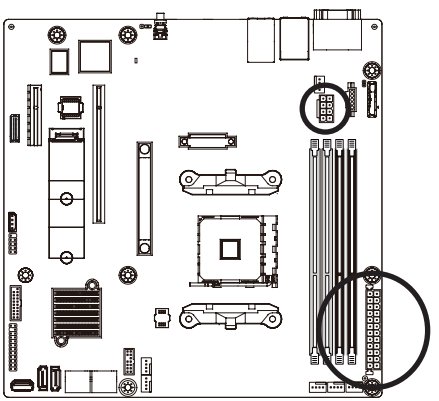
Read the following guidelines before connecting external devices:

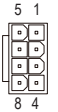
- First make sure your devices are compliant with the connectors you wish to connect.
- Before installing the devices, be sure to turn off the devices and your computer. Unplug the power cord from the power outlet to prevent damage to the devices.
- After installing the device and before turning on the computer, make sure the device cable has been securely attached to the connector on the motherboard.

1/2) ATX/ATX_12V (2x12 Main Power Connector and 2x4 12V Power Connector)

With the use of the power connector, the power supply can supply enough stable power to all the components on the motherboard. Before connecting the power connector, first make sure the power supply is turned off and all devices are properly installed. The power connector possesses a foolproof design. Connect the power supply cable to the power connector in the correct orientation. The 12V power connector mainly supplies power to the CPU. If the 12V power connector is not connected, the computer will not start.

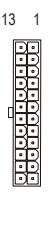
To meet expansion requirements, it is recommended that a power supply that can withstand high power consumption be used (500W or greater). If a power supply is used that does not provide the required power, the result can lead to an unstable or unbootable system.





ATX_12V

Pin No.	Definition
1	GND
2	GND
3	GND
4	GND
5	+12V
6	+12V
7	+12V
8	+12V

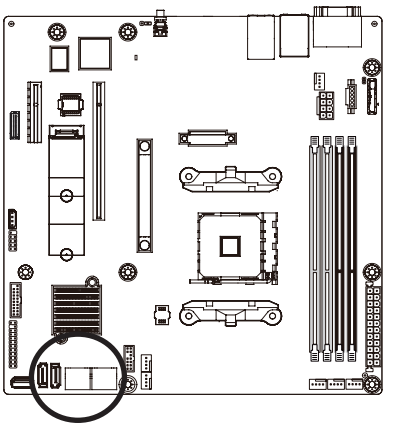


ATX

Pin No.	Definition	Pin No.	Definition
1	3.3V	13	3.3V
2	3.3V	14	-12V
3	GND	15	GND
4	+5V	16	PS_ON
5	GND	17	GND
6	+5V	18	GND
7	GND	19	GND
8	Power Good	20	NC
9	5VSB	21	+5V
10	+12V	22	+5V
11	+12V	23	+5V
12	3.3V	24	GND

3) SATA_0_1/SATA_2_3/SATA4/SATA5 (SATA III 6Gb/s Connectors)

The SATA connectors conform to SATA III 6Gb/s standard and are compatible with SATA 3Gb/s standard. Each SATA connector supports a single SATA device.

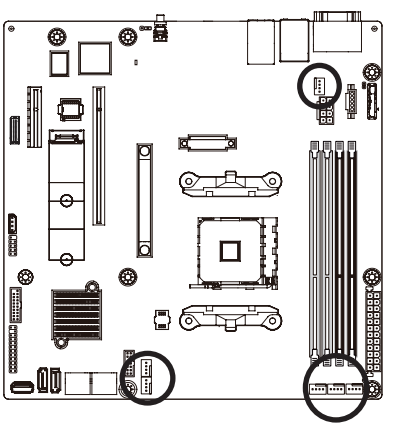


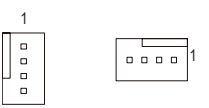


Pin No.	Definition
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

4/5/6) CPU_FAN/SYS_FAN1/SYS_FAN2/SYS_FAN3/SYS_FAN4/SYS_FAN5 (Fan Headers)

The motherboard has one 4-pin CPU fan header (CPU_FAN), and two 4-pin (SYS_FAN) system fan headers. Most fan headers possess a foolproof insertion design. When connecting a fan cable, be sure to connect it in the correct orientation (the black connector wire is the ground wire). The motherboard supports CPU fan speed control, which requires the use of a CPU fan with fan speed control design. For optimum heat dissipation, it is recommended that a system fan be installed inside the chassis.



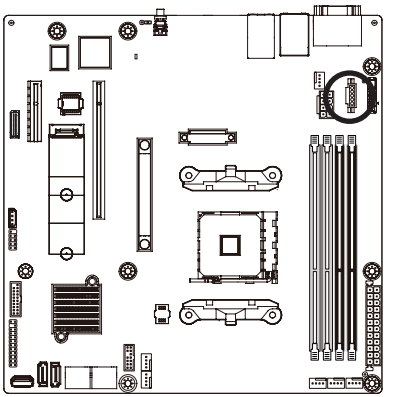


Pin No.	Definition
1	GND
2	+12V
3	FAN_TACH
4	FAN_PWM

- Be sure to connect fan cables to the fan headers to prevent your CPU and system from overheating. Overheating may result in damage to the CPU or the system may hang.
- These fan headers are not configuration jumper blocks. Do not place a jumper cap on the headers.

7) PMBus Connector

The Power Management Bus (PMBus) is a variant of the System Management Bus (SMBus) which is targeted at digital management of power supplies.

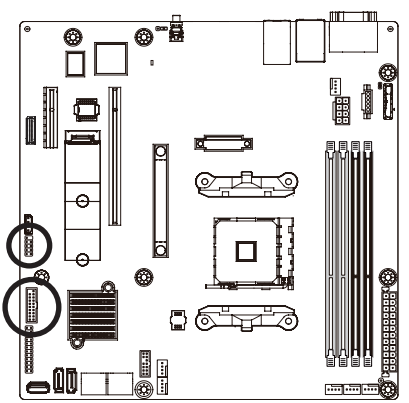




Pin No.	Definition
1	PMBus Clock
2	PMBus Data
3	PMBus Alert
4	GND
5	3.3V Sense

8/9) F_USB3_1/F_USB2_1 (Front Panel USB 3.1/2.0 Connector)

The header conform to USB 3.1/2.0 specification. Each USB header can provide two USB ports via an optional USB bracket. For purchasing the optional USB bracket, please contact the local dealer.



USB 2.0 Header



Pin No.	Definition	Pin No.	Definition
1	Power (5V)	6	USB DY+
2	Power (5V)	7	GND
3	USB DX-	8	GND
4	USB DY-	9	No Pin
5	USB DX+	10	No Connect

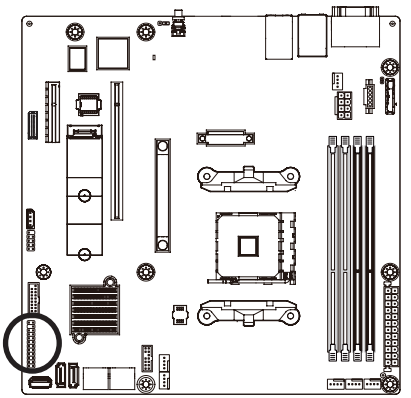
USB 3.1 Connector



Pin No.	Definition	Pin No.	Definition
1	Power	11	IntA_P2_D+
2	IntA_P1_SSRX-	12	IntA_P2_D-
3	IntA_P1_SSRX+	13	GND
4	GND	14	IntA_P2_SSTX+
5	IntA_P1_SSTX-	15	IntA_P2_SSTX-
6	IntA_P1_SSTX+	16	GND
7	GND	17	IntA_P2_SSRX+
8	IntA_P1_D-	18	IntA_P2_SSRX-
9	IntA_P1_D+	19	Power
10	NC	20	No Pin

10) FP_1 (Front Panel Header)

Connect the power switch, reset switch, speaker, chassis intrusion switch/sensor and system status indicator on the chassis to this header according to the pin assignments below. Note the positive and negative pins before connecting the cables.

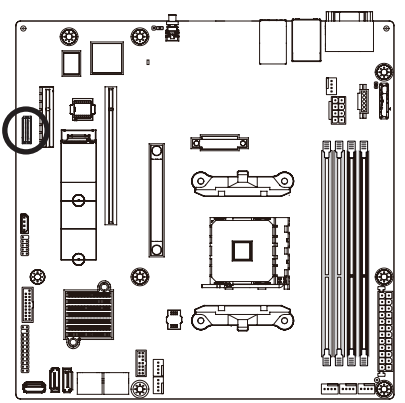




Pin No.	Definition	Pin No.	Definition
1	Power LED+	2	5V Standby
3	No Pin	4	ID LED+
5	Power LED-	6	ID LED-
7*	HDD LED+	8	System Status LED+
9*	HDD LED-	10	System Status LED-
11	Power Button	12	LAN1 Active LED+
13	GND	14	LAN1 Link LED-
15	Reset Button	16	SMBus Data
17	GND	18	SMBus Clock
19	ID Button	20	Case Open
21	GND	22	LAN2 Actve LED+
23	NMI Switch	24	LAN2 Link LED-
*Note: Pin 7 & Pin 9 are reserved for Gigabyte systems.			

The front panel design may differ by chassis. A front panel module mainly consists of power switch, reset switch, power LED, hard drive activity LED, speaker etc. When connecting your chassis front panel module to this header, make sure the wire assignments and the pin assignments are matched correctly.

11) BP_1 (HDD Backplane Board Connector)

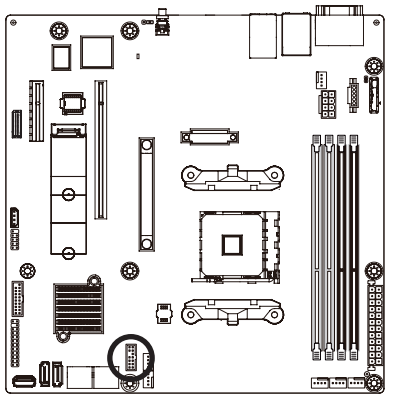




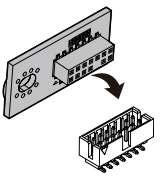
Pin No.	Definition	Pin No.	Definition
1	Reserved	2	BPMI DIN/OUT
3	GND	4	BPMI DOUT/IN
5	BPMI_LOAD	6	GND
7	BPMI_CLK	8	PLD_Program_EN
9	GLED_AMB_N	10	GLED_GRN_N
11	FAN_IRQ_N	12	Reserved
13	BP_SCL	14	GND
15	BP_SDA	16	BP_RST_N
17	SMB_U2_TMP_SCL	18	GND
19	SMB_U2_TMP_SDA	20	12C_DEV_RST
21	NC	22	GND
23	NC	24	GND
25	NC	26	GND
27	NC	28	GND
29	P3V3_AUX	30	P3V3_AUX

12) TPM (Trusted Platform Module Connector)

Trusted Platform Module (TPM) is an international standard for a secure cryptoprocessor, a dedicated microcontroller designed to secure hardware through integrated cryptographic keys.



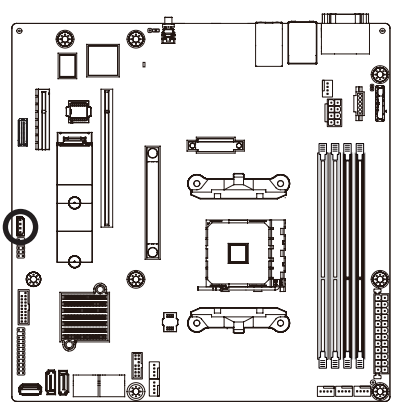




Pin No.	Definition	Pin No.	Definition
1	SPI_PCH_TPM_R_CLK	8	No Connect
2	A_VDD18S5	9	No Connect
3	LPC_RST_N	10	No Pin
4	NC	11	No Connect
5	SPI_PCH_TPM_R_MISO	12	GND
6	IRQ_TPM_SPI_R_N	13	SPI_PCH_TPM_R_CS_N
7	SPI_PCH_TPM_R_MOSI	14	GND

13) IPMB (Intelligent Platform Management Bus) Connector

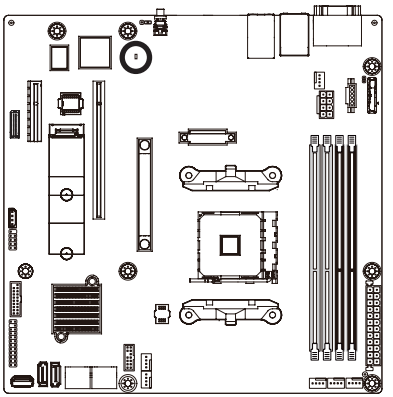
The Intelligent Platform Management Bus Communications Protocol defines a byte-level transport for transferring Intelligent Platform Management Interface Specification (IPMI) messages between intelligent I2C devices.





Pin No.	Definition
1	Clock
2	GND
3	Data
4	VCC

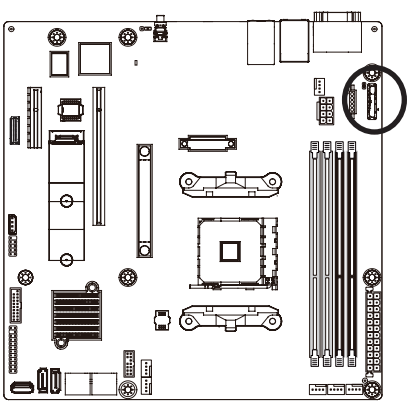
14) LED_BMC (BMC Firmware Readiness LED)

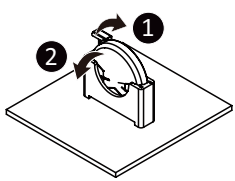


State	Description
On	BMC firmware is initial
Blink	BMC firmware is ready
Off	AC loss

19) BATTERY (Battery Socket)

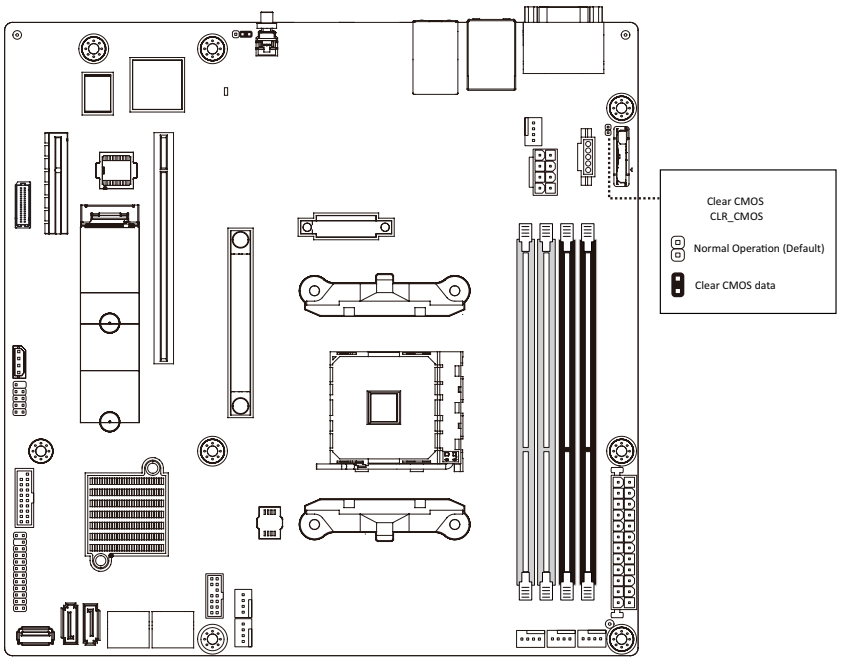
The battery provides power to keep the values (such as BIOS configurations, date, and time information) in the CMOS when the computer is turned off. Replace the battery when the battery voltage drops to a low level, or the CMOS values may not be accurate or may be lost.





- Always turn off your computer and unplug the power cord before replacing the battery.
- Replace the battery with an equivalent one. Danger of explosion if the battery is replaced with an incorrect model.
- Contact the place of purchase or local dealer if you are not able to replace the battery by yourself or uncertain about the battery model.
- Used batteries must be handled in accordance with local environmental regulations.

1-8 Jumper Settings



Chapter 2 BIOS Setup

BIOS (Basic Input and Output System) records hardware parameters of the system in the EFI on the motherboard. Its major functions include conducting the Power-On Self-Test (POST) during system startup, saving system parameters, loading the operating system etc. The BIOS includes a BIOS Setup program that allows the user to modify basic system configuration settings or to activate certain system features. When the power is turned off, the battery on the motherboard supplies the necessary power to the CMOS to keep the configuration values in the CMOS.

To access the BIOS Setup program, press the <DEL> key during the POST when the power is turned on.



- BIOS flashing is potentially risky, if you do not encounter any problems when using the current BIOS version, it is recommended that you don't flash the BIOS. To flash the BIOS, do it with caution. Inadequate BIOS flashing may result in system malfunction.
- It is recommended that you not alter the default settings (unless you need to) to prevent system instability or other unexpected results. Inadequately altering the settings may result in system's failure to boot. If this occurs, try to clear the CMOS values and reset the board to default values. (Refer to the **Exit** section in this chapter or introductions of the battery/clearing CMOS jumper in Chapter 1 for how to clear the CMOS values.)

BIOS Setup Program Function Keys

<←><→>	Move the selection bar to select the screen
<↑><↓>	Move the selection bar to select an item
<+>	Increase the numeric value or make changes
<->	Decrease the numeric value or make changes
<Enter>	Execute command or enter the submenu
<Esc>	Main Menu: Exit the BIOS Setup program Submenus: Exit current submenu
<F1>	Show descriptions of general help
<F3>	Restore the previous BIOS settings for the current submenus
<F9>	Load the Optimized BIOS default settings for the current submenus
<F10>	Save all the changes and exit the BIOS Setup program

- **Main**
  This setup page includes all the items of the standard compatible BIOS.
- **Advanced**
  This setup page includes all the items of AMI BIOS special enhanced features.
  (ex: Auto detect fan and temperature status, automatically configure hard disk parameters.)
- **Chipset**
  This setup page includes all the submenu options for configuring the functions of the North Bridge.
- **Server Management**
  Server additional features enabled/disabled setup menus.
- **Security**
  Change, set, or disable supervisor and user password. Configuration supervisor password allows you to restrict access to the system and BIOS Setup.
  A supervisor password allows you to make changes in BIOS Setup.
  A user password only allows you to view the BIOS settings but not to make changes.
- **Boot**
  This setup page provides items for configuration of the boot sequence.
- **Save & Exit**
  Save all the changes made in the BIOS Setup program to the CMOS and exit BIOS Setup. (Pressing <F10> can also carry out this task.)
  Abandon all changes and the previous settings remain in effect. Pressing <Y> to the confirmation message will exit BIOS Setup. (Pressing <Esc> can also carry out this task.)

2-1 The Main Menu

Once you enter the BIOS Setup program, the Main Menu (as shown below) appears on the screen. Use arrow keys to move among the items and press <Enter> to accept or enter other sub-menu.

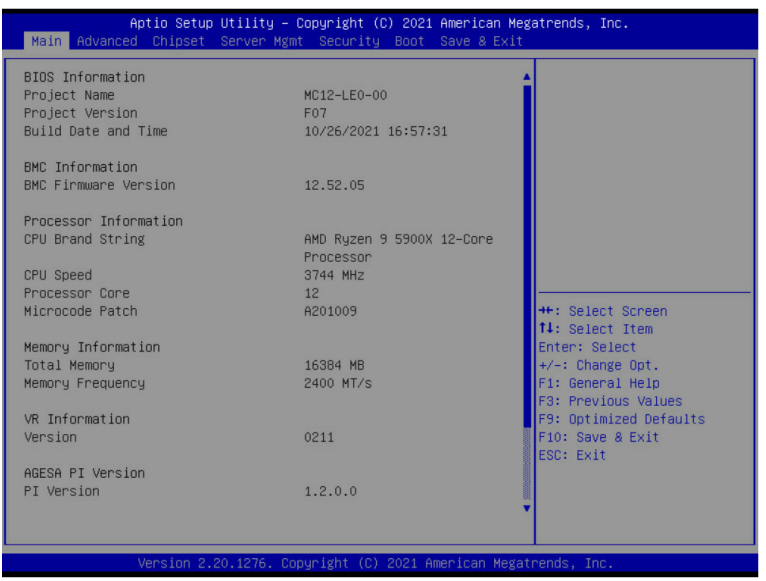
Main Menu Help

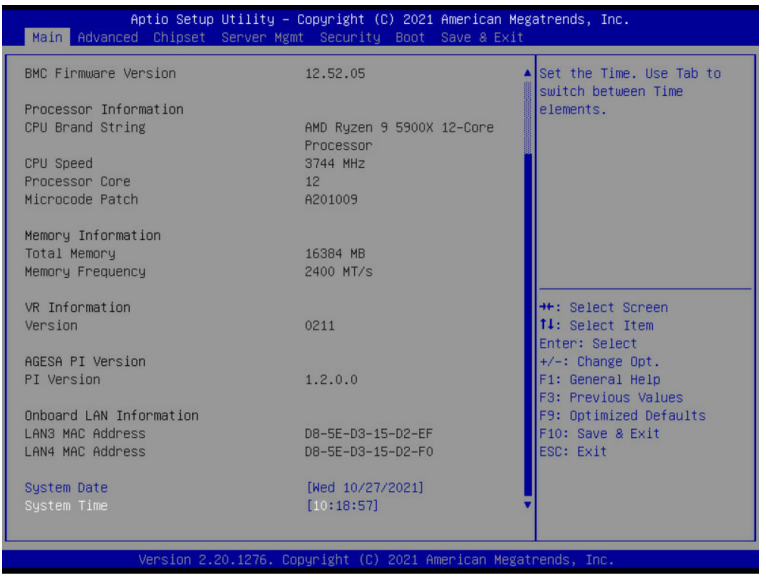
The on-screen description of a highlighted setup option is displayed on the bottom line of the Main Menu.

Submenu Help

While in a submenu, press <F1> to display a help screen (General Help) of function keys available for the menu. Press <Esc> to exit the help screen. Help for each item is in the Item Help block on the right side of the submenu.

- When the system is not stable as usual, select the **Restore Defaults** item to set your system to its defaults.
- The BIOS Setup menus described in this chapter are for reference only and may differ by BIOS version.





Parameter	Description
BIOS Information	
Project Name	Displays the project name information.
Project Version	Displays version number of the BIOS setup utility.
Build Date and Time	Displays the date and time when the BIOS setup utility was created.
BMC Information(Note1)	
BMC Firmware Version(Note1)	Displays BMC firmware version information.
Processor Information	
CPU Brand String/ CPU Speed / Processor Core / Microcode Patch	Displays the technical specifications for the installed processor(s).
Memory Information	
Total Memory(Note2)	Displays the total memory size of the installed memory.
Memory Frequency(Note2)	Displays the frequency information of the installed memory.
VR Information Version	Displays the VR version information.
AGESA PI Version	
PI Version	Displays AGESA PI version information.

(Note1) Functions available on selected models.

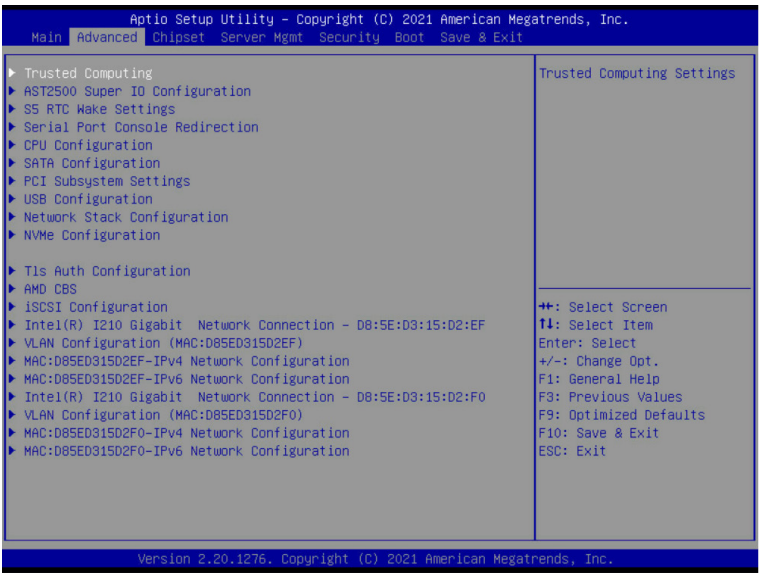
(Note2) This section will display capacity and frequency information of the memory that the customer has installed.

Parameter	Description
Onboard LAN Information	
LAN3 MAC Address(Note)	Displays LAN MAC address information.
LAN4 MAC Address (Note)	Displays LAN MAC address information.
System Date	Sets the date following the weekday-month-day-year format.
System Time	Sets the system time following the hour-minute-second format.

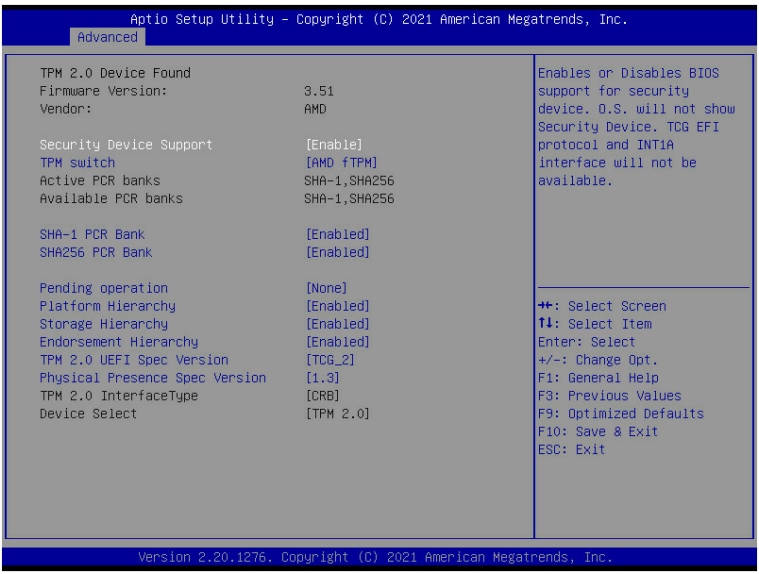
(Note) The number of LAN ports listed will depend on the motherboard / system model.

2-2 Advanced Menu

The Advanced Menu displays submenu options for configuring the function of various hardware components. Select a submenu item, then press <Enter> to access the related submenu screen.



2-2-1 Trusted Computing



Parameter	Description
TPM20 Device Found	
Firmware Version	Displays the firmware version information.
Vendor	Displays the vendor information.
Security Device Support	Enable/Disable BIOS support for security device. OS will not show security device. TCG EFI protocol and INT1A interface will not be available. Options available: Enable, Disable. Default setting is **Enable**.
TPM Switch	Options available: AMD fTPM, SPI TPM. Default setting is **AMD fTPM**.
Active PCR banks	Displays active Platform Configuration Register (PCR) banks.
Available PCR banks	Displays available PCR banks.
SHA-1 PCR Bank	Enable/Disable SHA-1 PCR bank. Options available: Enabled, Disabled. Default setting is **Enabled**.
SHA256 PCR Bank	Enable/Disable SHA256 PCR bank. Options available: Enabled, Disabled. Default setting is **Enabled**.

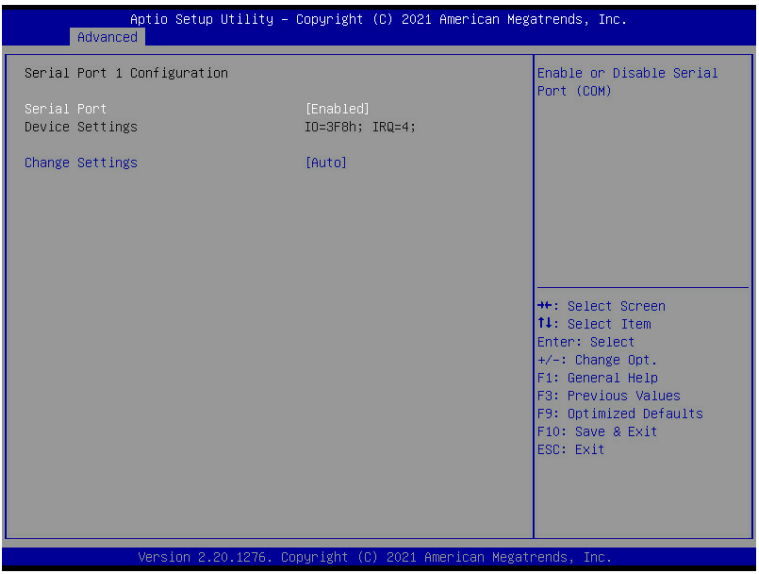
Parameter	Description
Pending operation	Schedule an operation for the security device. NOTE: Your computer will reboot during restart in order to change the state of a security device. Options available: None, TPM Clear. Default setting is **None**.
Platform Hierarchy	Enable/Disable platform hierarchy. Options available: Enabled, Disabled. Default setting is **Enabled**.
Storage Hierarchy	Enable/Disable storage hierarchy. Options available: Enabled, Disabled. Default setting is **Enabled**.
Endorsement Hierarchy	Enable/Disable endorsement hierarchy. Options available: Enabled, Disabled. Default setting is **Enabled**.
TPM2.0 UEFI Spec Version	Selects the TCG2 spec version support. Options available: TCG_1_2, TCG_2. Default setting is **TCG2**.
Physical Presence Spec Version	Selects the physical presence spec version. Options available: 1.2, 1.3. Default setting is **1.3**.
TPM 20 InterfaceType	Displays the TPM 2.0 interface type.
Device Select	Displays the TPM device information.

2-2-2 AST2500 Super IO Configuration



Parameter	Description
AST2500 Super IO Configuration	
Super IO Chip	Displays the super IO chip information
Serial Port 1 Configuration	Press [Enter] for configuration of advanced items.

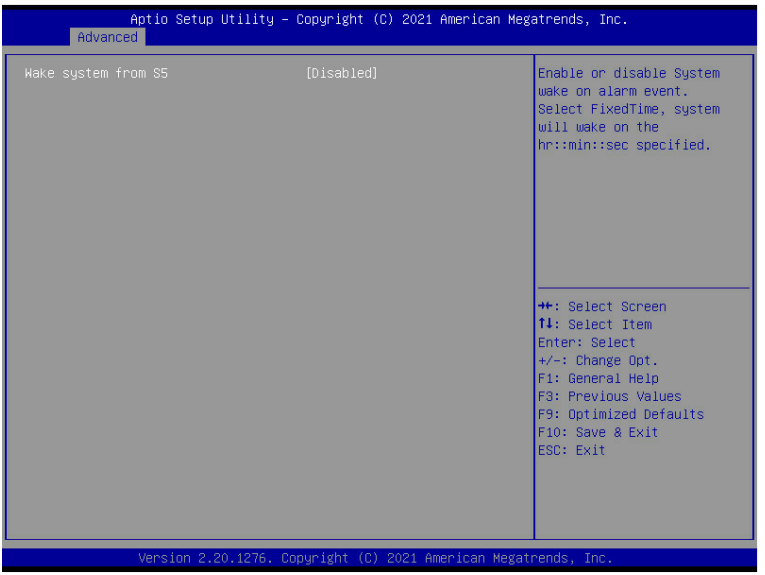
2-2-2-1 Serial Port 1 Configuration



Parameter	Description
Serial Port 1 Configuration	
Serial Port(Note1)	Enable/Disable the Serial Port (COM). When set to Enabled allows you to configure the Serial port 1 settings. When set to Disabled, displays no configuration for the serial port. Options available: Enabled, Disabled. Default setting is **Enabled**.
Devices Settings(Note2)	Displays the Serial Port 1 device settings.
Change Settings(Note2)	Select an optimal settings for Super IO Device. Options available for Serial Port 1: Auto IO=3F8h; IRQ=4; IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; Default setting is **Auto**.

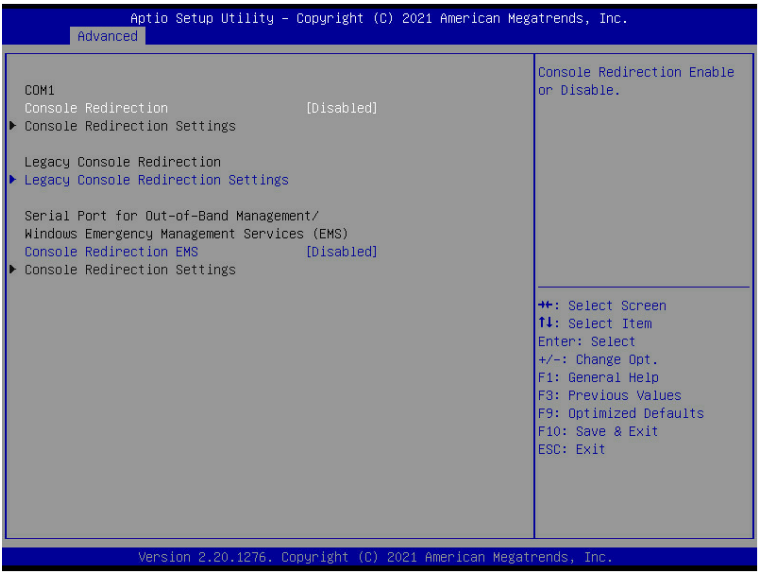
(Note1) Advanced items prompt when this item is defined.
(Note2) This item appears when **Serial Port** is set to **Enabled**.

2-2-3 S5 RTC Wake Settings



Parameter	Description
Wake System from S5	Enable/Disable system wake on alarm event. Options available: Disabled, Fixed Time. When Fixed Time is selected, system will wake on the hr::min::sec specified. Default setting is **Disabled**.

2-2-4 Serial Port Console Redirection



Parameter	Description
COM Console Redirection(Note))	Select whether to enable console redirection for specified device. Console redirection enables the users to manage the system from a remote location. Options available: Enabled, Disabled. Default setting is **Disabled**.
COM Console Redirection Settings	Press [Enter] to configure advanced items. **Please note that this item is configurable when COM Console Redirection is set to Enabled.** ◆ Terminal Type – Selects a terminal type to be used for console redirection. – Options available: VT100, VT100+, ANSI, VT-UTF8. Default setting is **ANSI**. ◆ Bits per second – Selects the transfer rate for console redirection. – Options available: 9600, 19200, 38400, 57600, 115200. Default setting is **115200**. ◆ Data Bits – Selects the number of data bits used for console redirection. – Options available: 7, 8. Default setting is **8**.

(Note) Advanced items prompt when this item is defined.

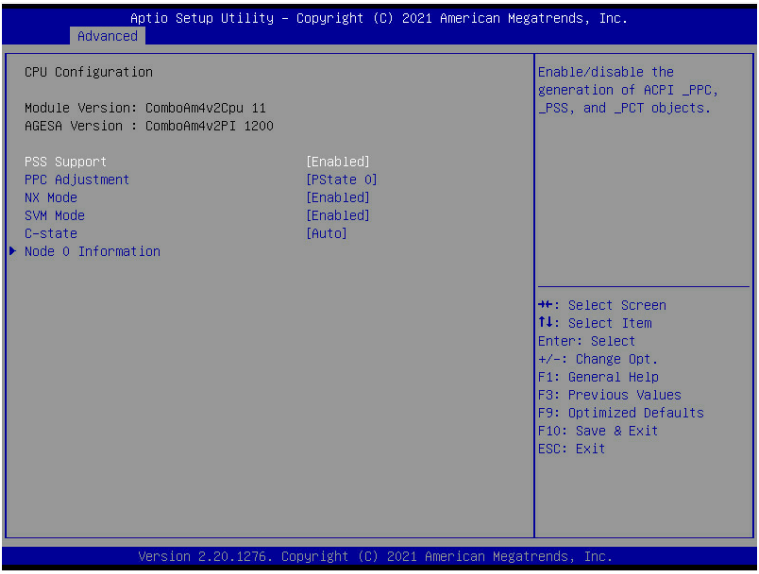
Parameter	Description
COM Console Redirection Settings (continued)	◆ Parity – A parity bit can be sent with the data bits to detect some transmission errors. – Even: parity bit is 0 if the num of 1's in the data bits is even. – Odd: parity bit is 0 if num of 1's in the data bits is odd. – Mark: parity bit is always 1. Space: Parity bit is always 0. – Mark and Space Parity do not allow for error detection. – Options available: None, Even, Odd, Mark, Space. Default setting is **None**. ◆ Stop Bits – Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit. – Options available: 1, 2. Default setting is **1**. ◆ Flow Control – Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals. – Options available: None, Hardware RTS/CTS. Default setting is **None**. ◆ VT-UTF8 Combo Key Support – Enable/Disable the VT-UTF8 Combo Key Support. – Options available: Enabled, Disabled. Default setting is **Enabled**. ◆ Recorder Mode – When this mode enabled, only texts will be send. This is to capture Terminal data. – Options available: Enabled, Disabled. Default setting is **Disabled**. ◆ Resolution 100x31 – Enable/Disable extended terminal resolution. – Options available: Enabled, Disabled. Default setting is **Enabled**. ◆ Putty KeyPad – Selects FunctionKey and KeyPad on Putty. – Options available: VT100, LINUX, XTERMR6, SC0, ESCN, VT400. Default setting is **VT100**.

Parameter	Description
Legacy Console Redirection	
Legacy Console Redirection Settings	Press [Enter] to configure advanced items. ◆ Redirection COM Port – Selects a COM port for Legacy serial redirection. – Default setting is **COM1**. ◆ Resolution – Selects the number of rows and columns used in Console Redirection for legacy OS support. – Options available: 80x24, 80x25. Default setting is **80x24**. ◆ Redirect After POST – When Bootloader is selected, then Legacy Console Redirection is disabled before booting to legacy OS. When Always Enable is selected, then Legacy Console Redirection is enabled for legacy OS. – Options available: Always Enable, BootLoader. Default setting is **Always Enable**.
Serial Port for Out-of-Band Management / Windows Emergency Management Services (EMS) Console Redirection(Note)	EMS console redirection allows the user to configure Console Redirection Settings to support Out-of-Band Serial Port management. Options available: Enabled, Disabled. Default setting is **Disabled**.
Serial Port for Out-of-Band EMS Console Redirection Settings	Press [Enter] to configure advanced items. **Please note that this item is configurable when Serial Port for Out-of-Band Management EMS Console Redirection is set to Enabled.** ◆ Out-of-Band Mgmt Port – Microsoft Windows Emergency Management Service (EMS) allows for remote management of a Windows Server OS through a serial port. – Default setting is **COM1**. ◆ Terminal Type EMS – Selects a terminal type to be used for console redirection. – Options available: VT100, VT100+, ANSI, VT-UTF8. Default setting is **ANSI**. ◆ Bits per second EMS – Selects the transfer rate for console redirection. – Options available: 9600, 19200, 38400, 57600, 115200. Default setting is **115200**.

(Note) Advanced items prompt when this item is defined.

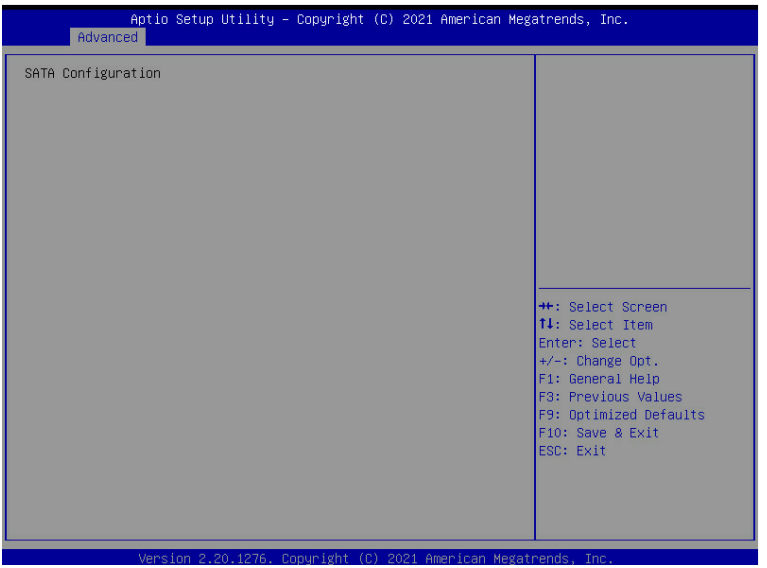
Parameter	Description
Serial Port for Out-of-Band EMS Console Redirection Settings(continued)	◆ Flow Control EMS – Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals. – Options available: None, Hardware RTS/CTS, Software Xon/Xoff. Default setting is **None**.

2-2-5 CPU Configuration



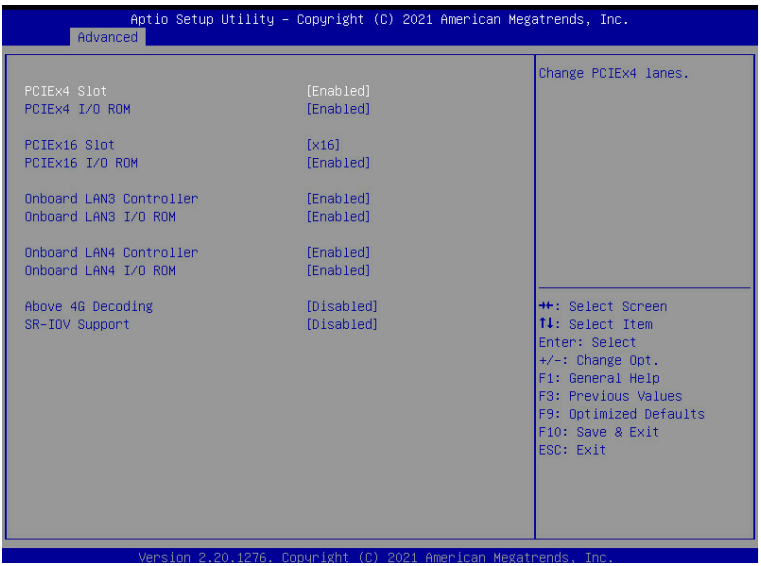
Parameter	Description
CPU Configuration	
Module Version	Displays the module version information.
AGESA Version	Displays the AGESA version information.
PSS Support	Enable/Disable the generation of ACPI_PPC, _PSS, and _PCT objects. Options available: Enabled, Disabled. Default setting is **Enabled**.
PPC Adjustment	Options available: PState 0, PState 1, PState 2. Default setting is **PState 0**.
NX Mode	Enable/Disable No-execute page protection Function. Options available: Enabled, Disabled. Default setting is **Enabled**.
SVM Mode	Enable/Disable the CPU Virtualization. Options available: Enabled, Disabled. Default setting is **Enabled**.
C-state	Controls IO based C-state generation and DF C-states. Options available: Enabled, Disabled, Auto. Default setting is **Auto**.
Node 0 Information	Press [Enter] to view the information related to Node 0.

2-2-6 SATA Configuration



Parameter	Description
SATA Configuration	Displays the installed HDD devices information. System will automatically detect HDD type.

2-2-7 PCI Subsystem Settings

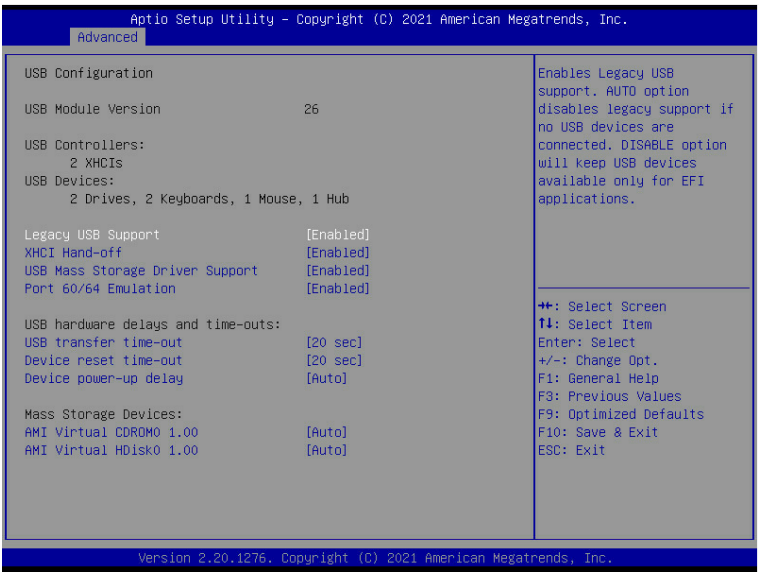


Parameter	Description
PCIEx4 Slot	Change the PCIEx4 lanes. Options available: Disabled, Enabled. Default setting is **Enabled**.
PCIEx4 I/O ROM	When enabled, this setting will initialize the device expansion ROM for the related PCI-E slot. Options available: Disabled, x16, x8 x8, x8 x4 x4, x4 x4 x8, x4 x4 x4 x4. Default setting is **x16**.
PCIEx16 Slot	Change the PCIEx16 lanes. Options available: Disabled, Enabled. Default setting is **Enabled**.
PCIEx16 I/O ROM	When enabled, this setting will initialize the device expansion ROM for the related PCI-E slot. Options available: Enabled, Disabled. Default setting is **Enabled**.
Onboard LAN3/4 Controller(Note)	Enable/Disable the onboard LAN controller. Options available: Enabled, Disabled. Default setting is **Enabled**.
Onboard LAN3/4 I/O ROM(Note)	Enable/Disable the onboard LAN devices, and initializes device expansion ROM. Options available: Enabled, Disabled. Default setting is **Enabled**.
Above 4G Decoding	Enable/Disable memory mapped I/O to 4GB or greater address space (Above 4G Decoding). Options available: Enabled, Disabled. Default setting is **Disabled**.

(Note) This section is dependent on the available LAN controller.

Parameter	Description
SR-IOV Support	If the system has SR-IOV capable PCIe devices, this item Enable/Disable Single Root IO Virtualization Support. Options available: Enabled, Disabled. Default setting is **Disabled**.

2-2-8 USB Configuration

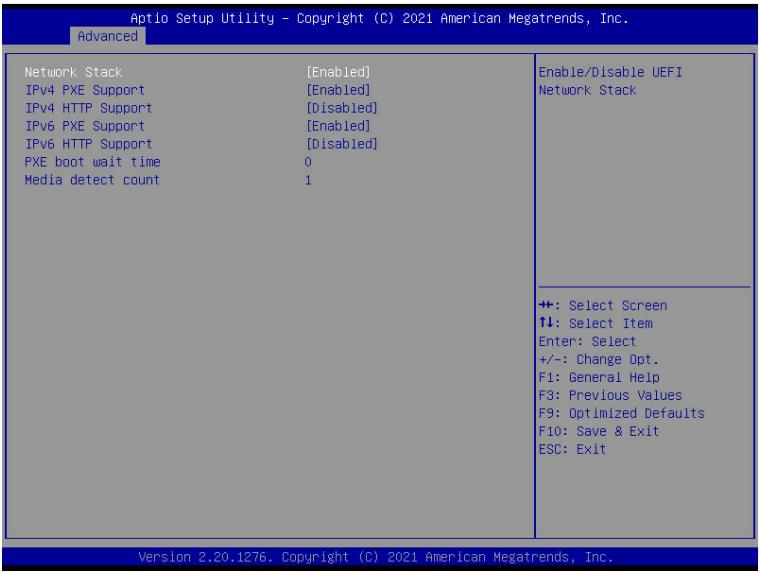


Parameter	Description
USB Configuration	
USB Module Version	Displays the USB module version information.
USB Controllers	Displays the supported USB controllers.
USB Devices:	Displays the USB devices connected to the system.
Legacy USB Support	Enable/Disable the Legacy USB support function. AUTO option disables legacy support if no USB devices are connected. DISABLE option will keep USB devices available only for EFI applications. Options available: Auto, Enabled, Disabled. Default setting is **Enabled**.
XHCI Hand-off	Enable/Disable the XHCI Hand-off support. Options available: Enabled, Disabled. Default setting is **Enabled**.
USB Mass Storage Driver Support(Note)	Enable/Disable the USB Mass Storage Driver Support. Options available: Enabled, Disabled. Default setting is **Enabled**.

(Note) This item is present only if you attach USB devices.

Parameter	Description
Port 60/64 Emulation	Enables the I/O port 60h/64h emulation support. This should be enabled for the complete USB Keyboard Legacy support for non-USB aware OS. Options available: Enabled, Disabled. Default setting is **Enabled**.
USB hardware delays and time-outs	
USB transfer time-out	Selects the time-out value for USB Control/Bulk/Interrupt transfers. Options available: 1 sec, 5 sec, 10 sec, 20 sec. Default setting is **20 sec**.
Device reset time-out	Selects the time-out value during a USB mass storage device reset. Options available: 10 sec, 20 sec, 30 sec, 40 sec. Default setting is **20 sec**.
Device power-up delay	Maximum time the device will take before it properly reports itself to the Host Controller. "Auto" uses default value: for a Root port it is 100 ms, for a Hub port the delay is taken from Hub descriptor. Options available: Auto, Manual. Default setting is **Auto**.
Mass Storage Devices	Displays the mass storage devices available on the system.

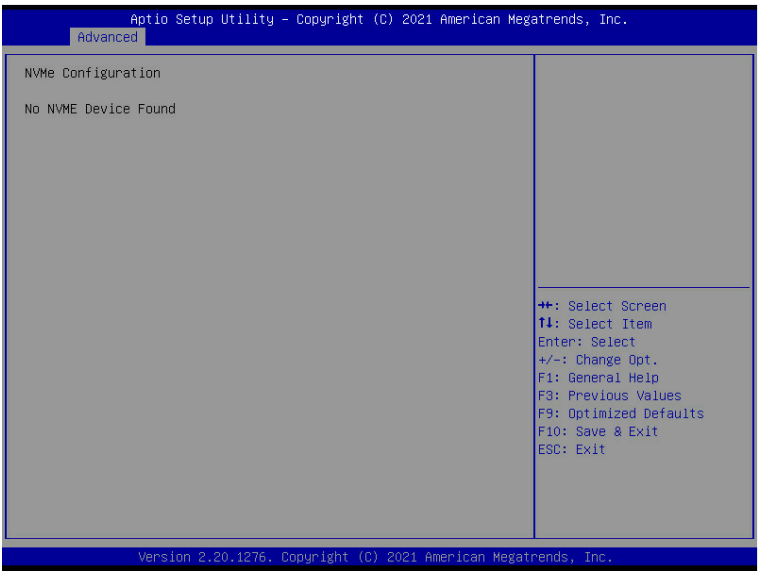
2-2-9 Network Stack Configuration



Parameter	Description
Network Stack	Enable/Disable the UEFI network stack. Options available: Enabled, Disabled. Default setting is **Enabled**.
Ipv4 PXE Support(Note)	Enable/Disable the Ipv4 PXE feature. Options available: Enabled, Disabled. Default setting is **Enabled**.
Ipv4 HTTP Support(Note)	Enable/Disable the Ipv4 HTTP feature. Options available: Enabled, Disabled. Default setting is **Disabled**.
Ipv6 PXE Support(Note)	Enable/Disable the Ipv6 PXE feature. Options available: Enabled, Disabled. Default setting is **Enabled**.
Ipv6 HTTP Support(Note)	Enable/Disable the Ipv6 HTTP feature. Options available: Enabled, Disabled. Default setting is **Disabled**.
PXE boot wait time(Note)	Wait time in seconds to press ESC key to abort the PXE boot. Press the <+> / <-> keys to increase or decrease the desired values.
Media detect count(Note)	Number of times the presence of media will be checked. Press the <+> / <-> keys to increase or decrease the desired values.

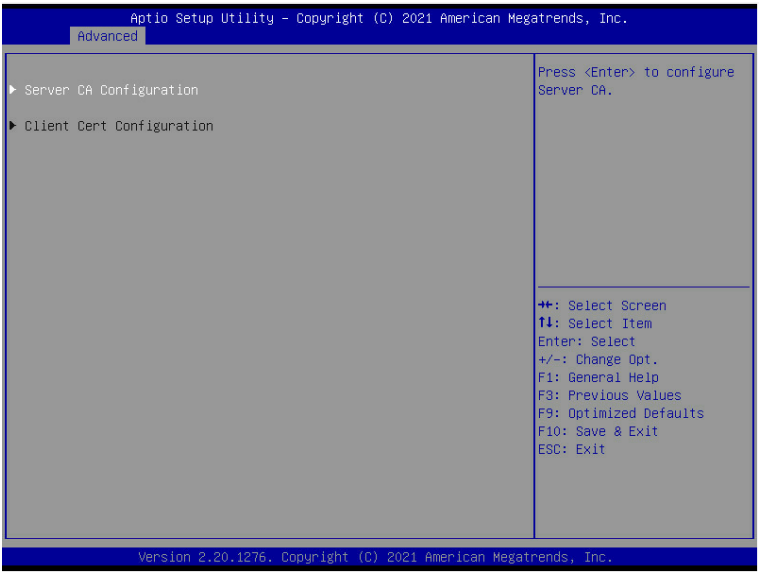
(Note) This item appears when **Network Stack** is set to **Enabled**.

2-2-10 NVMe Configuration



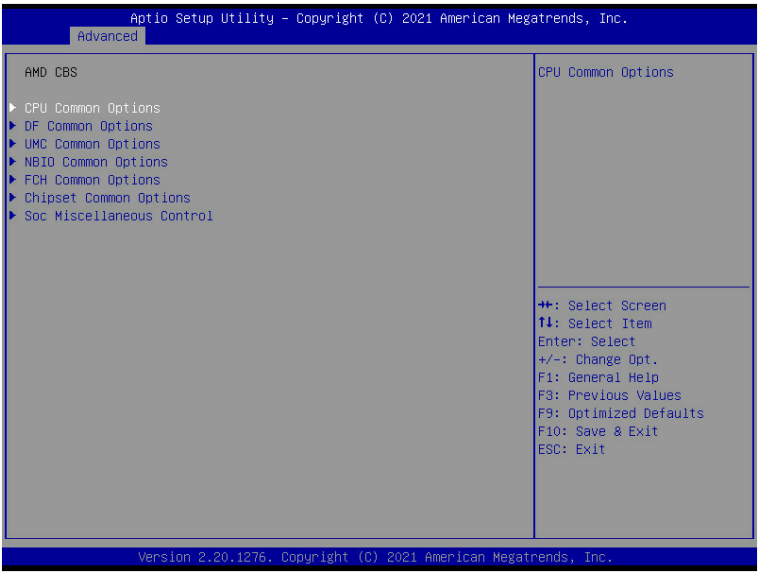
Parameter	Description
NVMe Configuration	Displays the NVMe devices connected to the system.

2-2-11 Tls Auth Configuration



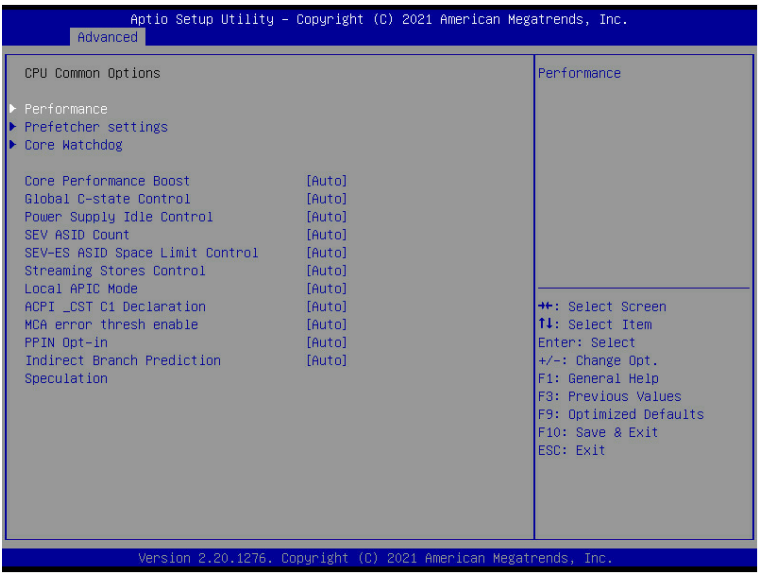
Parameter	Description
Server CA Configuration	Press [Enter] for configuration of advanced items. ◆ Enroll Cert – Press [Enter] to enroll a certificate • Enroll Cert Using File • Cert GUID Input digit character in 1111111-2222-3333-4444-1234567890ab format. – Commit Changes and Exit – Discard Changes and Exit ◆ Delete Cert
Client Cert Configuration	Press [Enter] for configuration of advanced items.

2-2-12 AMD CBS



Parameter	Description
AMD CBS	
CPU Common Options	Press [Enter] for configuration of advanced items.
DF Common Options	Press [Enter] for configuration of advanced items.
UMC Common Options	Press [Enter] for configuration of advanced items.
NBIO Common Options	Press [Enter] for configuration of advanced items.
FCH Common Options	Press [Enter] for configuration of advanced items.
Chipset Common Options	Press [Enter] for configuration of advanced items.
Soc Miscellaneous Control	Press [Enter] for configuration of advanced items.

2-2-12-1 CPU Common Options

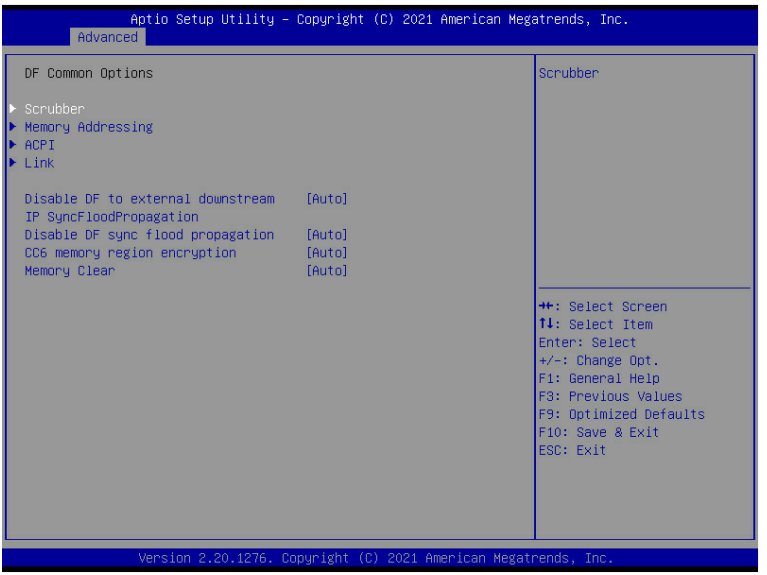


Parameter	Description
CPU Common Options	
Performance	Press [Enter] for configuration of advanced items. ◆ Custom Core Pstates – Allows you to accept or decline enabling Custom Core Pstates. When accepted, you can disable or customize core pstates.
Prefetcher settings	Press [Enter] for configuration of advanced items. ◆ L1 Stream HW Prefetcher – Options available: Auto, Enable, Disable. Default setting is **Auto**. ◆ L2 Stream HW Prefetcher – Options available: Auto, Enable, Disable. Default setting is **Auto**.
Core Watchdog	Press [Enter] for configuration of advanced items. ◆ Core Watchdog Timer Enable – Enable/Disable CPU Watchdog Timer. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**. ◆ Core Watchdog Timer Interval(Note) – Specifies the CPU Watchdog Timer interval. – Default setting is **Auto**. ◆ Core Watchdog Timer Severity(Note) – Specifies the CPU Watchdog Timer Severity. – Options available: Auto, No Error, Transparent, Corrected, Deferred, Uncorrected, Fatal. Default setting is **Auto**.

(Note) This item appears when **Core Watchdog Timer Enable** is set to **Enabled**.

Parameter	Description
Core Performance Boost	Enable/Disable the Core Performance Boost function. Options available: Auto, Disabled. Default setting is **Auto**.
Global C-State Control	Controls the IO based C-state generation and DF C-states. Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
Power Supply Idle Control	Configures the Power Supply Idle Control. Options available: Auto, Low Current Idle, Typical Current Idle. Default setting is **Auto**.
SEV ASID Count	Specifies the maximum valid ASID, which affects the maximum system physical address space. Options available: Auto, 253 ASIDs, 509 ASIDs. Default setting is **Auto**.
SEV-ES ASID Space Limit Control	Space limit control for SEV-ES ASIDs. Options available: Auto, Manual. Default setting is **Auto**.
Streaming Stores Control	Enable/Disable the Streaming Stores functionality. Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
Local APIC Mode	Sets the Local APIC Mode. Options available: Auto, xAPIC, x2APIC, Compatibility. Default setting is **Auto**.
ACPI_CST C1 Decaration	Determines whether or not to declare the C1 state to the OS.. Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
MCA error thresh enable	Enable MCA error thresholding. Options available: Auto, False, True. Default setting is **Auto**.
PPIN Opt-in	Enable/Disable the PPIN feature. Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
Indirect Branch Prediction Speculation	Options available: Auto, Enabled, Disabled. Default setting is **Auto**.

2-2-12-2 DF Common Options

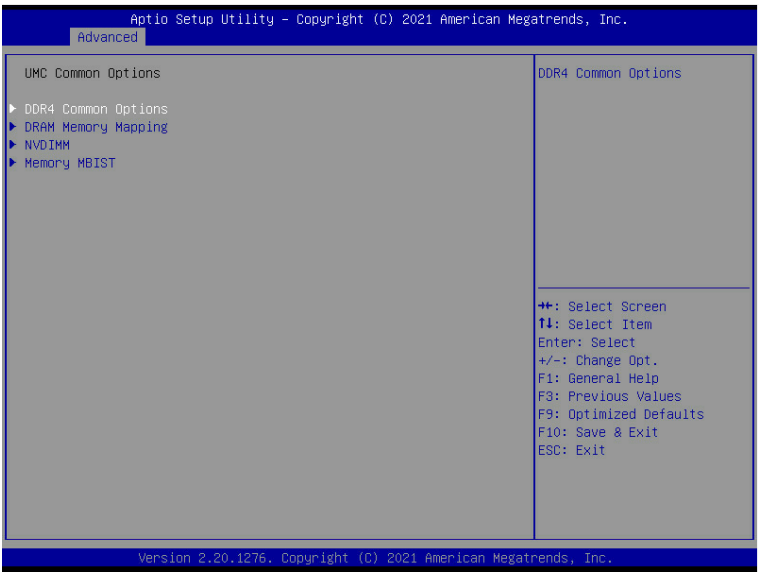


Parameter	Description
DF Common Options	
Scrubber	Press [Enter] for configuration of advanced items. ◆ DRAM scrub time – Provide a value that is the number of hours to scrub memory. – Options available: Auto, Disabled, 1 hour, 4 hours, 8 hours, 16 hours, 24 hours, 48 hours. Default setting is **Auto**. ◆ Poison scrubber control – Enable/Disable the Poison scrubber control feature. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**. ◆ Redirect scrubber control – Enable/Disable the Redirect scrubber control feature. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**. ◆ Redirect scrubber limit – Sets the redirect scrubber limit. – Options available: Auto, 2, 4, 8, Infinite. Default setting is **Auto**.
Memory Addressing	Press [Enter] for configuration of advanced items. ◆ NUMA nodes per socket – Specifies the number of desired NUMA nodes per socket. – Options available: Auto, NPS0, NPS1, NPS2, NPS4. Default setting is **Auto**. ◆ Memory interleaving – Enable/Disable the Memory interleaving feature. – Options available: Auto, Disabled. Default setting is **Auto**.

Parameter	Description
Memory Addressing (continued)	◆ Memory interleaving size – Controls the memory interleaving size. This determines the starting address of the interleave (bit 8, 9, 10 or 11). – Options available: Auto, 256Bytes, 512Bytes, 1KB, 2KB. Default setting is **Auto**. ◆ 1TB remap – Enable/Disable to remap DRAM out of the space just below the 1TB boundary. The ability to remap depends on DRAM configuration, NPS, and interleaving selection, and may not always be possible. – Options available: Auto, Do not remap, Attempt to remap. Default setting is **Auto**. ◆ DRAM map inversion – Enable/Disable the DRAM map inversion function. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
ACPI	Press [Enter] for configuration of advanced items. ◆ ACPI SRAT L3 Cache As NUMA Domain – Enable/Disable report each L3 cache as a NUMA Domain to the OS. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**. ◆ ACPI SLIT Distance Control – Determines how the SLIT distances are declared. – Options available: Auto, Manual. Default setting is **Auto**. ◆ ACPI SLIT remote relative distance – Sets the remote socket distance for 2P systems as near (2.8) or far (3.2). – Options available: Auto, Near, Far. Default setting is **Auto**.
Link	Press [Enter] for configuration of advanced items. ◆ GMI encryption control – Enable/Disable GMI link encryption. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**. ◆ xGMI encryption control – Enable/Disable xGMI link encryption. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**. ◆ CAKE CRC perf bounds Control – Options available: Auto, Manual. Default setting is **Auto**. ◆ 4-link xGMI max speed – Specifies the max speed of 4-link xGMI. – Default setting is **Auto**. ◆ 3-link xGMI max speed – Specifies the max speed of 3-link xGMI. – Default setting is **Auto**.

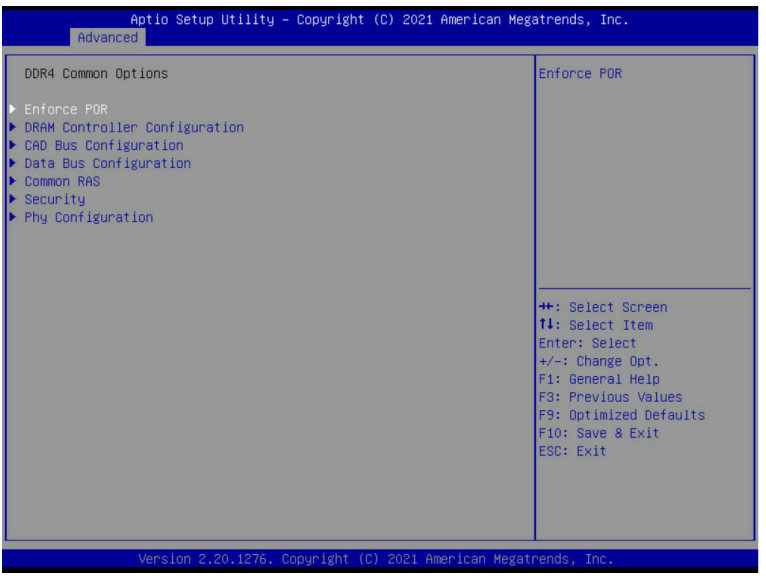
Parameter	Description
Link (continued)	◆ xGMI TXEQ Mode – Configures xGMI TXEQ/RX vetting Mode. – Options available: Auto, TXEQ_Disabled, TXEQ_Lane, TXEQ_Link, TXEQ_RX_Vet. Default setting is **Auto**. ◆ PcsCG control – Options available: Auto, Enable. Default setting is **Auto**.
Disable DF to external downstream IP SyncFloodPropagation	Enable/Disable SyncFlood to UMC & downstream slaves. Options available: Auto, Sync flood disabled, Sync flood enabled. Default setting is **Auto**.
Disable DF sync flood propagation	Enable/Disable DF Sync Flood propagation. Options available: Auto, Sync flood disabled, Sync flood enabled. Default setting is **Auto**.
CC6 memory region encryption	Controls whether or not the CC6 save/restor memory is encrypted. Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
Memory Clear	Enable/Disable the Memory Clear feature. Options available: Auto, Enabled, Disabled. Default setting is **Auto**.

2-2-12-3 UMC Common Options



Parameter	Description
UMC Common Options	
DDR4 Common Options	Press [Enter] for configuration of advanced items.
DRAM Memory Mapping	Press [Enter] for configuration of advanced items.
NVDIMM	Press [Enter] for configuration of advanced items.
Memory MBIST	Press [Enter] for configuration of advanced items.

2-2-12-3-1 DDR4 Common Options

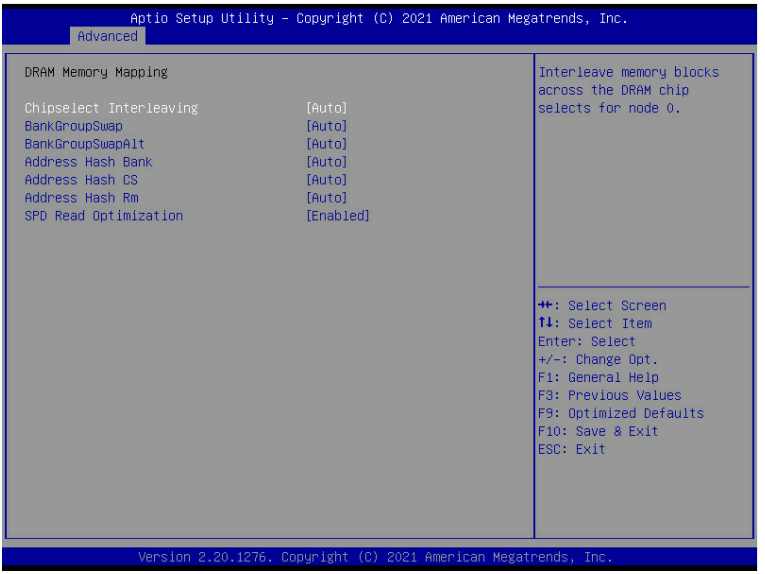


Parameter	Description
DDR4 Common Options	
Enforce POR	Press [Enter] to enable / disable restrictions for DDR4 frequency and voltage programming. Memory speeds will be capped at AMD guidelines. ◆ Decline ◆ Accept – Overclock: Enable/Disable Memory Overclock Settings. – Options available: Auto, Enabled. Default setting is **Auto**.
DRAM Controller Configuration	Press [Enter] for configuration of advanced items. ◆ DRAM Power Options – Power Down Enable » Enable/Disable DDR power down mode. » Options available: Auto, Enabled, Disabled. Default setting is **Auto**. – Disable Burst/Postponed Refresh » Options available: Auto, Enabled. Default setting is **Auto**. – DRAM Maximum Activate Count » Default setting is **Auto**. ◆ Cmd2T – Selects the Cmd2T mode on ADDR/CMD. – Options available: Auto, 1T, 2T. Default setting is **Auto**. ◆ Gear Down Mode – Enable/Disable the Gear Down Mode function. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**.

Parameter	Description
CAD Bus Configuration	Press [Enter] for configuration of advanced items. ◆ CAD Bus Timing User Controls – Setup time on CAD bus signals to Auto or Manual. – Options available: Auto, Manual. Default setting is **Auto**. ◆ CAD Bus Drive Strength User Controls – Drive Strength on CAD bus signals to Auto or Manual. – Options available: Auto, Manual. Default setting is **Auto**.
Data Bus Configuration	Press [Enter] for configuration of advanced items. ◆ Data Bus Configuration User Controls – Specifies the mode for drive strength to Auto or Manual. – Options available: Auto, Manual. Default setting is **Auto**.
Common RAS	Press [Enter] for configuration of advanced items. ◆ Data Poisoning – Enable/Disable the Data Poisoning function. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**. ◆ DRAM Post Package Repair – Enable/Disable the DRAM Post Package Repair function. – Options available: Enable, Disable. Default setting is **Disable**. ◆ RCD Parity – Enable/Disable the RCD Parity function. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**. ◆ DRAM Address Command Parity Retry – Enable/Disable the DRAM Address Command Parity Retry function. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**. ◆ Max Parity Error Replay – Configures the Max Parity Error Replay. (0~0x3f). – Default setting is **8**. – **Please note that this item is configurable when DRAM Address Command Parity Retry is set to Enabled.** ◆ Write CRC Enable – Enable/Disable the Write CRC function. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**. ◆ DRAM Write CRC Enable and Retry Limit – Enable/Disable DRAM Write CRC Enable and Retry Limit. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**. ◆ Max Write CRC Error Replay – Configures the Max Write CRC Error Replay. (0~0x3f). – Default setting is **8**. – **Please note that this item is configurable when DRAM Write CRC Enable and Retry Limit is set to Enabled.** ◆ Disable Memory Error Injection – Options available: False, True. Default setting is **True**.

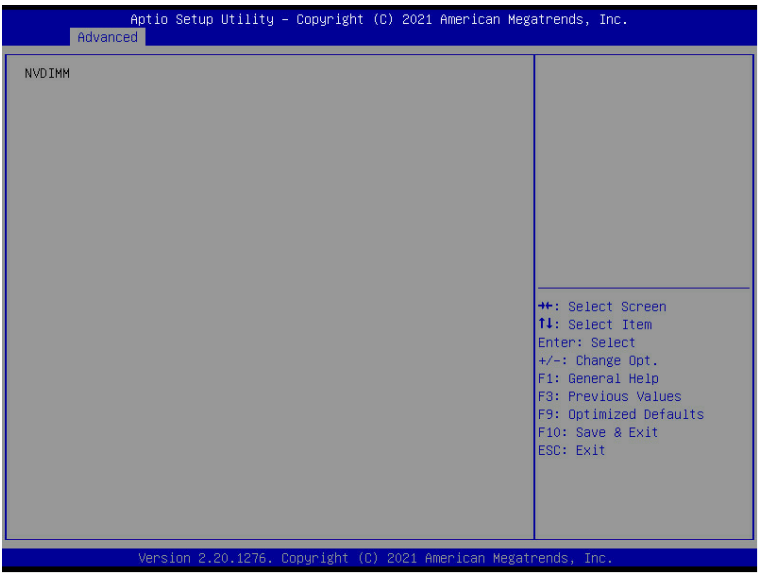
Parameter	Description
Common RAS (continued)	◆ ECC Configuration – DRAM ECC Symbol Size » Configures the DRAM ECC Symbol Size. » Options available: Auto, x4, x8, x16. Default setting is **Auto**. – DRAM ECC Enable » Enable/Disable DRAM ECC. When set to Auto, it will set ECC to enable. » Options available: Auto, Enabled, Disabled. Default setting is **Auto**. – DRAM UECC Retry » Enable/Disable DRAM UECC Retry. » Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
Security	Press [Enter] for configuration of advanced items. ◆ TSME – Enable/Disable transparent secure memory encryption. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**. ◆ Data Scramble – Enable/Disable Data Scrambling. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
Phy Configuration	Press [Enter] for configuration of advanced items. ◆ PMU Training – DFE Read Training » Perform 2D Read Training with DFE on. » Options available: Auto, Enable, Disable. Default setting is **Auto**. – FFE Write Training » Perform 2D Write Training with FFE on. » Options available: Auto, Enable, Disable. Default setting is **Auto**. – PMU Pattern Bits Control » Options available: Auto, Manual. Default setting is **Auto**. – MR6VrefDQ Control » Options available: Auto, Manual. Default setting is **Auto**. – CPU Vref Training Seed Control » Options available: Auto, Manual. Default setting is **Auto**.

2-2-12-3-2 DRAM Memory Mapping



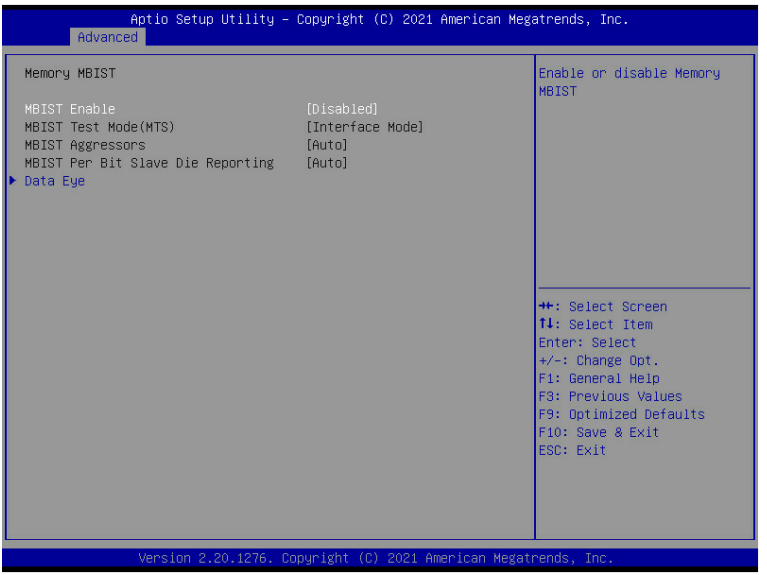
Parameter	Description
DRAM Memory Mapping	
Chipselect Interleaving	Interleave memory blocks across the DRAM chip selects for node 0. Options available: Auto, Disabled. Default setting is **Auto**.
BankGroupSwap	Configures the BankGroupSwap. BankGroupSwap (BGS) is a new memory mapping option in AGESA that alters how applications get assigned to physical locations within the memory modules. When this option sets to Auto, it is null: No help string. Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
BankGroupSwapAlt	Configures the BankGroupSwapAlt. Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
Address Hash Bank	Enable/Disable bank address hashing. Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
Address Hash CS	Enable/Disable CS address hashing. Options available: Auto, Enabled, Disabled. Default setting is **Auto**
Address Hash Rm	Enable/Disable RM address hashing. Options available: Auto, Enabled, Disabled. Default setting is **Auto**
SPD Read Optimization	Enable/Disable SPD Read Optimization. Options available: Auto, Enabled, Disabled. Default setting is **Auto**

2-2-12-3-3 NVDIMM



Parameter	Description
NVDIMM	Displays the information of the devices/controllers if installed

2-2-12-3-4 Memory MBIST



Parameter	Description
Memory MBIST	
MBIST Enable	Enable/Disable the Memory MBIST function. Options available: Enabled, Disabled. Default setting is **Enabled**.
MBIST Test Mode(Note)	Selects MBIST Test Mode. **Interface Mode**: Tests Single and Multiple CS transactions and Basic Connectivity. **Data Eye Mode**: Measures Voltage vs. Timing. Options available: Auto, Both, Interface Mode, Data Eye Mode. Default setting is **Interface Mode**.
MBIST Aggressors(Note)	Enable/Disable MBIST Aggressor test. Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
MBIST Per Bit Slave Die Reporting(Note)	Enable/Disable to report 2D data eye results in ABL log for each DQ, Chipselect, and Channel. Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
Data Eye	Press [Enter] to configure advanced items. ◆ Pattern Select – Options available: PRBS, SSO, Both. Default setting is **PRBS**. ◆ Pattern Length – Determines the pattern length. The possible options are N=3....12.

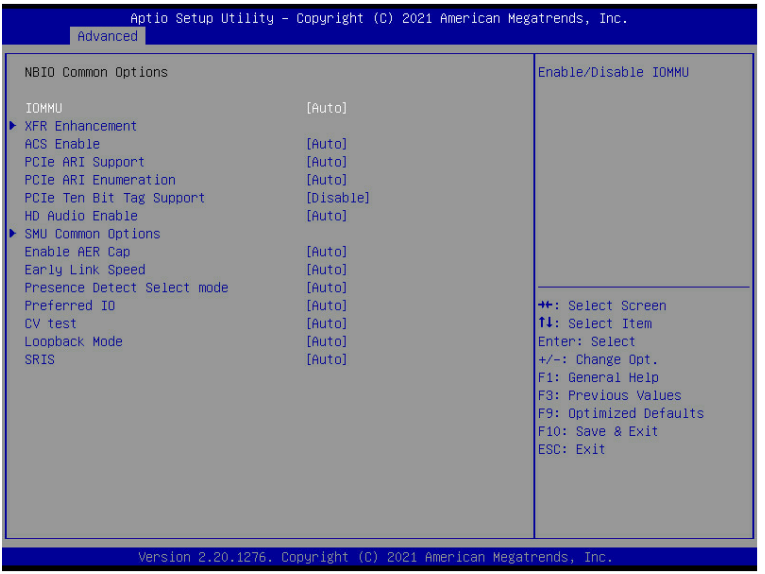
(Note) This item is available when **MBIST Enable** is set to **Enabled**.

Parameter	Description
Data Eye (Continued)	◆ Aggressor Channel – This item helps read the aggressors channels. – Options available: Disabled, 1 Aggressor Channel, 3 Aggressor Channels, 7 Aggressor Channels. Default setting is **1 Aggressor Channel**. ◆ Aggressor Static Lane Control – Enable/Disable the Aggressor Static Lane Control function. – Options available: Enabled, Disabled. Default setting is **Disabled**. ◆ Aggressor Static Lane Select Upper 32 bits(Note1) ◆ Aggressor Static Lane Select Lower 32 bits(Note1) ◆ Aggressor Static Lane Select ECC(Note1) ◆ Aggressor Static Lane Value(Note1) ◆ Target Static Lane Control – Enable/Disable the Target Static Lane Control function. – Options available: Enabled, Disabled. Default setting is **Disabled**. ◆ Target Static Lane Select Upper 32 bits(Note2) ◆ Target Static Lane Select Lower 32 bits(Note2) ◆ Target Static Lane Select ECC(Note2) ◆ Target Static Lane Value(Note2) ◆ Worst Case Margin Granularity – Options available: Per Chip Select, Per Nibble. Default setting is **Per Chip Select**. ◆ Read Voltage Sweep Step Size – Options available: 1, 2, 4. Default setting is **1**. ◆ Read Timing Sweep Step Size – Options available: 1, 2, 4. Default setting is **1**. ◆ Write Voltage Sweep Step Size – Options available: 1, 2, 4. Default setting is **1**. ◆ Write Timing Sweep Step Size – Options available: 1, 2, 4. Default setting is **1**.

(Note1) This item is configurable when **Aggressor Static Lane Control** is set to **Enabled**.

(Note2) This item is configurable when **Target Static Lane Control** is set to **Enabled**.

2-2-12-4 NBIO Common Options

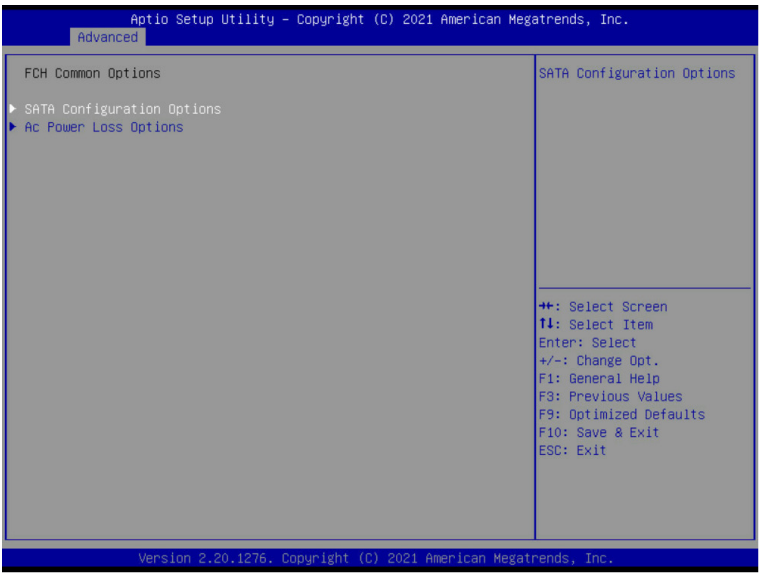


Parameter	Description
NBIO Common Options	
IOMMU	Enable/Disable the IOMMU function. Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
XFR Enhancement	Press [Enter] to configure advanced items. ◆ Declined ◆ Accepted – Precision Boost Overdrive » Options available: Auto, Enable, Disable, Manual. Default setting is **Auto**. – Precision Boost Overdrive Scalar » Options available: Auto, Disabled, Manual. Default setting is **Auto**. ◆ FCLK Frequency – Specifies the FCLK frequency. Default setting is **Auto**. ◆ SOC OVERCLOCK VID – Specifies the Voltage ID (VID) value for VDDR_SOC to support overclocking. NOTE: This is a VID value in hex and not a voltage level and applied if DDR speed is > 2933. ◆ UCLK DIV1 MODE – Options available: Auto, UCLK==MEMCLK, UCLK==MEMCLK/2. Default setting is **Auto**. ◆ VDDP Voltage Control – Options available: Auto, Manual. Default setting is **Auto**.

Parameter	Description
XFR Enhancement (continued)	◆ VDDG Voltage Control – Options available: Auto, Manual. Default setting is **Auto**. ◆ SoC/Uncore OC Mode – Forces CPU SoC/Uncore components (e.g. Infinity Fabric, memory, and integrated graphics) to run at their maximum specified frequency at all times. May improve performance at the expense of idle power savings. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**. ◆ LN2 Mode – Send a message to SMU to help with cold boot and operating under LN2 conditions for GMI2. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
ACS Enable	AER must be enabled for ACS enable to work. Options available: Auto, Enable, Disabled. Default setting is **Auto**.
PCIe ARI Support	Enable/Disable Alternative Routing-ID Interpretation. Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
PCIe ARI Enumeration	Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
PCIe Ten Bit Tag Support	Enable/Disable PCIe ten bit tags for supported devices. (Auto=Disabled) Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
HD Audio Enable	Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
SMU Common Options	Press [Enter] for configuration of advanced items. ◆ Max Voltage Offset – Default setting is **Auto**. ◆ cTDP Control – Selects use the fused TDP or set customized TDP. **TDP is used to define the RC thermal model only** – Options available: Auto, Manual. Default setting is **Auto**. ◆ Fan Control – Fan Table Control » Options available: Auto, Manual. Default setting is **Auto**. ◆ EfficiencyModeEn – Options available: Auto, Enabled. Default setting is **Auto**. ◆ Package Power Limit Control – Selects use the fused PPT or set customized PPT. **PPT will be used as the ASIC power limit** – Options available: Auto, Manual. Default setting is **Auto**. ◆ APBDIS – Options available: Auto, 0, 1. Default setting is **Auto**. ◆ DF Cstates – Enable/Disable DF C-states. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**.

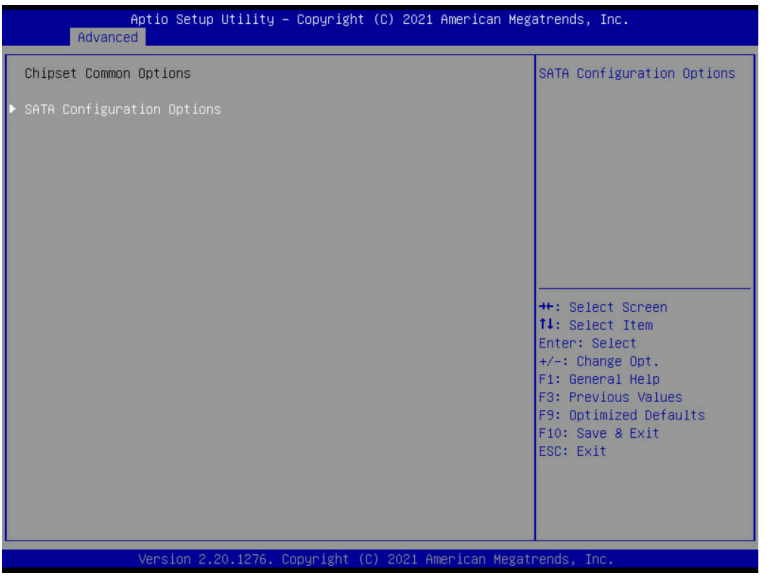
Parameter	Description
SMU Common Options (continued)	◆ CPPC – Options available: Auto, Enabled, Disabled. Default setting is **Auto**. ◆ CPPC Preferred Cores – Options available: Auto, Enabled, Disabled. Default setting is **Auto**. ◆ NBIO LCLK DPM – This setting controls how the NBIO Power Management is controlled. – Options available: Auto, Manual. Default setting is **Auto**.
Enable AER Cap	Enable/Disable Advanced Error Reporting Capability. Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
Early Link Speed	Configures Early Link Speed. Options available: Auto, Gen1, Gen2. Default setting is **Auto**.
Presence Detect Select mode	Controls the Presence Detect Select mode. Options available: Auto, OR, AND. Default setting is **Auto**.
Preferred IO	Preferred IO select type. Manual: Bus Number manually. Auto: Default. Options available: Auto, Manual. Default setting is **Auto**.
CV test	Enable/Disable the running PCIECV tool support. Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
Loopback Mode	Enable/Disable the PCIe loopback mode. Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
ARIS	Options available: Auto, Enable, Disable. Default setting is **Auto**.

2-2-12-5 FCH Common Options



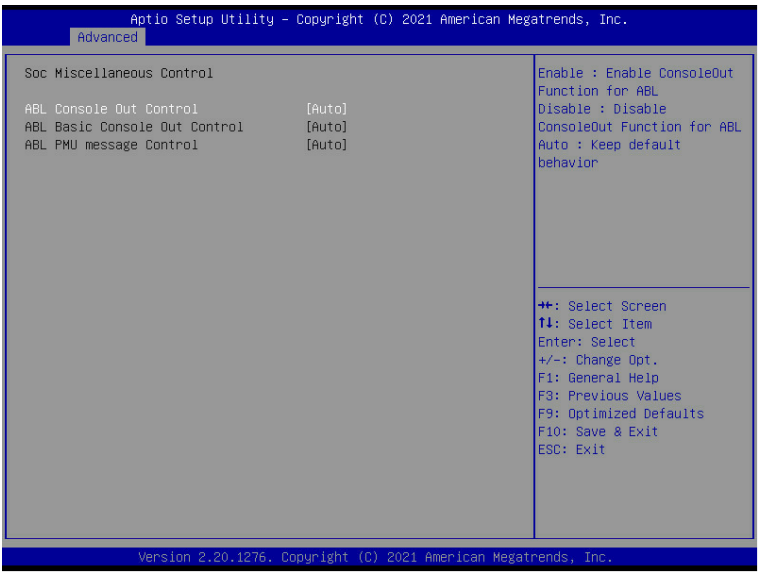
Parameter	Description
FCH Common Options	
SATA Configuration Options	Press [Enter] for configuration of advanced items. ◆ SATA Enable – Enable/Disable OnChip SATA controller. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**. ◆ SATA Mode – Selects OnChip SATA Type. – Options available: AHCI, AHCI as ID 0x7904, Auto, RAID. Default setting is **AHCI**. ◆ SATA RAS Support – Enable/Disable SATA RAS Support. – Options available: Auto, Enabled, Disabled. Default setting is **Auto**. ◆ SATA Disabled AHCI Prefetch Function – Options available: Auto, Enabled, Disabled. Default setting is **Auto**. ◆ Aggressive SATA Device Sleep Port 0/1 – Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
AC Power Loss Options	Press [Enter] for configuration of advanced items. ◆ AC Loss Control – Selects the AC Loss Control Method. – Options available: Power Off, Power On, Last State. Default setting is **Last State**.

2-2-12-6 Chipset Common Options



Parameter	Description
Chipset Common Options	
SATA Configuration Options	Press [Enter] for configuration of advanced items. ◆ SATA Mode – Select SATA Type. – Options available: AHCI, RAID, Auto. Default setting is **Auto**. ◆ Aggressive SATA Device Sleep Port 0/1 – Options available: Auto, Enable, Disable. Default setting is **Auto**.

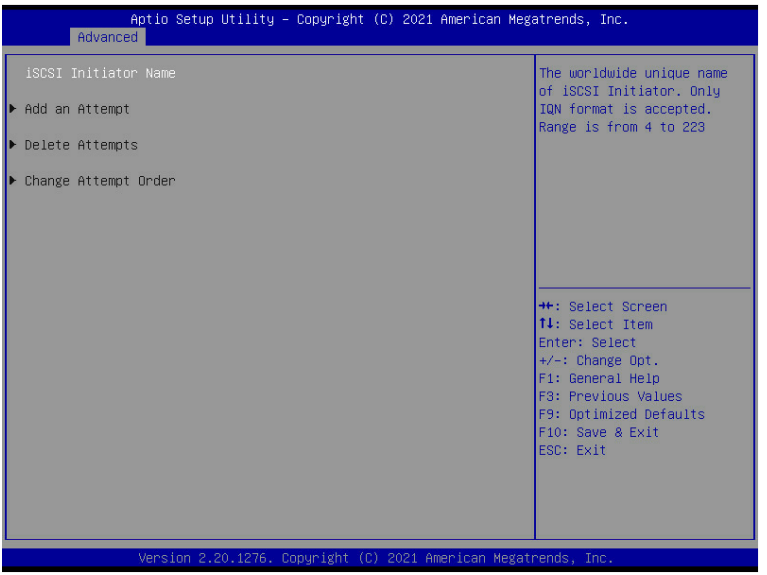
2-2-12-7 SOC Miscellaneous Control



Parameter	Description
SOC Miscellaneous Control	
ABL Console Out Control	Enable/Disable the ConsoleOut function for ABL. Options available: Auto, Enable, Disable. Default setting is **Auto**.
ABL Basic Console Out Control(Note)	Enable/Disable the Basic ConsoleOut function for ABL. Options available: Auto, Enable, Disable. Default setting is **Auto**.
ABL PMU message Control(Note)	To Control the total number of PMU debug messages. Options available: Auto, Detailed debug message, Coarse debug message, Stage completion, Firmware completion message only. Default setting is **Auto**.

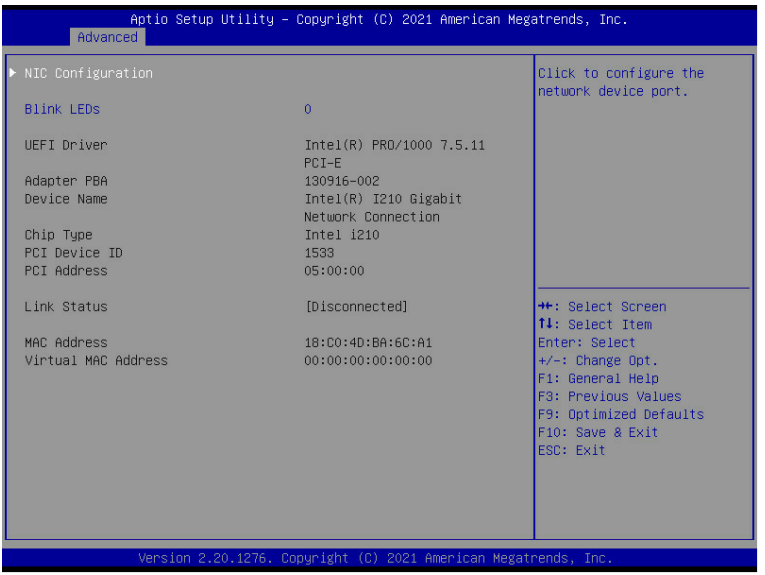
(Note) This item is configurable when **ABL Console Out Control** is set to **Enabled**.

2-2-13 iSCSI Configuration



Parameter	Description
iSCSI Initiator Name	Press [Enter] and name iSCSI Initiator. Only IQN format is accepted. Range: from 4 to 223
Add an Attempt	Press [Enter] to configure advanced items.
Delete Attempts	Press [Enter] to configure advanced items.
Change Attempt Order	Press [Enter] to configure advanced items.

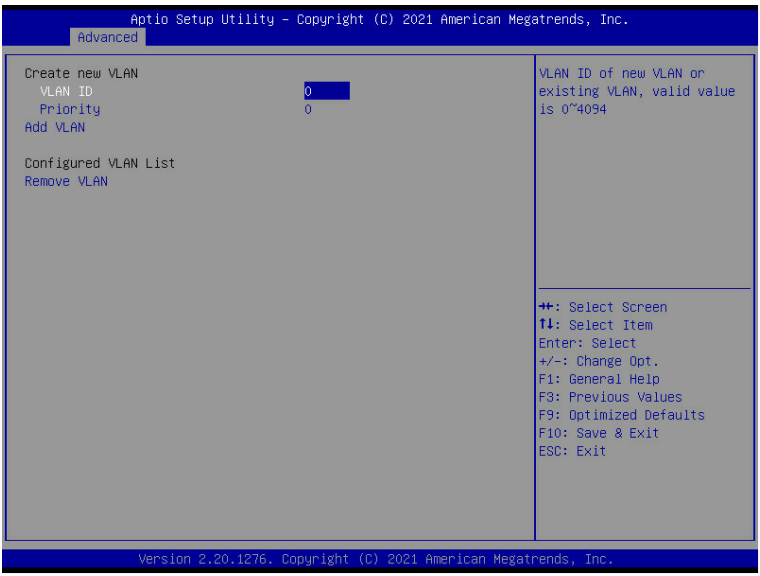
2-2-14 Intel(R) I210 Gigabit Network Connection



Parameter	Description
NIC Configuration	◆ Link Speed – Allows for automatic link speed adjustment. – Options available: Auto Negotiated, 10 Mbps Half, 10 Mbps Full, 100 Mbps Half, 100 Mbps Full. Default setting is **Auto Negotiated**. ◆ Wake On LAN – Enables power on of the system via LAN. Note that configuring Wake on LAN in the operating system does not change the value of this setting, but does override the behavior of Wake on LAN in OS controlled power states. – Options available: Enabled/Disabled. Default setting is **Enabled**.
Blink LEDs	Identifies the physical network port by blinking the associated LED. Press the numeric keys to adjust desired values.
UEFI Driver	Displays the technical specifications for the Network Interface Controller.
Adapter PBA	Displays the technical specifications for the Network Interface Controller.
Device Name	Displays the technical specifications for the Network Interface Controller.
Chip Type	Displays the technical specifications for the Network Interface Controller.
PCI Device ID	Displays the technical specifications for the Network Interface Controller.
PCI Address	Displays the technical specifications for the Network Interface Controller.

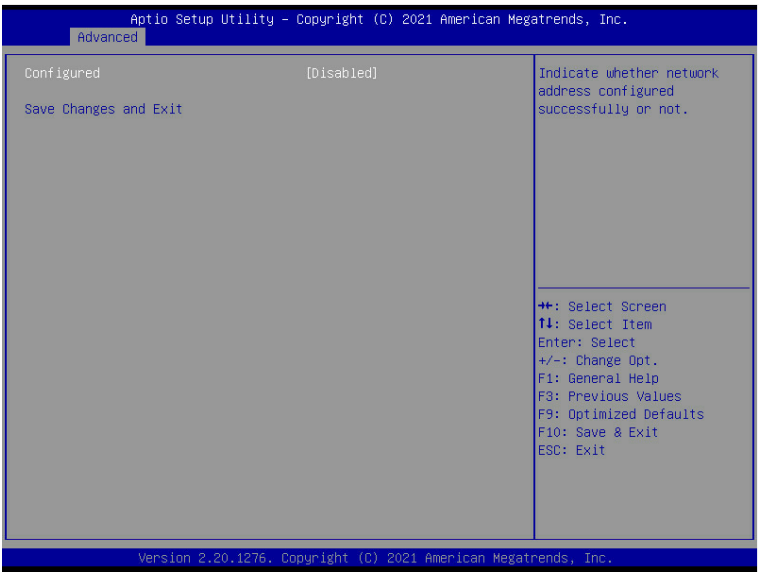
Parameter	Description
Link Status	Specifies the link status of the port.
MAC Address	Displays the MAC address of the Ethernet controller.
Virtual MAC Address	Displays the virtual MAC address of the Ethernet controller.

2-2-15 VLAN Configuration



Parameter	Description
Enter Configuration Menu	Press [Enter] to configure advanced items. ◆ Create new VLAN ◆ VLAN ID – Sets VLAN ID for a new VLAN or an existing VLAN. – Press the <+> / <-> keys to increase or decrease the desired values. – The valid range is from 0 to 4094. ◆ Priority – Sets 802.1Q Priority for a new VLAN or an existing VLAN. – Press the <+> / <-> keys to increase or decrease the desired values. – The valid range is from 0 to 7. ◆ Add VLAN – Press [Enter] to create a new VLAN or update an existing VLAN. ◆ Configured VLAN List ◆ Remove VLAN – Press [Enter] to remove an existing VLAN.

2-2-16 MAC IPv4 Network Configuration



Parameter	Description
Configured	Indicates whether network address is configured successfully or not. Options available: Enabled, Disabled. Default setting is **Disabled.**
Enable DHCP(Note)	Options available: Enabled, Disabled. Default setting is **Enabled.**
Local IP Address(Note)	Press [Enter] to configure local IP address.
Local NetMask(Note)	Press [Enter] to configure local NetMask.
Local Gateway(Note)	Press [Enter] to configure local Gateway
Local DNS Servers(Note)	Press [Enter] to configure local DNS servers
Save Changes and Exit	Press [Enter] to save all configurations.

(Note) This item appears when **Configured** is set to **Enabled**.

2-2-17 MAC IPv6 Network Configuration

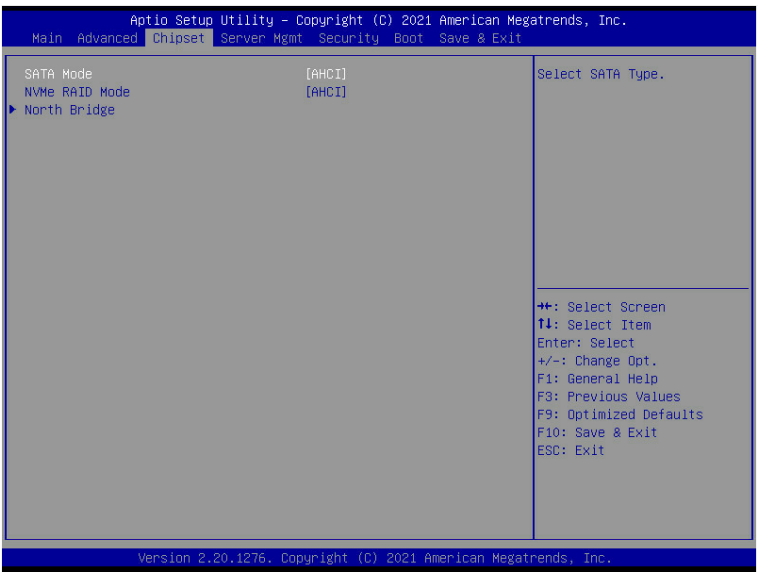


Parameter	Description
Enter Configuration Menu	Press [Enter] to configure advanced items. ◆ Displays the MAC Address information. ◆ Interface ID – The 64 bit alternative interface ID for the device. The string is colon separated. e.g. ff:dd:88:66:cc:1:2:3. ◆ DAD Transmit Count – The number of consecutive Neighbor solicitation messages sent while performing Duplicate Address Detection on a tentative address. A value of zero indicates that Duplicate Address Detection is not performed. ◆ Policy – Options available: automatic, manual. Default setting is **automatic**. ◆ Save Changes and Exit – Press [Enter] to save all configurations.

2-3 Chipset Setup Menu

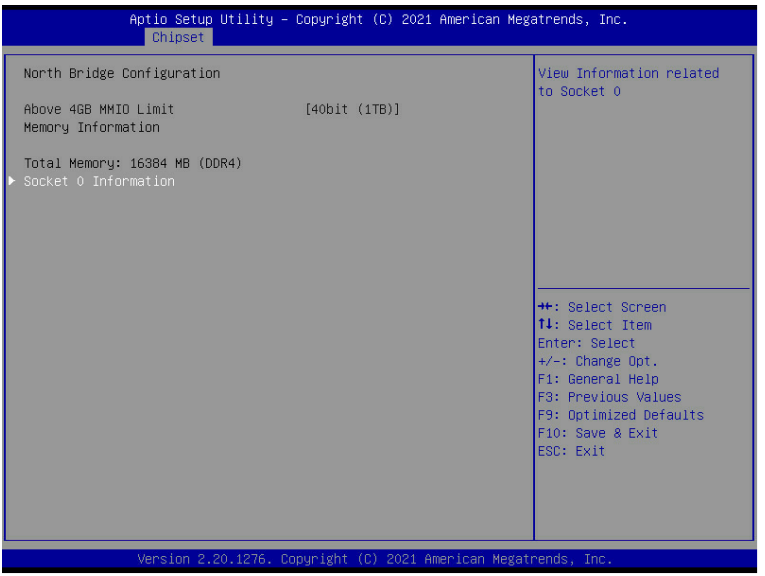
Chipset Setup menu displays submenu options for configuring the function of the South Bridge and North Bridge.

Select a submenu item, then press <Enter> to access the related submenu screen.



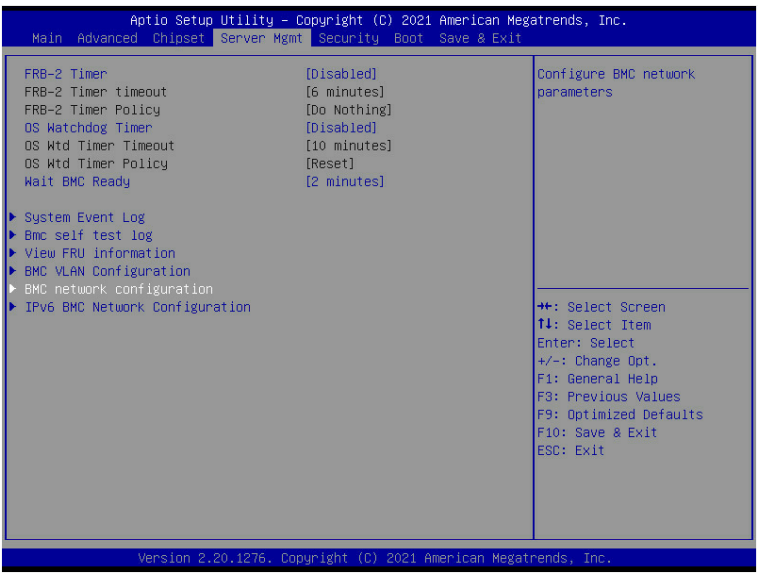
Parameter	Description
SATA Mode	Select SATA Type. Options available: AHCI, RAID. Default setting is **AHCI**.
NVMe RAID Mode	Options available: AHCI, RAID. Default setting is **AHCI**.
North Bridge	Press [Enter] for configuration of advanced items.

2-3-1 North Bridge



Parameter	Description
North Bridge Configuration	
Above 4GB MMIO Limit	Selects Above 4GB MMIO Limit to 38~43 bits limit. This option works only when "Above 4G decoding" is enabled. Options available: 40bit (1TB), 41bit (2TB), 42bit (4TB), 43bit (8TB). Default setting is **40bit (1TB)**.
Memory Information	
Total Memory	Displays the total memory information.
Socket 0 Information	Press [Enter] to view information related to socket 0.

2-4 Server Management Menu



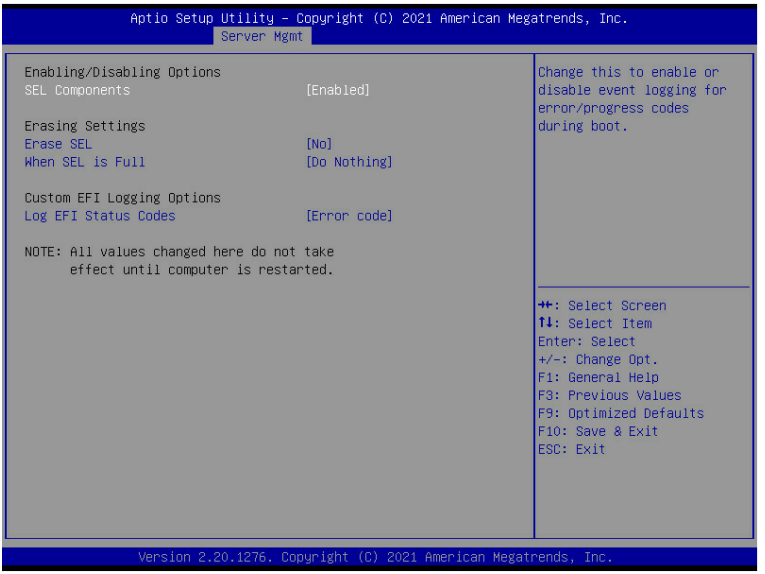
Parameter	Description
FRB-2 Timer	Enable/Disable FRB-2 timer (POST timer). Options available: Enabled, Disabled. Default setting is **Enabled**.
FRB-2 Timer (Note1) timeout	Configures the FRB2 Timer timeout. Options available: 3 minutes, 4 minutes, 5 minutes, 6 minutes. Default setting is **6 minutes**.
FRB-2 Timer Policy(Note1)	Configures the FRB2 Timer policy. Options available: Do Nothing, Reset, Power Down. Default setting is **Do Nothing**.
OS Watchdog Timer	Enable/Disable OS Watchdog Timer function. Options available: Enabled, Disabled. Default setting is **Disabled**.
OS Wtd Timer Timeout(Note2)	Configures OS Watchdog Timer. Options available: 5 minutes, 10 minutes, 15 minutes, 20 minutes. Default setting is **10 minutes**.
OS Wtd Timer Policy(Note2)	Configure OS Watchdog Timer Policy. Options available: Reset, Do Nothing, Power Down. Default setting is **Reset**.
Wait BMC Ready	Post wait BMC ready and reboot system. Options available: Disabled, 2 minutes, 4 minutes, 6 minutes. Default setting is **2 minutes**.
System Event Log	Press [Enter] to configure advanced items.

(Note1) This item is configurable when **FRB-2 Timer** is set to **Enabled**.

(Note2) This item is configurable when **OS Watchdog Timer** is set to **Enabled**.

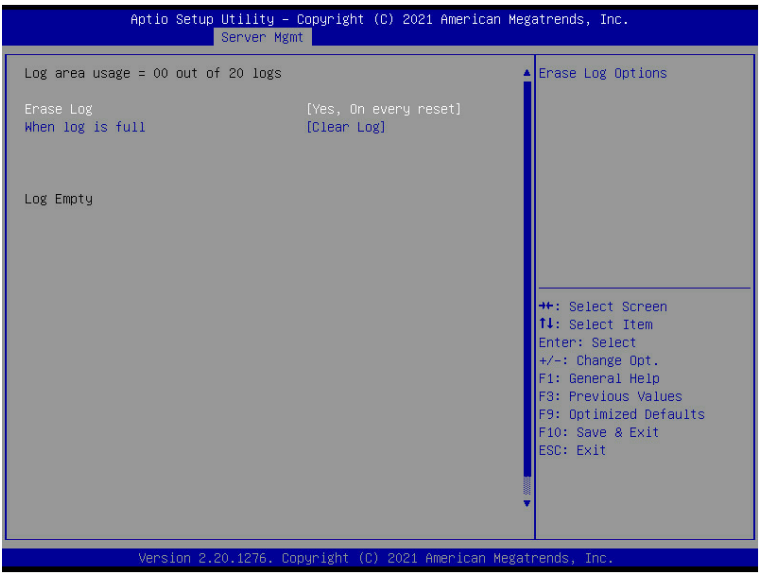
Parameter	Description
Bmc self test log	Press [Enter] to configure advanced items.
View FRU Information	Press [Enter] to view the FRU information.
BMC VLAN Configuration	Press [Enter] to configure advanced items.
BMC network configuration	Press [Enter] to configure advanced items.
IPv6 BMC Network Configuration	Press [Enter] to configure advanced items.

2-4-1 System Event Log



Parameter	Description
Enabling / Disabling Options	
SEL Components	Change this item to enable or disable all features of System Event Logging during boot. Options available: Enabled, Disabled. Default setting is **Enabled**.
Erasing Settings	
Erase SEL	Choose options for erasing SEL. Options available: No/Yes, On next reset/Yes, On every reset. Default setting is **No**.
When SEL is Full	Choose options for reactions to a full SEL. Options available: Do Nothing, Erase Immediately. Default setting is **Do Nothing**.
Custom EFI Logging Options	
Log EFI Status Codes	Enable/Disable the logging of EFI Status Codes (if not already converted to legacy). Options available: Disabled, Both, Error code, Progress code. Default setting is **Error code**.

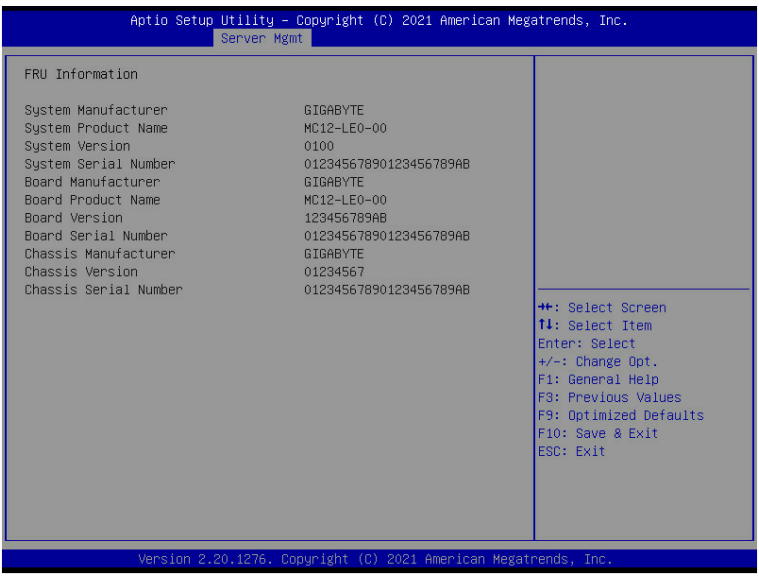
2-4-2 Bmc self test log



Parameter	Description
Log area usage = 00 out of 20 logs	
Erase Log	Choose options for erasing SEL. Options available: Yes, On every reset/ No. Default setting is **Yes, On every reset**.
When log is Full	Choose options for reactions to a full SEL. Options available: Clear Log, Do not log any more. Default setting is **Clear Log**.

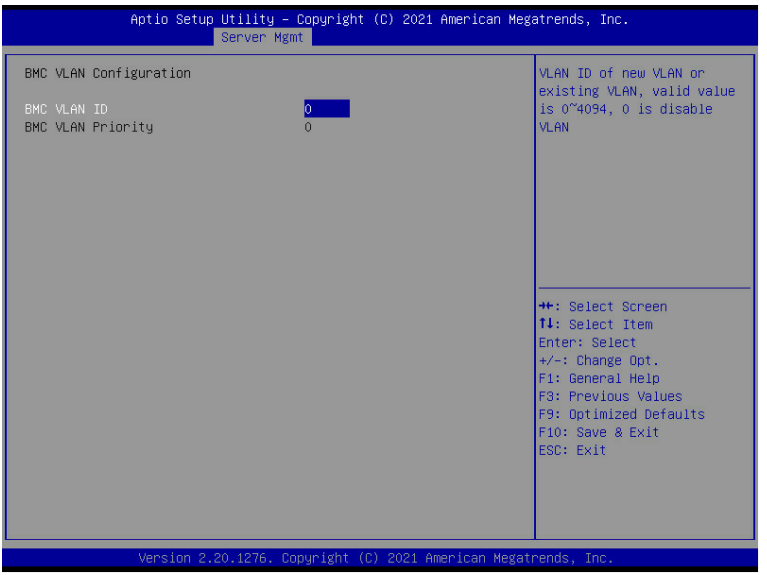
2-4-3 View FRU Information

The FRU page is a simple display page for basic system ID information, as well as System product information. Items on this window are non-configurable.



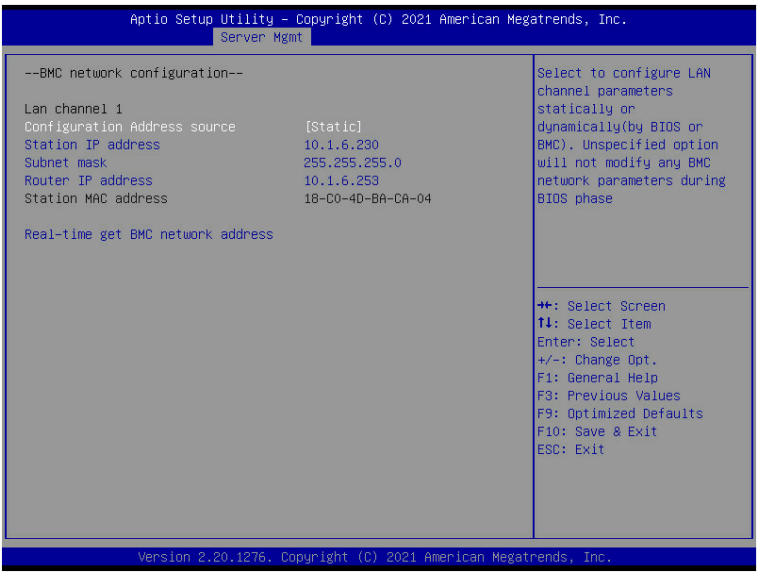
(Note) The model name will vary depends on the product you purchased

2-4-4 BMC VLAN Configuration



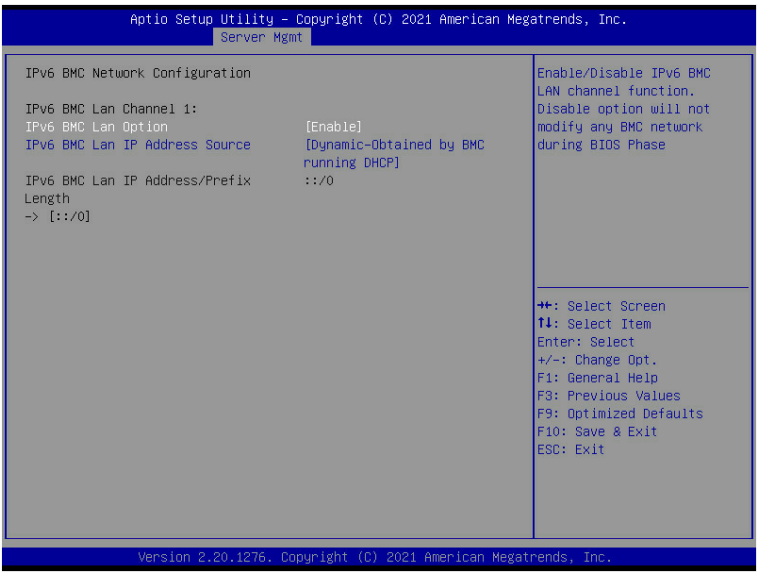
Parameter	Description
BMC VLAN Configuration	
BMC VLAN ID	Sets VLAN ID for a new VLAN or an existing VLAN. Press the <+> / <-> keys to increase or decrease the desired values. The valid range is from 0 to 4094.
BMC VLAN Priority	Sets 802.1Q Priority for a new VLAN or an existing VLAN. Press the <+> / <-> keys to increase or decrease the desired values. The valid range is from 0 to 7.

2-4-5 BMC Network Configuration



Parameter	Description
BMC network configuration	
Lan Channel 1	
Configuration Address source	Selects to configure LAN channel parameters statically or dynamically (DHCP). Do nothing option will not modify any BMC network parameters during BIOS phase. Options available: Unspecified, Static, DynamicBmcDhcp. Default setting is **Static**.
Station IP address	Displays IP Address information.
Subnet mask	Displays Subnet Mask information. Please note that the IP address must be in three digitals, for example, 192.168.000.001.
Router IP address	Displays the Router IP Address information.
Station MAC address	Displays the MAC Address information.
Real-time get BMC network address	Press [Enter] will set LAN mode and Address source and then get IP, Subnet, Gateway and MAC address.

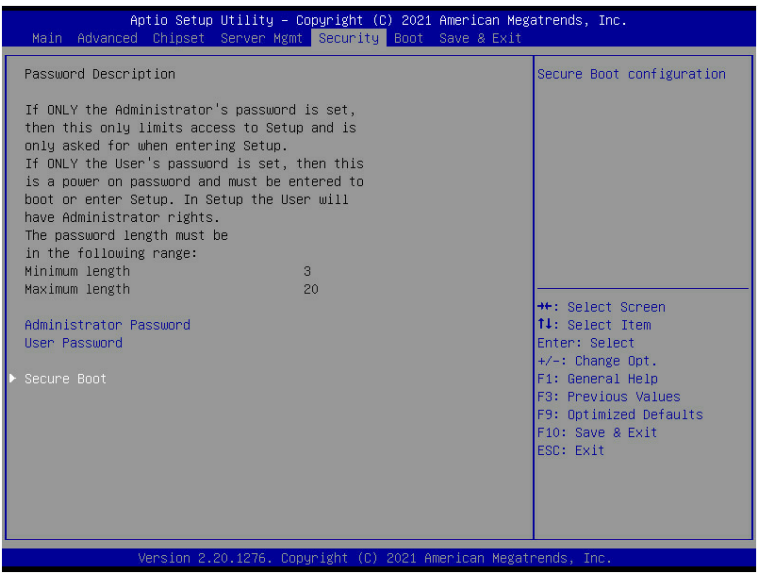
2-4-6 IPv6 BMC Network Configuration



Parameter	Description
IPv6 BMC network configuration	
IPv6 BMC Lan Channel 1	
IPv6 BMC Lan Option	Enable/Disable IPv6 BMC LAN channel function. When this item is disabled, the system will not modify any BMC network during BIOS phase. Options available: Unspecified, Disable, Enable. Default setting is **Enable**.
IPv6 BMC Lan IP Address Source	Selects to configure LAN channel parameters statically or dynamically (by BIOS or BMC). Options available: Unspecified, Static, Dynamic-Obtained by BMC running DHCP. Default setting is **Enable Dynamic-Obtained by BMC running DHCP**.
IPv6 BMC Lan IP Address/ Prefix Length	Check if the IPv6 BMC LAN IP address matches those displayed on the screen.

2-5 Security Menu

The Security menu allows you to safeguard and protect the system from unauthorized use by setting up access passwords.



There are two types of passwords that you can set:

- Administrator Password

  Entering this password will allow the user to access and change all settings in the Setup Utility.

- User Password

  Entering this password will restrict a user's access to the Setup menus. To enable or disable this field, a Administrator Password must first be set. A user can only access and modify the System Time, System Date, and Set User Password fields.

Parameter	Description
Administrator Password	Press [Enter] to configure the administrator password.
User Password	Press [Enter] to configure the user password.
Secure Boot	Press [Enter] to configure advanced items.

2-5-1 Secure Boot

The Secure Boot submenu is applicable when your device is installed the Windows® 8 (or above) operating system.



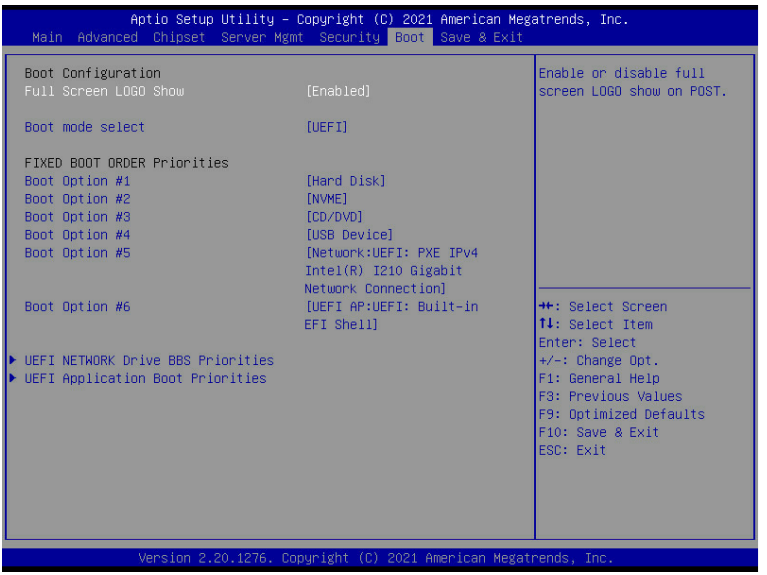
Parameter	Description
System Mode	Displays if the system is in User mode or Setup mode.
Secure Boot	Enable/ Disable the Secure Boot function. Options available: Enabled, Disabled. Default setting is **Disabled**.
Secure Boot Mode(Note)	Secure Boot requires all the applications that are running during the booting process to be pre-signed with valid digital certificates. This way, the system knows all files being loaded before Windows loads to the login screen have not been tampered with. When set to Standard, it will automatically load the Secure Boot keys form the BIOS databases. When set to Custom, you can customize the Secure Boot settings and manually load its keys from the BIOS database. Options available: Standard, Custom. Default setting is **Standard**.
Restore Factory Keys	Forces the system to user mode and installs factory default Secure Boot key database.
Reset to Setup Mode	Press [Enter] to reset the system to Setup mode.

(Note) Advanced items prompt when this item is set to **Custom**.

Parameter	Description
Key Management	Press [Enter] to configure advanced items. **Please note that this item is configurable when Secure Boot Mode is set to Custom.** ◆ Factory Key Provision – Allows to provision factory default Secure Boot keys when system is in Setup Mode. – Options available: Enabled, Disabled. Default setting is **Disabled**. ◆ Restore Factory Keys – Installs all factory default keys. It will force the system in User Mode. – Options available: Yes, No. ◆ Enroll Efi Image – Press [Enter] to enroll SHA256 hash of the binary into Authorized Signature Database (db). ◆ Restore DB defaults – Restore DB variable to factory defaults. ◆ Secure Boot variable – Displays the current status of the variables used for secure boot. ◆ Platform Key (PK) – Displays the current status of the Platform Key (PK). – Press [Enter] to configure a new PK. – Options available: Update. ◆ Key Exchange Keys (KEK) – Displays the current status of the Key Exchange Key Database (KEK). – Press [Enter] to configure a new KEK or load additional KEK from storage devices. – Options available: Update, Append. ◆ Authorized Signatures (DB) – Displays the current status of the Authorized Signature Database. – Press [Enter] to configure a new DB or load additional DB from storage devices. – Options available: Update, Append. ◆ Forbidden Signatures (DBX) – Displays the current status of the Forbidden Signature Database. – Press [Enter] to configure a new dbx or load additional dbx from storage devices. – Options available: Update, Append. ◆ Authorized TimeStamps (DBT) – Displays the current status of the Authorized TimeStamps Database. – Press [Enter] to configure a new DBT or load additional DBT from storage devices. – Options available: Update, Append. ◆ OsRecovery Signatures – Displays the current status of the OsRecovery Signature Database. – Press [Enter] to configure a new OsRecovery Signature or load additional OsRecovery Signature from storage devices. – Options available: Update, Append.

2-6 Boot Menu

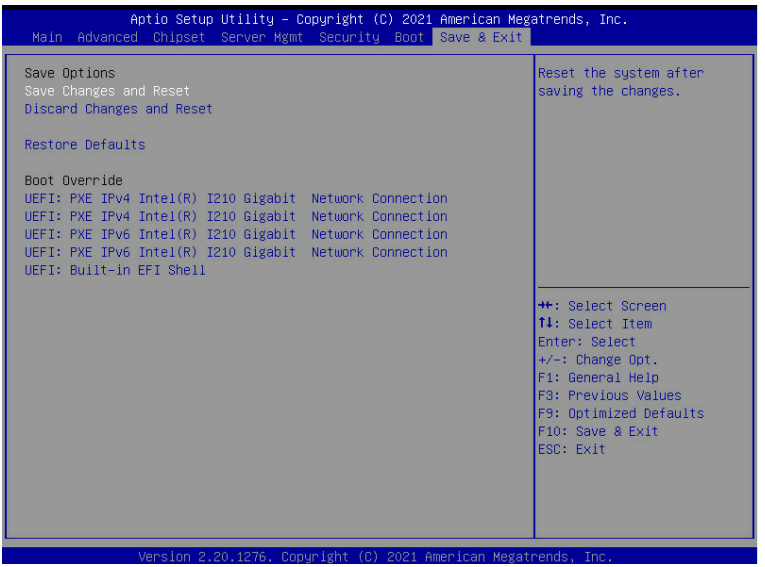
The Boot menu allows you to set the drive priority during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.



Parameter	Description
Boot Configuration	
Full Screen LOGO Show	Enable/Disable showing the logo during POST. Options available: Enabled, Disabled. Default setting is **Enabled**.
Boot mode select	Selects the boot mode. Options available: LEGACY, UEFI. Default setting is **UEFI**.
FIXED BOOT ORDER Priorities	
Boot Option #	Press [Enter] to configure the boot priority.
UEFI Network Drive BBS Priorities	Press [Enter] to configure the boot priority.
UEFI Application Boot Priorities	Press [Enter] to configure the boot priority.

2-7 Save & Exit Menu

The Save & Exit menu displays the various options to quit from the BIOS setup. Highlight any of the exit options then press <Enter>.



Parameter	Description
Save Options	
Save Changes and Exit	Saves changes made and closes the BIOS setup. Options available: Yes, No.
Discard Changes and Exit	Discards changes made and exits the BIOS setup. Options available: Yes, No.
Save Changes	Saves changes done so far to any of the setup options. Options available: Yes, No.
Default Options	
Restore Defaults	Loads the default settings for all BIOS setup parameters. Setup Defaults are quite demanding in terms of resources consumption. If you are using low-speed memory chips or other kinds of low-performance components and you choose to load these settings, the system might not function properly. Options available: Yes, No.
Boot Override	Press [Enter] to configure the device as the boot-up drive.

2-8 BIOS POST Beep code (AMI standard)

2-8-1 PEI Beep Codes

# of Beeps	Description
1	Memory not Installed.
1	Memory was installed twice (InstallPeiMemory routine in PEI Core called twice)
2	Recovery started
3	DXEIPL was not found
3	DXE Core Firmware Volume was not found
4	Recovery failed
4	S3 Resume failed
7	Reset PPI is not available

2-8-2 DXE Beep Codes

# of Beeps	Description
1	Invalid password
4	Some of the Architectural Protocols are not available
5	No Console Output Devices are found
5	No Console Input Devices are found
6	Flash update is failed
7	Reset protocol is not available
8	Platform PCI resource requirements cannot be met