

QSFP-100G-PSM4-2

100GBase-PSM4 QSFP28 Transceiver

Hot Pluggable, +3.3V, MPO-12, 1310nm, 2km, SMF, Commercial Temperature

FEATURES

- MPO12 optical interface
- Maximum link length up to 2km on Single Mode Fibre
- Up to 25.78125Gb/s data links per lane
- +3.3V power supply
- QSFP MSA compliant package
- Hot Pluggable
- High performance signal mode DML transmitter
- High sensitivity PIN/TIA optical receiver
- BER < 5E-5@-10.5dBm (OMA)
- Built-in CDR
- Data and Control Interfaces
- Tx Data CML/AC Coupled
- Rx Data CML/AC Coupled
- ModSelL LVTTTL
- ResetL LVTTTL
- ModPrsL LVTTTL
- LPMode LVTTTL
- 2-wire I2C communication bus
- Commercial Operating Temperature Range: 0 to 70°C
- RoHS 6 compliance

APPLICATIONS

- 100G BASE Ethernet
- InfiniBand EDR interconnects
- Enterprise networking

DESCRIPTION

ATGBICS QSFP-100G-PSM4-2 QSFP28 optical transceiver has a reach of up to 2km with a four-lane 25.78125G data rate. It is based on a 3.3V DC power supply and operates in the commercial temperature range. It is compliant with QSFP MSA, SFF-8436, SFF-8636 and PSM4 MSA. Digital diagnostic functions are available via I2C interface and the control functions can be achieved by LVTTTL interfaces on the host mainly including Module Select (ModSelL) Module Reset (ResetL) and Low Power Mode (LPMode). The transceiver incorporates a four-laser array which is usually a DFB four-PIN diode array, a high-performance CDR integrated four drivers and TIAs IC separately. The differential AC coupled Tx and Rx data interfaces are CML compatible.

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
Storage Temperature	T_{stg}	-40	-	+95	°C	
Case Operating Temperature	T_O	0	-	+70	°C	
Relative Humidity - Storage	R_{HS}	0	-	95	%	
Relative Humidity - Operating	R_{HO}	0	-	85	%	
Supply Voltage	VCC	-0.3	-	3.6	V	

RECOMMENDED OPERATING ENVIRONMENT

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
Case Operating Temperature	T_{case}	0	-	+70	°C	
DC Supply Voltage	VCC	3.135	-	3.465	V	
Module Supply Current	I_{IN}	-	-	1060	mA	

ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Min	Typical	Max	Unit	Note
TRANSMITTER						
Differential Data input Swing	V_{IN}	180	-	900	mV	
Tx Differential Input Impedence	Z_{in}	90	100	110	Ω	
Tx Differential Output Impedence	Z_{out}	45	50	55	Ω	
ResetL Disable Voltage	V_r	2.0	-	Vcc+0.3	V	
ResetL Enable Voltage	V_{rEN}	0	-	0.8	V	
ModSelL Disable Voltage	V_m	2.0	-	Vcc+0.3	V	
ModSelL Enable Voltage	V_{mEN}	0	-	0.8	V	
RECEIVER						
Differential Data Output Swing	V_{OUT}	180	-	900	mV	
Rx Differential Output Impedence	Z_{OUT}	90	100	110	Ω	
IntL Assert Voltage	V_{Int}	Vcc-0.5	-	Vcc+0.3	V	
IntL De-assert Voltage	VD_{Int}	0	-	+0.4	V	

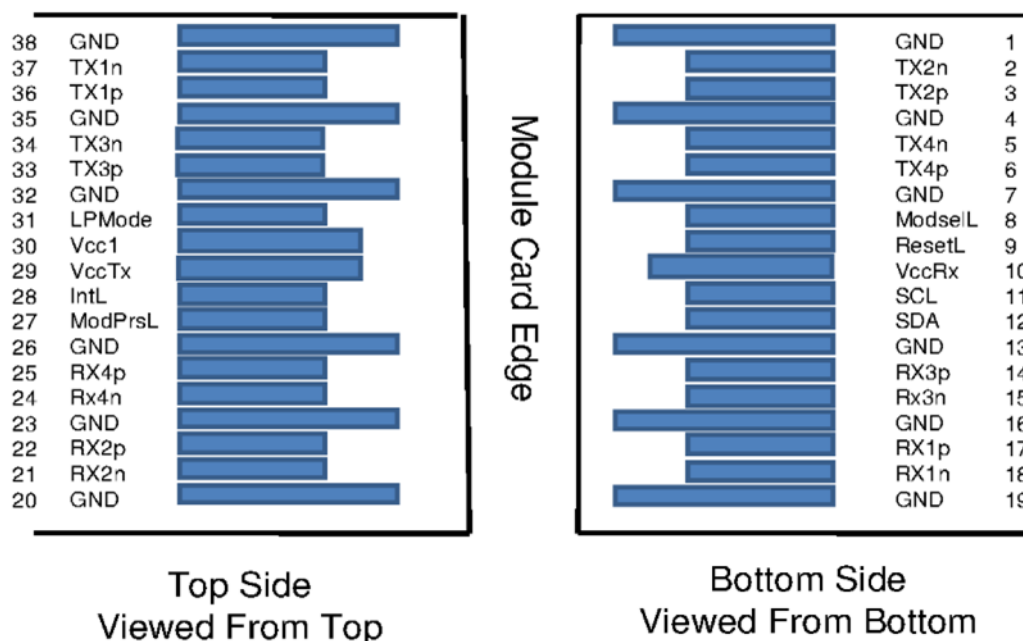
OPTICAL PARAMETERS

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
TRANSMITTER						
Signalling Rate per Lane	-	25.78125±100ppm			Gbps	
Lane Wavelength	Λ	1295	1310	1325	nm	
Side Mode Suppression Ratio	SMSR	30	-	-	dB	
Total Average Launch Power	P _{tol}	-	-	8.5	dBm	
Average Launch Power Each Lane	P _{avg}	-3.5	-	2.5	dBm	
Optical Modulation Amplitude per Lane	OMA	-	-	2.5	dBm	
Transmitter and Dispersion Penalty per Lane	TDP	-	-	2.2	dB	
Eye Mask coordinates: {X1, X2, X3, Y1, Y2, Y3}	-	{0.31, 0.4, 0.45, 0.34, 0.38, 0.4}				1
Average Launch Power of OFF Transmitter per Lane	-	-	-	-30	dBm	
Extinction Ratio	ER	3.5	-	-	dB	
Spectral Width (20dB)	-	-	-	1	nm	
Transmitter Reflectance	-	-	-	-12	dB	
Optical return loss tolerance	-	-	-	20	dB	
RECEIVER						
Signalling Rate per Lane	-	25.78125±100ppm			Gbps	
Lane Wavelength	Λ	1295	1310	1325	nm	
Damage Threshold per Lane	THd	-	-	3.0	dBm	2
Average Receive Power per Lane	-	-10.6		2.5	dBm	3
Sensitivity OMA per Lane	Sen	-	-	-10.5	dBm	3
Receiver Reflectance	-	-	-	-26	dB	
LOS Assert	LOSA	-24	-	-		
LOS Deassert	LOSD	-	-	-11.6		
LOS Hysteresis	LOSH	0.5	-	6		

Note:

1. Hit ratio of 5e-5, per IEEE.
2. The receiver shall be able to tolerate, without damage, continuous exposure to a modulated optical input signal having this power level on one lane. The receiver does not have to operate correctly at this input power.
3. Measured with conformance test signal at receiver input for BER = 5e-5 BER

PIN DESCRIPTION



PIN DESCRIPTION

Pin	Logic	Symbol	Name/Description	Note
1		GND	Ground	
2	CML-I	Tx2n	Transmitter Inverted Data Input	
3	CML-I	Tx2p	Transmitter Non-Inverted Data output	
4		GND	Ground	
5	CML-I	Tx4n	Transmitter Inverted Data Input	
6	CML-I	Tx4p	Transmitter Non-Inverted Data output	
7		GND	Ground	
8	LVTTL-I	ModSelL	Module Select	
9	LVTTL-I	ResetL	Module Reset	
10		VccRx	+ 3.3V Power Supply Receiver	
11	LVC MOS-I/O	SCL	2-Wire Serial Interface Clock	
12	LVC MOS-I/O	SDA	2-Wire Serial Interface Data	
13		GND	Ground	
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	
15	CML-O	Rx3n	Receiver Inverted Data Output	
16		GND	Ground	
17	CML-O	Rx1p	Receiver Non-Inverted Data Output	
18	CML-O	Rx1n	Receiver Inverted Data Output	
19		GND	Ground	
20		GND	Ground	
21	CML-O	Rx2n	Receiver Inverted Data Output	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	
23		GND	Ground	
24	CML-O	Rx4n	Receiver Inverted Data Output	
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	
26		GND	Ground	
27	LVTTL-O	ModPrsL	Module Present	
28	LVTTL-O	IntL	Interrupt	
29		VccTx	+3.3 V Power Supply transmitter	
30		Vcc1	+3.3 V Power Supply	

CONTINUED

31	LVTTL-I	LPMode	Low Power Mode	
32		GND	Ground	
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input	
34	CML-I	Tx3n	Transmitter Inverted Data Output	
35		GND	Ground	
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	
37	CML-I	Tx1n	Transmitter Inverted Data Output	
38		GND	Ground	

DIGITAL DIAGNOSTIC MONITOR ACCURACY

The following characteristics are defined over recommended operating conditions

Parameter	Accuracy	Unit
Internally measured transceiver temperature	+/-3	°C
Internally measured transceiver supply voltage	+/-3	%
Measured Tx bias current	+/-10	%
Measured Tx output power	+/-3	dB
Measured Rx received average optical power	+/-3	dB

MECHANICAL DIMENSIONS (UNIT: mm)

